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ON Semiconductor®

### **FGB3040CS**

### EcoSPARK¤ 300mJ, 400V, N-Channel Current Sensing Ignition IGBT

### **General Description**

The FGB3040CS is an Ignition IGBT that offers outstanding SCIS capability along with a ratiometric emitter current sensing capability. This sensing is based on a emitter active area ratio of 200:1. The output is provided through a fourth (sense) lead. This signal provides a current level that is proportional to the main collector to emitter current. The effective ratio as measured on the sense lead is a function of the sense output, the collector current and the gate to emitter drive voltage.

### **Applications**

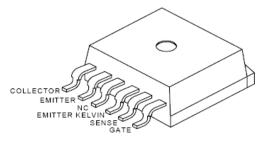
- Smart Automotive Ignition Coil Driver Circuits
- ECU Based Systems
- Distributorless Based Systems
- Coil on Plug Based Systems

#### **Features**

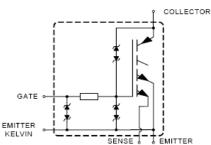
- SCIS Energy = 300mJ at T<sub>.1</sub> = 25°C
- Logic Level Gate Drive
- Qualified to AEC Q101
- RoHS Compliant



### **Package**



## **Symbol**



## **Device Maximum Ratings** $T_A = 25$ °C unless otherwise noted

Symbol	Parameter	Ratings	Units
BV <sub>CER</sub>	Collector to Emitter Breakdown Voltage (I <sub>C</sub> = 2mA)	430	V
BV <sub>ECS</sub>	Emitter to Collector Breakdown Voltage (I <sub>C</sub> = 1mA) (Reverse Battery Condition)	24	V
E <sub>SCIS25</sub>	Self Clamping Inductive Switching Energy (at starting T <sub>J</sub> = 25°C)	300	mJ
E <sub>SCIS150</sub>	Self Clamping Inductive Switching Energy (at starting T <sub>J</sub> = 150°C)	170	mJ
I <sub>C25</sub>	Continuous Collector Current, at V <sub>GE</sub> = 4.0V, T <sub>C</sub> = 25°C	21	Α
I <sub>C110</sub>	Continuous Collector Current, at V <sub>GE</sub> = 4.0V, T <sub>C</sub> = 110°C	19	Α
$V_{GEM}$	Maximum Continuous Gate to Emitter Voltage	±10	V
D	Power Dissipation, at T <sub>C</sub> = 25°C	150	W
$P_{D}$	Power Dissipation Derating, for T <sub>C</sub> > 25°C	1	W/°C
$T_{J}$	Operating Junction Temperature Range	-40 to 175	°C
T <sub>STG</sub>	Storage Junction Temperature Range	-40 to 175	°C
$T_L$	Max. Lead Temp. for Soldering (at 1.6mm from case for 10sec)	300	°C
T <sub>PKG</sub>	Max. Package Temp. for Soldering (Package Body for 10 sec)	260	°C
ESD	Electrostatic Discharge Voltage, HBM model (100pfd, 1500 ohms)	4	kV

## Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
3040CS	FGB3040CS	TO-263 6 Lead	300mm	24mm	800
3040CS	FGB3040CS	TO-263 6 Lead	Tube	N/A	50

# **Electrical Characteristics** T<sub>A</sub> = 25°C unless otherwise noted

	Symbol	Parameter	Test Conditions	Min	Тур	Max	Units	
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#### **Off State Characteristics**

BV <sub>CER</sub>	Collector to Emitter Breakdown Voltage	$I_{CE}$ = 2mA, $V_{GE}$ = 0, $R_{GE}$ = 1KΩ, See Fig. 17 $T_{J}$ = -40 to 150°C		370	410	430	V
BV <sub>CES</sub>	Collector to Emitter Breakdown Voltage	$T_J = -40 \text{ to } 150^{\circ}\text{C}$		390	430	450	V
BV <sub>ECS</sub>	Emitter to Collector Breakdown Voltage	$I_{CE}$ = -75mA, $V_{GE}$ = 0V, $T_{C}$ = 25°C		30	-	1	V
$BV_{GES}$	Gate to Emitter Breakdown Voltage	I <sub>GES</sub> = ±2mA		±12	±14	-	V
$I_{GEO}$	Gate to Emitter Leakage Current	V <sub>GE</sub> = ±10V		-	-	±9	μА
	Collector to Emitter Leakage Current	V <sub>CES</sub> = 250V,	$T_{\rm C} = 25^{\rm o}{\rm C}$	-	-	25	μА
ICES	Collector to Emitter Leakage Current	See Fig. 13	$T_{\rm C} = 150^{\rm o}{\rm C}$	-	-	1	mA
	Emittanta Callactan Laglaga Comment	V <sub>EC</sub> = 24V,	T <sub>C</sub> = 25°C	-	-	1	A
IECS		See Fig. 13	T <sub>C</sub> = 150°C	-	-	40	mA
R <sub>1</sub>	Series Gate Resistance			ı	100	1	Ω

#### **On State Characteristics**

V <sub>CE(SAT)</sub>	Collector to Emitter Saturation Voltage	I <sub>CE</sub> = 6A, V <sub>GE</sub> = 4V	T <sub>C</sub> = 25°C See Fig. 5	-	1.3	1.6	٧
V <sub>CE(SAT)</sub>	Collector to Emitter Saturation Voltage	I <sub>CE</sub> = 10A, V <sub>GE</sub> = 4.5V	T <sub>C</sub> = 150°C See Fig. 6	1	1.6	1.85	٧
V <sub>CE(SAT)</sub>	Collector to Emitter Saturation Voltage	I <sub>CE</sub> = 15A, V <sub>GE</sub> = 4.5V	$T_{\rm C} = 150^{\rm o}{\rm C}$	-	1.8	2.35	٧
I <sub>CE(ON)</sub>	Collector to Emitter On State Current	V <sub>CE</sub> = 5V, V <sub>GE</sub> = 5V		-	37	-	Α

#### **Dynamic Characteristics**

$Q_{G(ON)}$	Gate Charge	I <sub>CE</sub> = 10A, V <sub>CE</sub> = 12V, V <sub>GE</sub> = 5V, See Fig. 16		-	15	-	nC
V	Gate to Emitter Threshold Voltage	I <sub>CE</sub> = 1mA, V <sub>CE</sub> = V <sub>GE</sub>	$T_{\rm C} = 25^{\rm o}{\rm C}$	1.3	1.6	2.2	V
$V_{GE(TH)}$	Gate to Emitter Threshold Voltage	See Fig. 12	$T_{\rm C} = 150^{\rm o}{\rm C}$	0.75	1.1	1.8	V
$V_{GEP}$	Gate to Emitter Plateau Voltage	I <sub>CE</sub> = 10A, V <sub>CE</sub> = 12V		-	3.0	-	V
$\beta_{AREA}$	Emitter Sense Area Ratio	Sense Area/Total Area		-	1/200	-	-
$\beta_{5\Omega}$	Emitter Current Sense Ratio	$I_{CE}$ = 8.0A, $V_{GE}$ = 5V, $R_{SEI}$	NSE = 5 Ω	-	230	-	-
$\beta_{20\Omega}$	Emitter Current Sense Ratio	$I_{CE}$ = 9.0A, $V_{GE}$ = 5V, $R_{SE}$	NSE = 20 Ω	550	640	765	-

## **Switching Characteristics**

$t_{d(ON)R}$	Current Turn-On Delay Time-Resistive	VE . E	-	0.6	4	μS
t <sub>rR</sub>	Current Rise Time-Resistive	$V_{GE}$ = 5V, $R_G$ = 1K $\Omega$ T <sub>J</sub> = 25°C, See Fig. 14	-	1.5	7	μS
t <sub>d(OFF)L</sub>	Current Turn-Off Delay Time-Inductive			4.7	15	μS
$t_{fL}$	Current Fall Time-Inductive	$V_{GE}$ = 5V, $R_G$ = 1K $\Omega$ T <sub>J</sub> = 25°C, See Fig. 14	-	2.6	15	μS
SCIS	Self Clamped inductive Switching	$T_J$ = 25°C, L = 3.0mHy, $I_{CE}$ = 14.2A, $R_G$ = 1k $\Omega$ , $V_{GE}$ = 5V, See Fig. 3&4	1	ı	300	mJ

### **Thermal Characteristics**

$R_{ m  heta JC}$   Thermal Resistance Junction to Case   All Packages   -   -   1.0   $^{ m oC}/$	$R_{\theta JC}$	Thermal Resistance Junction to Case	All Packages	-	-	1.0	°C/W
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### **Typical Performance Curves**

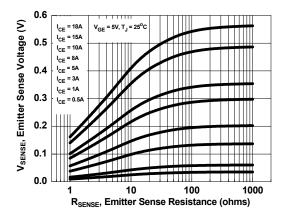


Figure 1. Emitter Sense Voltage vs. Emitter Sense Resistance

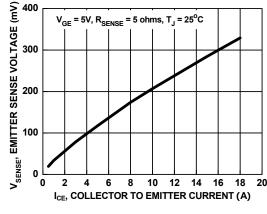


Figure 2. Emitter Sense Voltage vs. Collector to Emitter Current

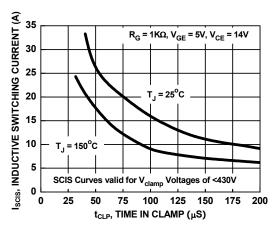


Figure 3. Self Clamped Inductive Switching Current vs. Time in Clamp

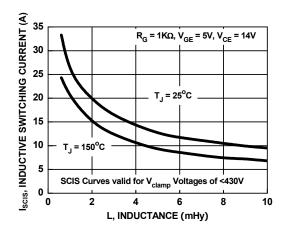


Figure 4. Self Clamped Inductive Switching Current vs. Inductance

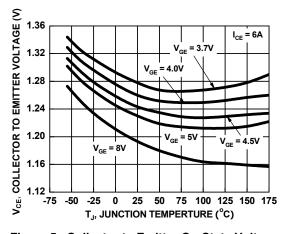


Figure 5. Collector to Emitter On-State Voltage vs. Junction Temperature

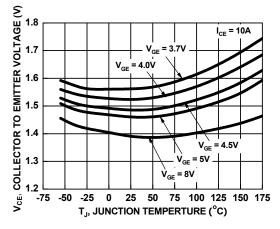


Figure 6. Collector to Emitter On-State Voltage vs. Junction Temperature

#### **Typical Performance Curves** (Continued)

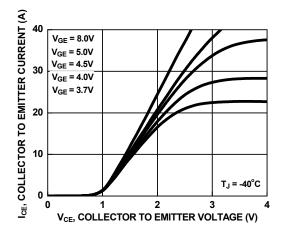


Figure 7. Collector to Emitter On-State Voltage vs. Collector Current

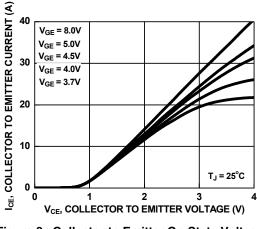


Figure 8. Collector to Emitter On-State Voltage vs. Collector Current

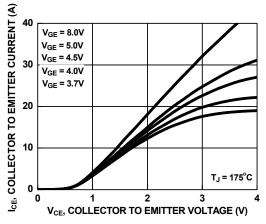


Figure 9. Collector to Emitter On-State Voltage vs. Collector Current

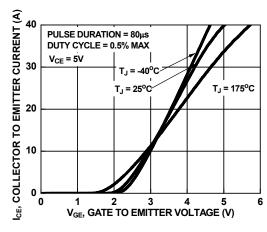


Figure 10. Transfer Characteristics

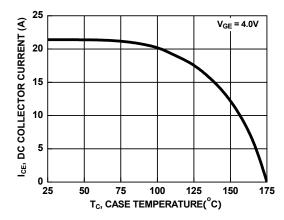


Figure 11. DC Collector Current vs. Case Temperature

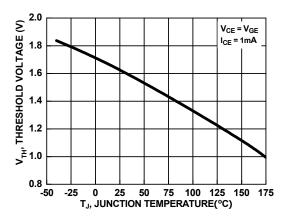


Figure 12. Threshold Voltage vs. Junction Temperature

### **Typical Performance Curves** (Continued)

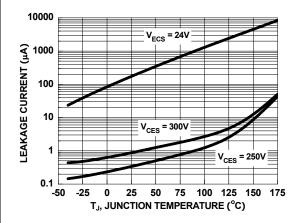


Figure 13. Leakage Current vs. Junction Temperature

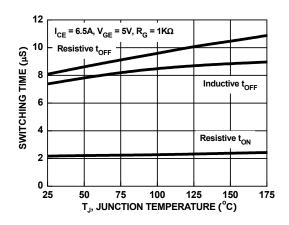


Figure 14. Switching Time vs. Junction Temperature

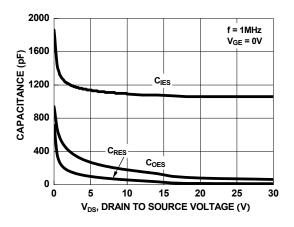


Figure 15. Capacitance vs. Collector to Emitter Voltage

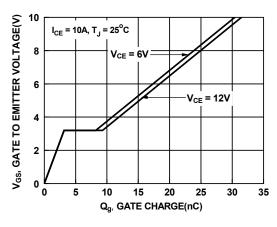


Figure 16. Gate Charge

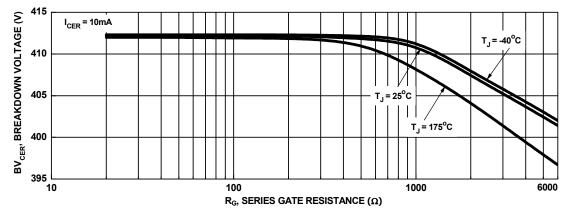


Figure 17. Break 8 own Voltage vs. Series Gate Resistance

# **Typical Performance Curves**

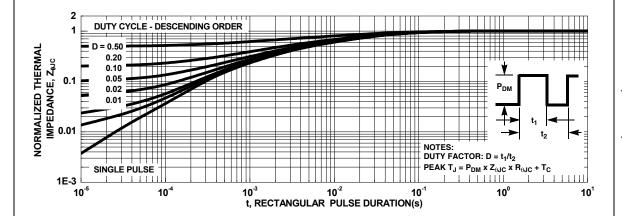


Figure 18. IGBT Normalized Transient Thermal Impedance, Junction to Case

### **Test Circuit and Waveforms**

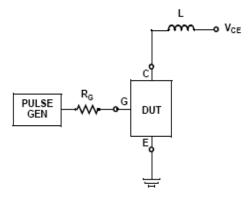


Figure 19. Inductive Switching Test Circuit

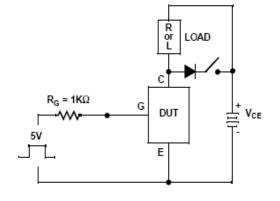


Figure 20.  $t_{ON}$  and  $t_{OFF}$  Switching Test Circuit

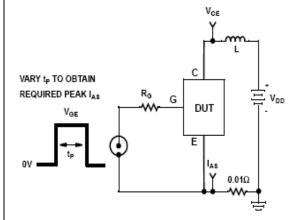


Figure 21. Energy Test Circuit

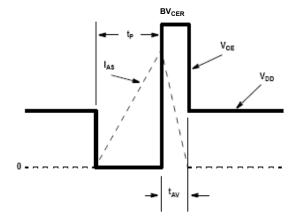
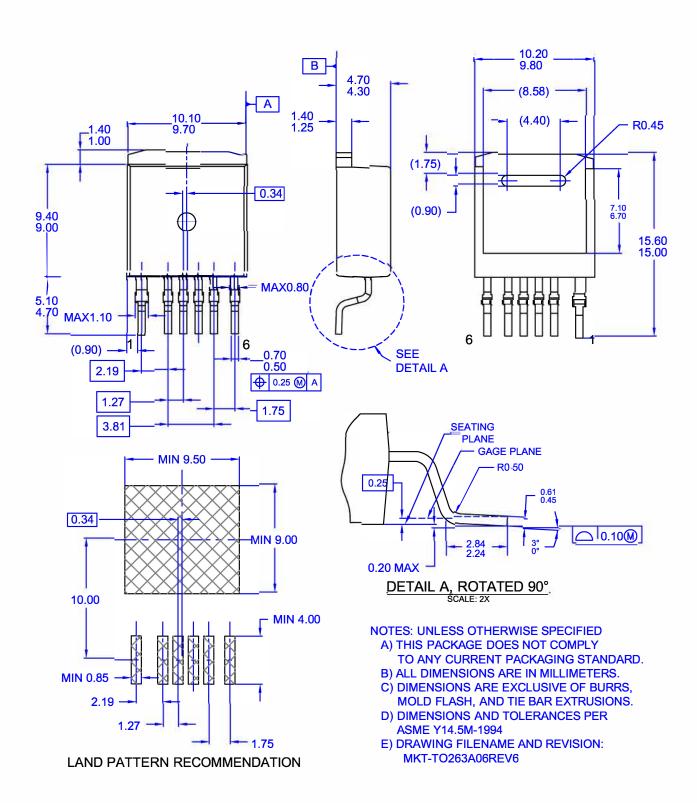


Figure 22. Energy Waveforms



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