

MMFT960T1

Preferred Device

Power MOSFET 300 mA, 60 Volts

N-Channel SOT-223

This Power MOSFET is designed for high speed, low loss power switching applications such as switching regulators, dc-dc converters, solenoid and relay drivers. The device is housed in the SOT-223 package which is designed for medium power surface mount applications.

Features

- Silicon Gate for Fast Switching Speeds
- Low Drive Requirement
- The SOT-223 Package can be Soldered Using Wave or Reflow
- The Formed Leads Absorb Thermal Stress During Soldering Eliminating the Possibility of Damage to the Die
- Pb-Free Package is Available

MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DS}	60	V
Gate-to-Source Voltage – Non-Repetitive	V_{GS}	± 30	V
Drain Current	I_D	300	mAdc
Total Power Dissipation @ $T_A = 25^\circ\text{C}$ (Note 1) Derate above 25°C	P_D	0.8 6.4	W mW/ $^\circ\text{C}$
Operating and Storage Temperature Range	T_J, T_{stg}	-65 to 150	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	156	$^\circ\text{C/W}$
Maximum Temperature for Soldering Purposes Time in Solder Bath	T_L	260 10	$^\circ\text{C}$ S

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. Device mounted on a FR-4 glass epoxy printed circuit board using minimum recommended footprint.

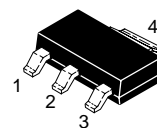
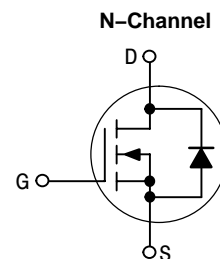


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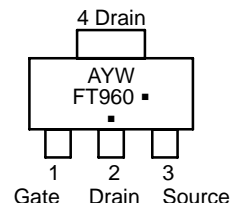
300 mA, 60 VOLTS

$R_{DS(on)} = 1.7 \Omega$



TO-261AA
CASE 318E
STYLE 3

MARKING DIAGRAM AND PIN ASSIGNMENT



A = Assembly Location
Y = Year
W = Work Week
▪ = Pb-Free Package
FT960 = Device Code
(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping†
MMFT960T1	SOT-223	1000 Tape & Reel
MMFT960T1G	SOT-223 (Pb-Free)	1000 Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

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ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Drain-to-Source Breakdown Voltage ($V_{GS} = 0$, $I_D = 10\ \mu\text{A}$)	$V_{(BR)DSS}$	60	–	–	Vdc
Zero Gate Voltage Drain Current ($V_{DS} = 60\ \text{V}$, $V_{GS} = 0$)	I_{DSS}	–	–	10	μA_{dc}
Gate-Body Leakage Current ($V_{GS} = 15\ \text{Vdc}$, $V_{DS} = 0$)	I_{GSS}	–	–	50	nA_{dc}

ON CHARACTERISTICS (Note 2)

Gate Threshold Voltage ($V_{DS} = V_{GS}$, $I_D = 1.0\ \text{mA}_{dc}$)	$V_{GS(th)}$	1.0	–	3.5	Vdc
Static Drain-to-Source On-Resistance ($V_{GS} = 10\ \text{Vdc}$, $I_D = 1.0\ \text{A}$)	$R_{DS(on)}$	–	–	1.7	Ω
Drain-to-Source On-Voltage ($V_{GS} = 10\ \text{V}$, $I_D = 0.5\ \text{A}$) ($V_{GS} = 10\ \text{V}$, $I_D = 1.0\ \text{A}$)	$V_{DS(on)}$	– –	– –	0.8 1.7	Vdc
Forward Transconductance ($V_{DS} = 25\ \text{V}$, $I_D = 0.5\ \text{A}$)	g_{fs}	–	600	–	mmhos

DYNAMIC CHARACTERISTICS

Input Capacitance	$(V_{DS} = 25\ \text{V}, V_{GS} = 0, f = 1.0\ \text{MHz})$	C_{iss}	–	65	–	pF
Output Capacitance		C_{oss}	–	33	–	
Transfer Capacitance		C_{rss}	–	7.0	–	
Total Gate Charge	$(V_{GS} = 10\ \text{V}, I_D = 1.0\ \text{A}, V_{DS} = 48\ \text{V})$	Q_g	–	3.2	–	nC
Gate-Source Charge		Q_{gs}	–	1.2	–	
Gate-Drain Charge		Q_{gd}	–	2.0	–	

2. Pulse Test: Pulse Width $\leq 300\ \mu\text{s}$, Duty Cycle $\leq 2.0\%$.

TYPICAL ELECTRICAL CHARACTERISTICS

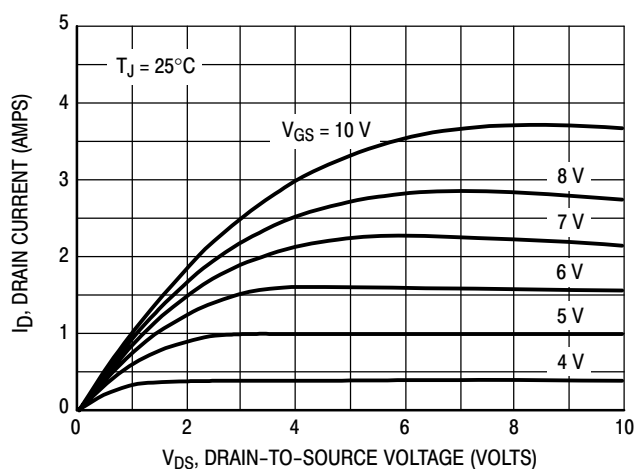


Figure 1. On-Region Characteristics

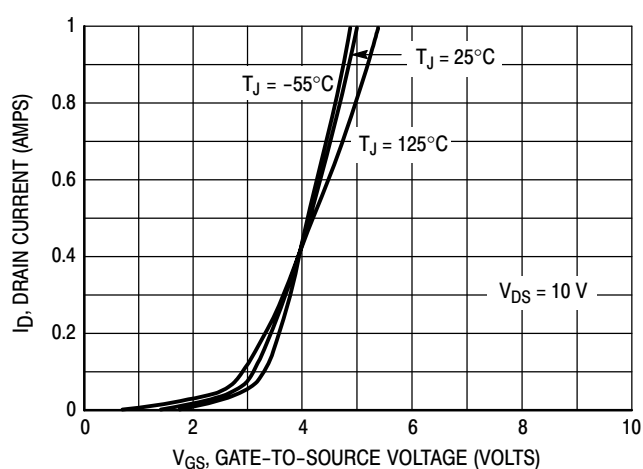


Figure 2. Transfer Characteristics

MMFT960T1

TYPICAL ELECTRICAL CHARACTERISTICS

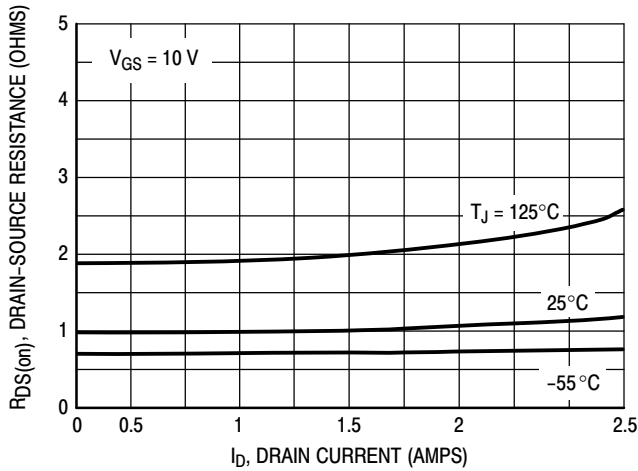


Figure 3. On-Resistance versus Drain Current

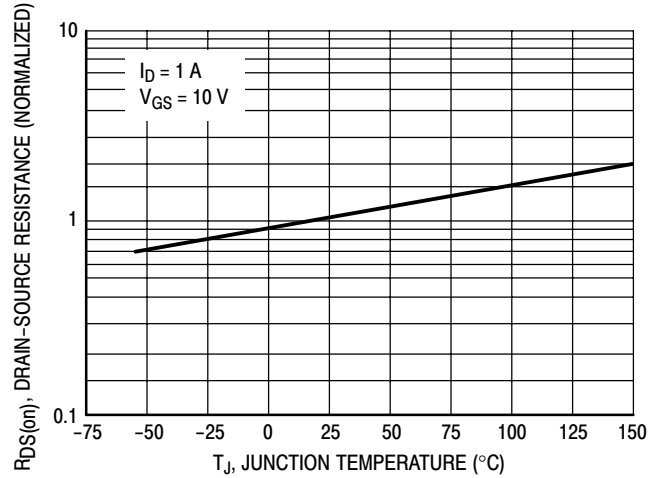


Figure 4. On-Resistance Variation with Temperature

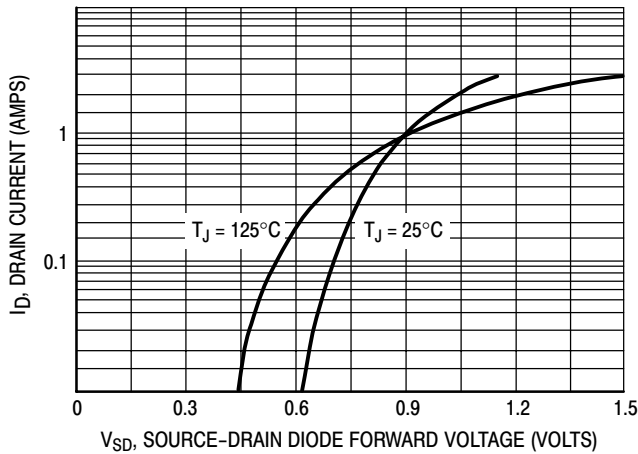


Figure 5. Source-Drain Diode Forward Voltage

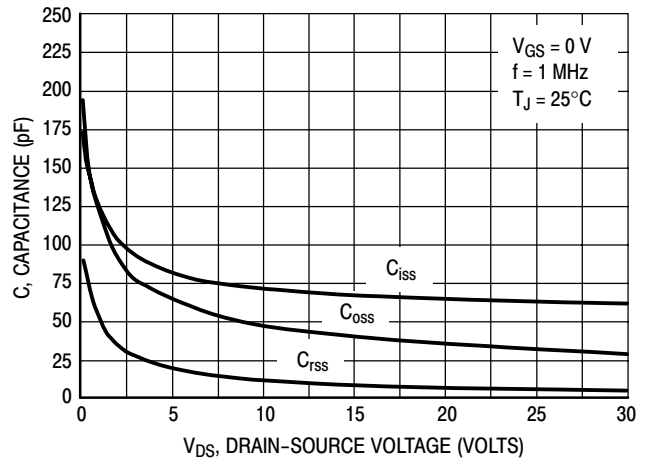


Figure 6. Capacitance Variation

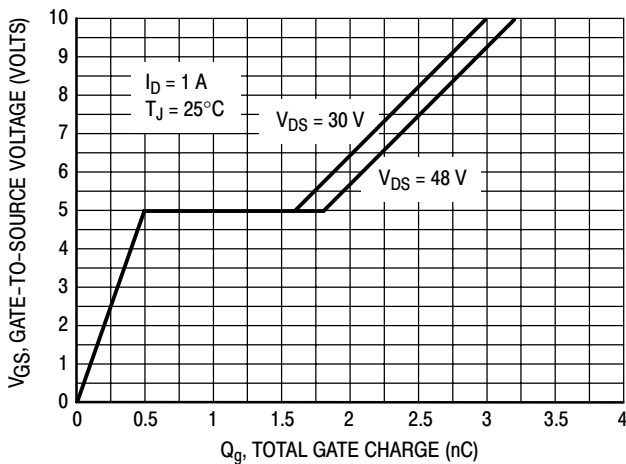


Figure 7. Gate Charge versus Gate-to-Source Voltage

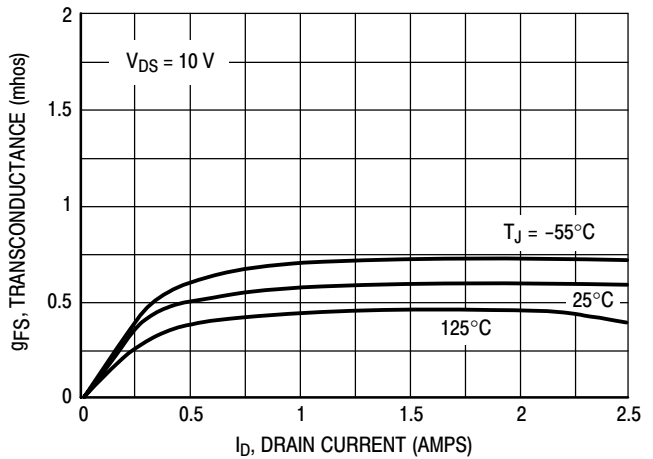


Figure 8. Transconductance

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