

# NSS30100LT1G

## 30 V, 2 A, Low $V_{CE(sat)}$ PNP Transistor

ON Semiconductor's e<sup>2</sup>PowerEdge family of low  $V_{CE(sat)}$  transistors are miniature surface mount devices featuring ultra low saturation voltage ( $V_{CE(sat)}$ ) and high current gain capability. These are designed for use in low voltage, high speed switching applications where affordable efficient energy control is important.

Typical application are DC-DC converters and power management in portable and battery powered products such as cellular and cordless phones, PDAs, computers, printers, digital cameras and MP3 players. Other applications are low voltage motor controls in mass storage products such as disc drives and tape drives. In the automotive industry they can be used in air bag deployment and in the instrument cluster. The high current gain allows e<sup>2</sup>PowerEdge devices to be driven directly from PMU's control outputs, and the Linear Gain (Beta) makes them ideal components in analog amplifiers.

- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

### MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ )

Rating	Symbol	Max	Unit
Collector-Emitter Voltage	$V_{CEO}$	-30	Vdc
Collector-Base Voltage	$V_{CBO}$	-50	Vdc
Emitter-Base Voltage	$V_{EBO}$	-5.0	Vdc
Collector Current - Continuous	$I_C$	-1.0	A
Collector Current - Peak	$I_{CM}$	-2.0	A

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$ (Note 1)	310	mW
		2.5	mW/ $^\circ\text{C}$
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$ (Note 1)	403	$^\circ\text{C}/\text{W}$
Total Device Dissipation $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$ (Note 2)	710	mW
		5.7	mW/ $^\circ\text{C}$
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$ (Note 2)	176	$^\circ\text{C}/\text{W}$
Total Device Dissipation (Single Pulse < 10 sec.)	$P_{D\text{single}}$ (Note 3)	575	mW
Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to +150	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

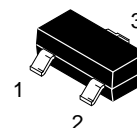
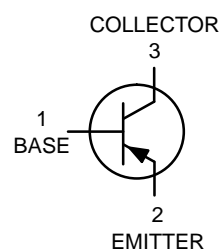
1. FR-4 @ Minimum Pad.
2. FR-4 @ 1.0 X 1.0 inch Pad.
3. Refer to Figure 8.



ON Semiconductor®

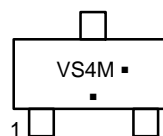
[www.onsemi.com](http://www.onsemi.com)

**30 VOLTS  
2.0 AMPS  
PNP LOW  $V_{CE(sat)}$  TRANSISTOR  
EQUIVALENT  $R_{DS(on)}$  200 m $\Omega$**



SOT-23 (TO-236)  
CASE 318  
STYLE 6

### MARKING DIAGRAM



VS4 = Specific Device Code  
M = Date Code\*  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or overbar may vary depending upon manufacturing location.

### ORDERING INFORMATION

Device	Package	Shipping†
NSS30100LT1G, NSV30100LT1G	SOT-23 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

# NSS30100LT1G

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
<b>OFF CHARACTERISTICS</b>				
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = –10 mA, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	–30	–	Vdc
Collector–Base Breakdown Voltage (I <sub>C</sub> = –0.1 mA, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	–50	–	Vdc
Emitter–Base Breakdown Voltage (I <sub>E</sub> = –0.1 mA, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	–5.0	–	Vdc
Collector Cutoff Current (V <sub>CB</sub> = –30 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	–	–0.1	μA <sub>dc</sub>
Collector–Emitter Cutoff Current (V <sub>CES</sub> = –30 Vdc)	I <sub>CES</sub>	–	–0.1	μA <sub>dc</sub>
Emitter Cutoff Current (V <sub>EB</sub> = –4.0 Vdc)	I <sub>EBO</sub>	–	–0.1	μA <sub>dc</sub>

## ON CHARACTERISTICS

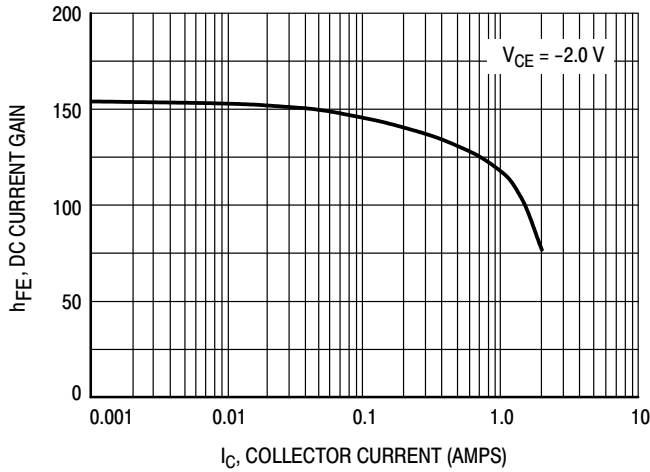
DC Current Gain (Note 4) (Figure 1) (I <sub>C</sub> = –1.0 mA, V <sub>CE</sub> = –2.0 V) (I <sub>C</sub> = –500 mA, V <sub>CE</sub> = –2.0 V) (I <sub>C</sub> = –1.0 A, V <sub>CE</sub> = –2.0 V) (I <sub>C</sub> = 2.0 A, V <sub>CE</sub> = –2.0 V)	h <sub>FE</sub>	100 100 80 40	– 300 – –	
Collector–Emitter Saturation Voltage (Note 4) (Figure 3) (I <sub>C</sub> = –0.5 A, I <sub>B</sub> = –0.05 A) (I <sub>C</sub> = –1.0 A, I <sub>B</sub> = 0.1 A) (I <sub>C</sub> = –2.0 A, I <sub>B</sub> = –0.2 A)	V <sub>CE(sat)</sub>	– – –	–0.25 –0.30 –0.65	V
Base–Emitter Saturation Voltage (Note 4) (Figure 2) (I <sub>C</sub> = –1.0 A, I <sub>B</sub> = –0.1 A)	V <sub>BE(sat)</sub>	–	–1.2	V
Base–Emitter Turn–on Voltage (Note 4) (I <sub>C</sub> = –1.0 A, V <sub>CE</sub> = –2.0 V)	V <sub>BE(on)</sub>	–	–1.1	V
Cutoff Frequency (I <sub>C</sub> = –100 mA, V <sub>CE</sub> = –5.0 V, f = 100 MHz)	f <sub>T</sub>	100	–	MHz
Output Capacitance (f = 1.0 MHz)	C <sub>obo</sub>	–	15	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

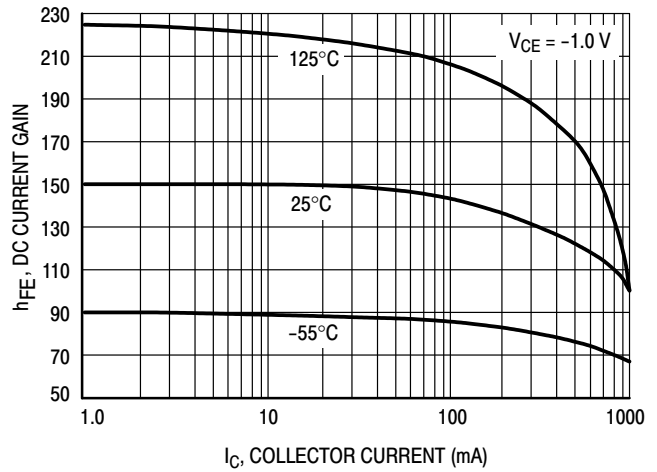
4. Pulsed Condition: Pulse Width = 300 msec, Duty Cycle ≤ 2%.

# NSS30100LT1G

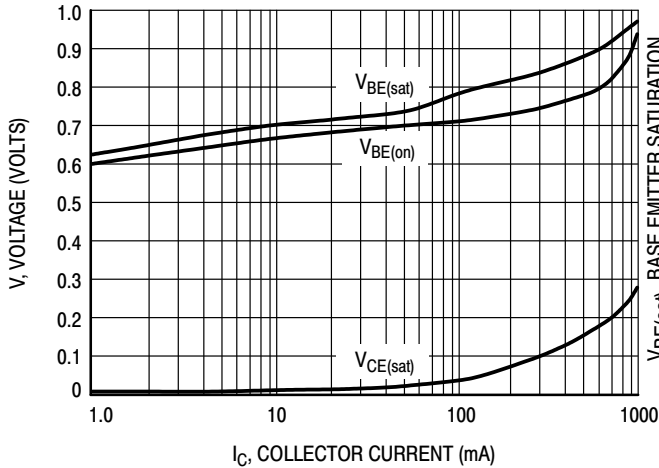
## TYPICAL CHARACTERISTICS



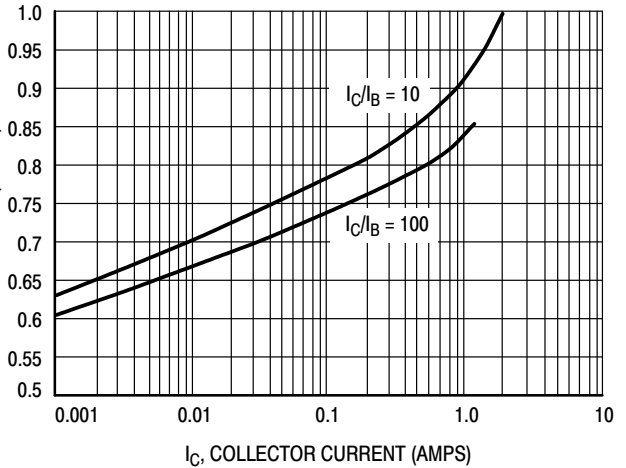
**Figure 1. DC Current Gain versus Collector Current**



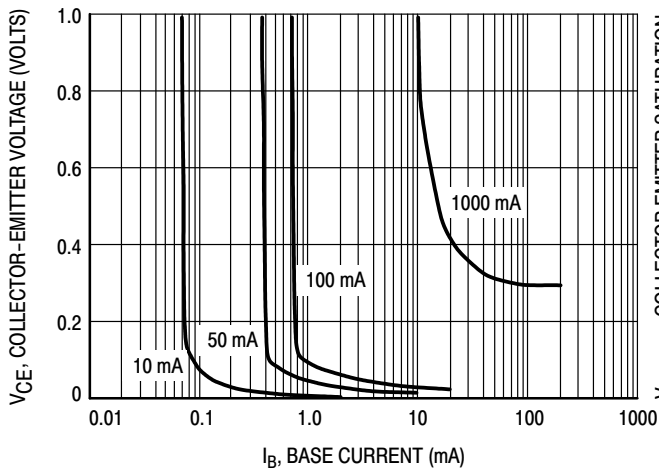
**Figure 2. DC Current Gain versus Collector Current**



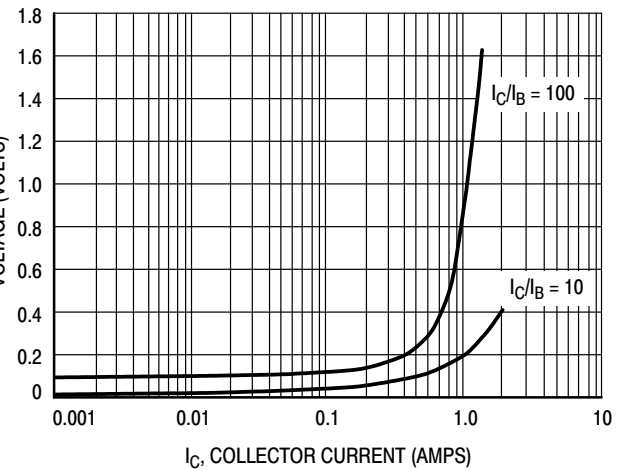
**Figure 3. "On" Voltages**



**Figure 4. Base Emitter Saturation Voltage versus Collector Current**



**Figure 5. Collector Emitter Saturation Voltage versus Base Current**



**Figure 6. Collector Emitter Saturation Voltage versus Collector Current**

# NSS30100LT1G

## TYPICAL CHARACTERISTICS

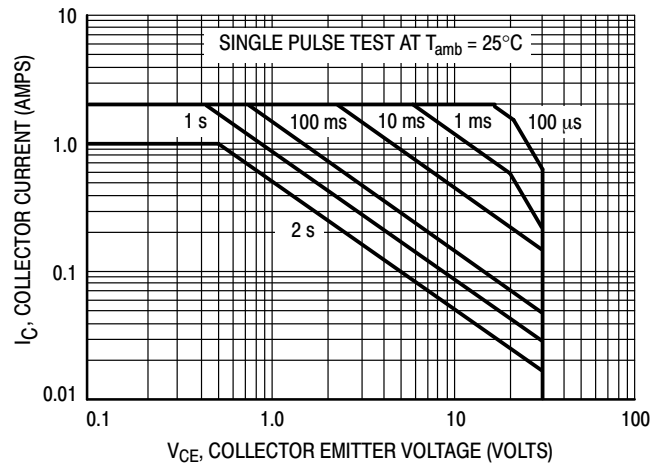


Figure 7. Safe Operating Area

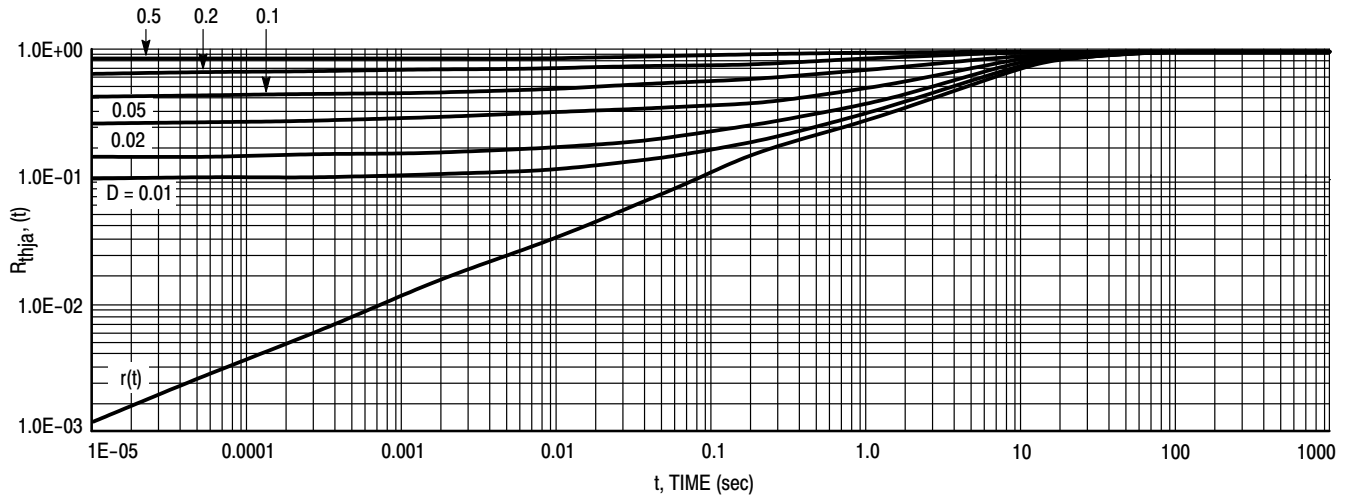


Figure 8. Normalized Thermal Response

# MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS



**SOT-23 (TO-236)**  
CASE 318  
ISSUE AT

DATE 01 MAR 2023

SCALE 4:1



**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M,1994.
2. CONTROLLING DIMENSION: MILLIMETERS
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

DIM	MILLIMETERS			INCHES		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	0.89	1.00	1.11	0.035	0.039	0.044
A1	0.01	0.06	0.10	0.000	0.002	0.004
b	0.37	0.44	0.50	0.015	0.017	0.020
c	0.08	0.14	0.20	0.003	0.006	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
e	1.78	1.90	2.04	0.070	0.075	0.080
L	0.30	0.43	0.55	0.012	0.017	0.022
L1	0.35	0.54	0.69	0.014	0.021	0.027
H <sub>E</sub>	2.10	2.40	2.64	0.083	0.094	0.104
T	0°	---	10°	0°	---	10°

**GENERIC MARKING DIAGRAM\***



- XXX = Specific Device Code
- M = Date Code
- = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.



**RECOMMENDED MOUNTING FOOTPRINT**

\* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

**STYLES ON PAGE 2**

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**MECHANICAL CASE OUTLINE  
PACKAGE DIMENSIONS**



**SOT-23 (TO-236)  
CASE 318  
ISSUE AT**

DATE 01 MAR 2023

- |   |   |   |   |   |   |
|---|---|---|---|---|---|
| STYLE 1 THRU 5:<br>CANCELLED                            | STYLE 6:<br>PIN 1. BASE<br>2. EMITTER<br>3. COLLECTOR | STYLE 7:<br>PIN 1. EMITTER<br>2. BASE<br>3. COLLECTOR       | STYLE 8:<br>PIN 1. ANODE<br>2. NO CONNECTION<br>3. CATHODE  |   |   |
| STYLE 9:<br>PIN 1. ANODE<br>2. ANODE<br>3. CATHODE      | STYLE 10:<br>PIN 1. DRAIN<br>2. SOURCE<br>3. GATE     | STYLE 11:<br>PIN 1. ANODE<br>2. CATHODE<br>3. CATHODE-ANODE | STYLE 12:<br>PIN 1. CATHODE<br>2. CATHODE<br>3. ANODE       | STYLE 13:<br>PIN 1. SOURCE<br>2. DRAIN<br>3. GATE           | STYLE 14:<br>PIN 1. CATHODE<br>2. GATE<br>3. ANODE          |
| STYLE 15:<br>PIN 1. GATE<br>2. CATHODE<br>3. ANODE      | STYLE 16:<br>PIN 1. ANODE<br>2. CATHODE<br>3. CATHODE | STYLE 17:<br>PIN 1. NO CONNECTION<br>2. ANODE<br>3. CATHODE | STYLE 18:<br>PIN 1. NO CONNECTION<br>2. CATHODE<br>3. ANODE | STYLE 19:<br>PIN 1. CATHODE<br>2. ANODE<br>3. CATHODE-ANODE | STYLE 20:<br>PIN 1. CATHODE<br>2. ANODE<br>3. GATE          |
| STYLE 21:<br>PIN 1. GATE<br>2. SOURCE<br>3. DRAIN       | STYLE 22:<br>PIN 1. RETURN<br>2. OUTPUT<br>3. INPUT   | STYLE 23:<br>PIN 1. ANODE<br>2. ANODE<br>3. CATHODE         | STYLE 24:<br>PIN 1. GATE<br>2. DRAIN<br>3. SOURCE           | STYLE 25:<br>PIN 1. ANODE<br>2. CATHODE<br>3. GATE          | STYLE 26:<br>PIN 1. CATHODE<br>2. ANODE<br>3. NO CONNECTION |
| STYLE 27:<br>PIN 1. CATHODE<br>2. CATHODE<br>3. CATHODE | STYLE 28:<br>PIN 1. ANODE<br>2. ANODE<br>3. ANODE     |   |   |   |   |

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