

Schottky Barrier Diode, 500 mA, 30 V

NSR05T304MX

These Schottky barrier diodes are optimized for low forward voltage drop and low leakage current that offers the most optimal power dissipation in applications. They are housed in spacing saving micro-packaging ideal for space constraint applications.

Features

- Low Forward Voltage Drop – 410 mV (Typ.) @ $I_F = 500$ mA
- Low Reverse Current – 35 μ A (Typ.) @ $V_R = 30$ V
- 500 mA of Continuous Forward Current
- High Switching Speed
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Typical Applications

- LCD and Keypad Backlighting
- Camera Photo Flash
- Buck and Boost dc-dc Converters
- Reverse Voltage and Current Protection
- Clamping & Protection

MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|---|-----------|-------|------|
| Reverse Voltage | V_R | 30 | V |
| Forward Current (DC) | I_F | 500 | mA |
| Forward Surge Current (60 Hz @ 1 cycle) | I_{FSM} | 2.2 | A |
| Repetitive Peak Forward Current (Pulse Wave = 1 sec, Duty Cycle = 66%) | I_{FRM} | 1.4 | A |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



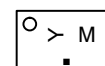
ON Semiconductor®

www.onsemi.com



**X2DFN2
CASE 714AB**

MARKING DIAGRAM



- Y = Specific Device Code
M = Date Code
■ = Pb-Free Package



ORDERING INFORMATION

| Device | Package | Shipping† |
|----------------|---------------------|-----------------------------------|
| NSR05T304MXT5G | X2DFN2 (Pb-Free) | 2 mm Pitch 8000/Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

NSR05T304MX

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Min | Typ | Max | Unit |
|--|--------------------------|------------|-----|-------------|--------------------------|
| Thermal Resistance Junction-to-Ambient (Note 1) Total Power Dissipation @ $T_A = 25^\circ\text{C}$ | $R_{\theta JA}$ P_D | | | 310 480 | $^\circ\text{C/W}$ mW |
| Thermal Resistance Junction-to-Ambient (Note 2) Total Power Dissipation @ $T_A = 25^\circ\text{C}$ | $R_{\theta JA}$ P_D | | | 150 1000 | $^\circ\text{C/W}$ mW |
| Junction and Storage Temperature Range | T_J, T_{stg} | -55 to +85 | | | $^\circ\text{C}$ |

1. Mounted onto a 4 in square FR-4 board 50 mm sq. 1 oz. Cu 0.06" thick single sided. Operating to steady state.
2. Mounted onto a 4 in square FR-4 board 650 mm sq. 1 oz. Cu 0.06" thick single sided. Operating to steady state.

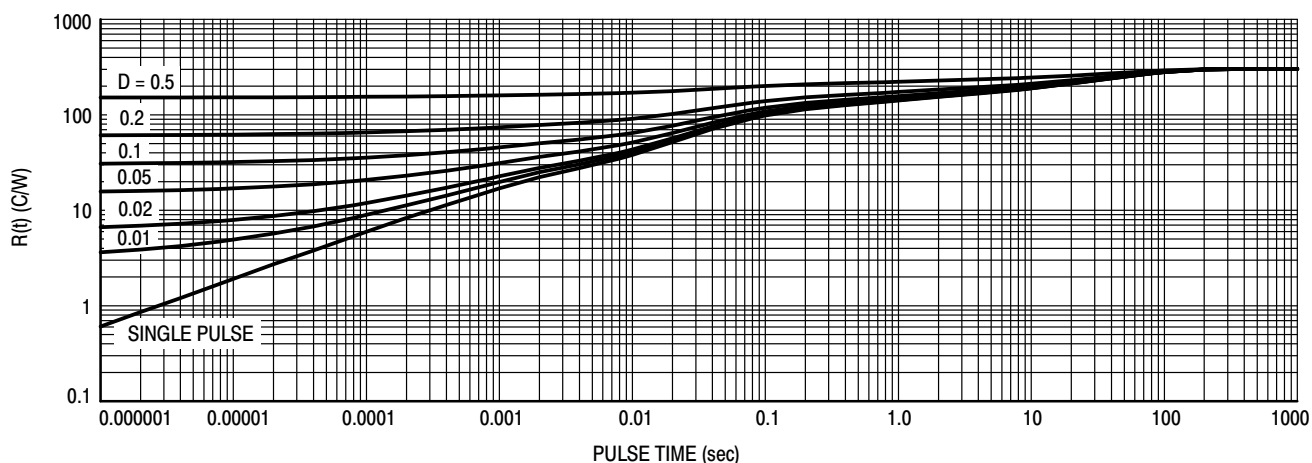


Figure 1. Thermal Response (Note 1)

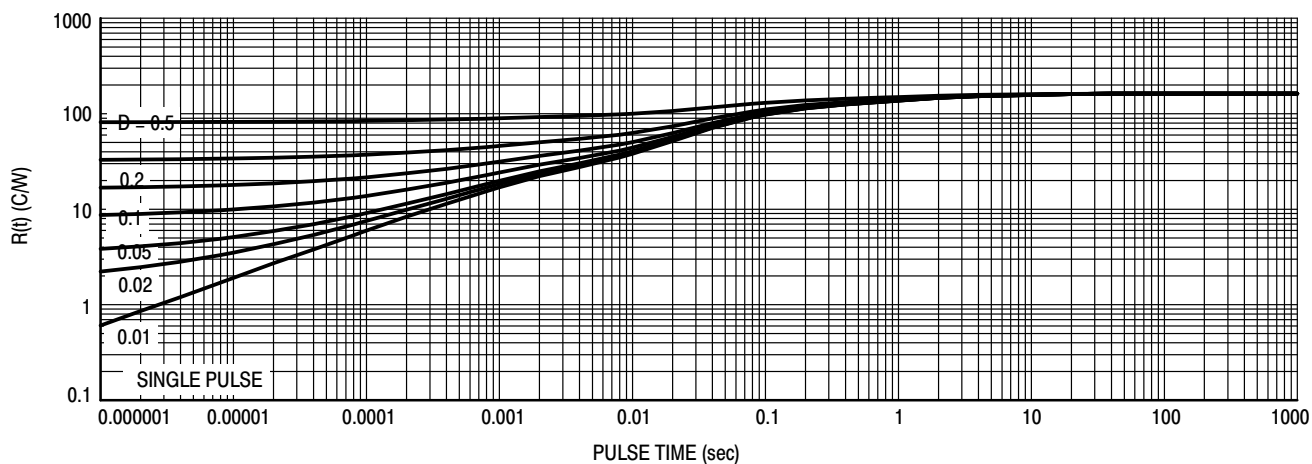
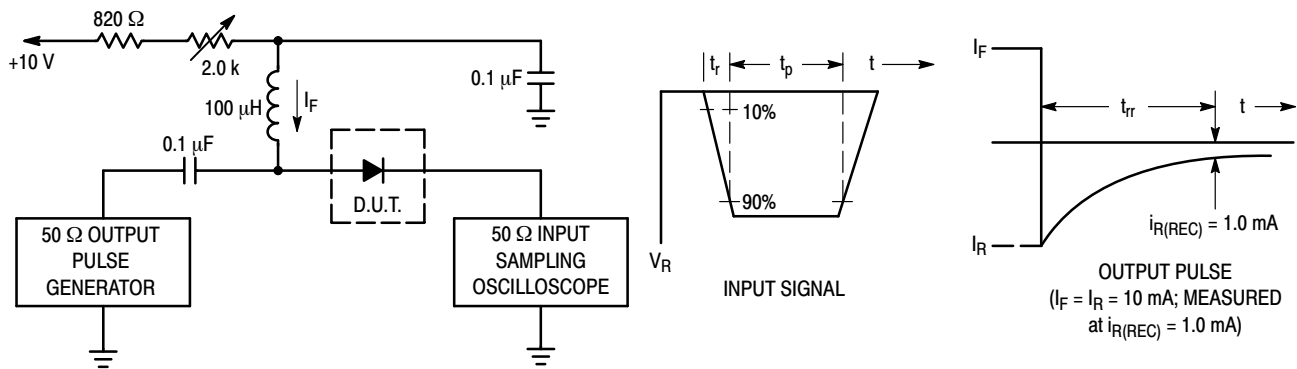


Figure 2. Thermal Response (Note 2)

NSR05T304MX

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

| Characteristic | Symbol | Min | Typ | Max | Unit |
|--|-----------|-----|--------------------------|--------------------------|---------------|
| Reverse Leakage ($V_R = 10\text{ V}$) ($V_R = 30\text{ V}$) | I_R | | 20 35 | 50 75 | μA |
| Forward Voltage ($I_F = 10\text{ mA}$) ($I_F = 100\text{ mA}$) ($I_F = 200\text{ mA}$) ($I_F = 500\text{ mA}$) | V_F | | 215 295 330 410 | 245 320 355 435 | mV |
| Total Capacitance ($V_R = 1.0\text{ V}$, $f = 1.0\text{ MHz}$) | C_T | | 30 | | pF |
| Reverse Recovery Time ($I_F = I_R = 10\text{ mA}$, $I_{R(\text{REC})} = 1.0\text{ mA}$, Figure 3) | t_{rr} | | 15 | | ns |
| Peak Forward Recovery Voltage ($I_F = 100\text{ mA}$, $t_r = 20\text{ ns}$, Figure 4) | V_{FRM} | | 430 | | mV |



- Notes: 1. A 2.0 k Ω variable resistor adjusted for a Forward Current (I_F) of 10 mA.
2. Input pulse is adjusted so $I_{R(\text{peak})}$ is equal to 10 mA.
3. $t_p \gg t_{rr}$

Figure 3. Recovery Time Equivalent Test Circuit

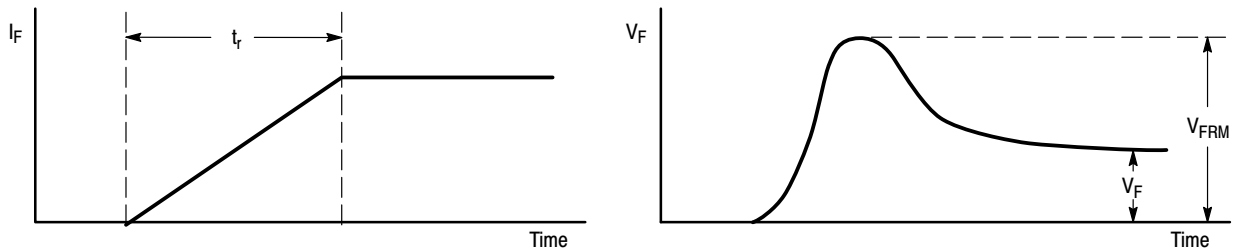


Figure 4. Peak Forward Recovery Voltage Definition

TYPICAL CHARACTERISTICS

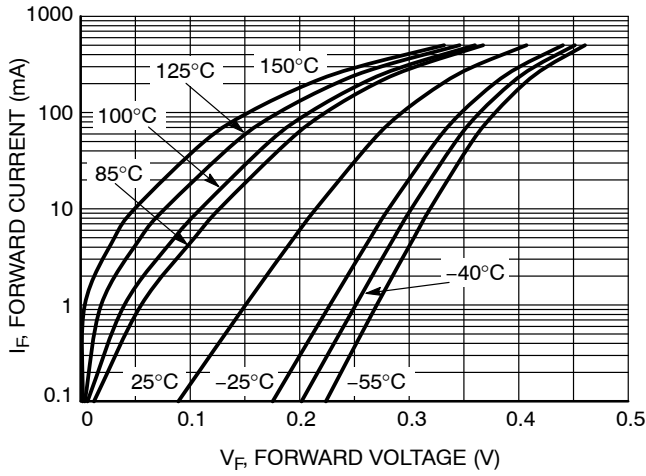


Figure 5. Forward Voltage

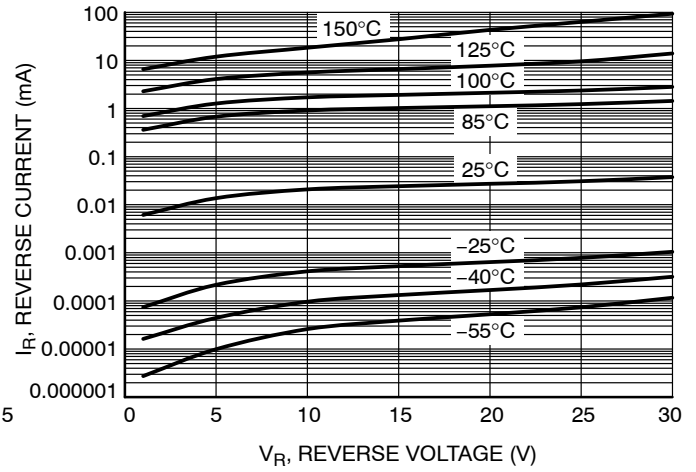


Figure 6. Leakage Current

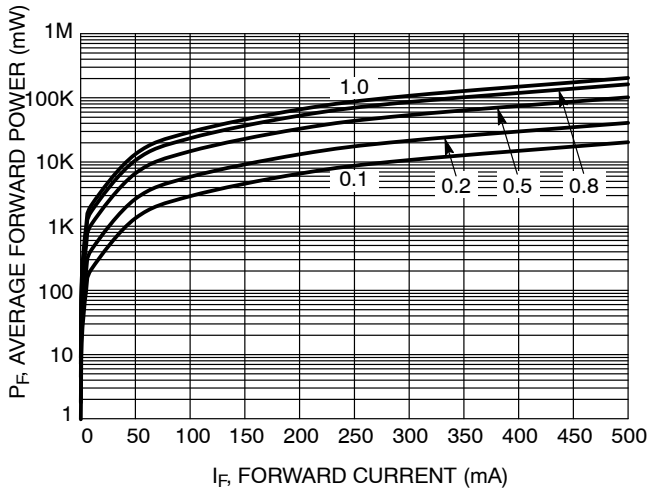


Figure 7. Average Forward Power Dissipation

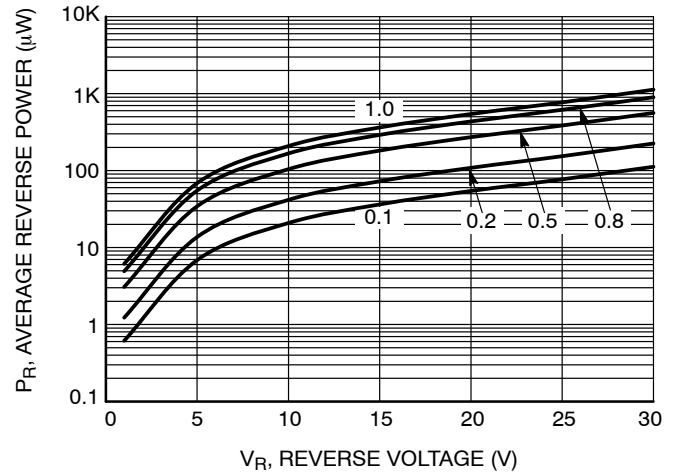


Figure 8. Average Reverse Power Dissipation

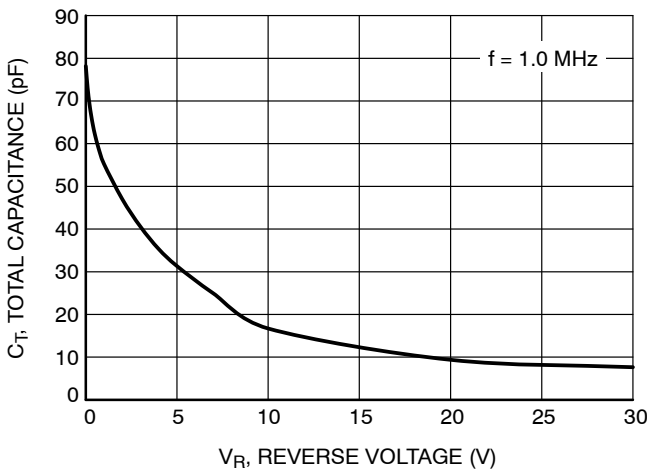


Figure 9. Total Capacitance

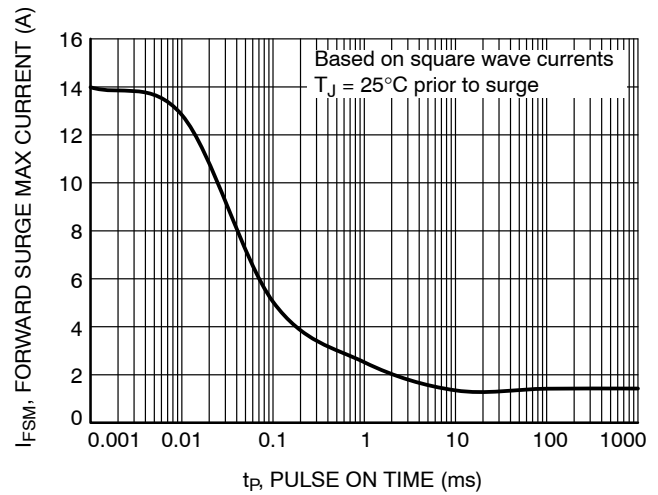
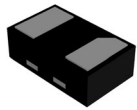


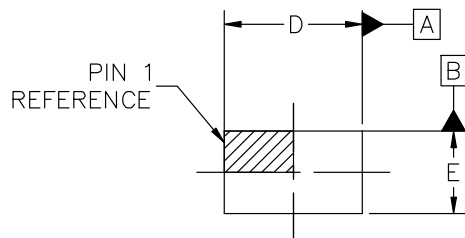
Figure 10. Forward Surge Current

MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

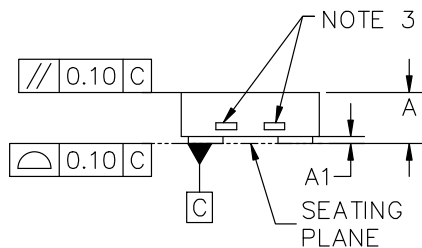


X2DFN2 1.00x0.60x0.37, 0.65P
CASE 714AB
ISSUE C

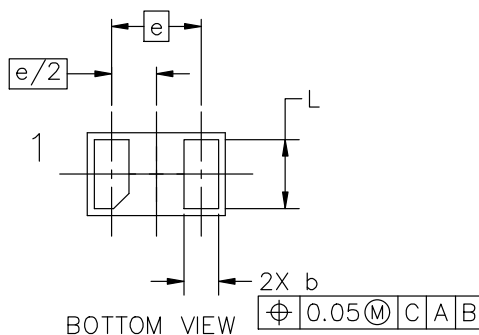
DATE 21 FEB 2024



TOP VIEW



SIDE VIEW

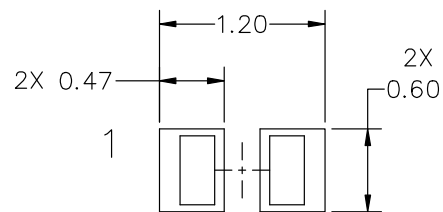


BOTTOM VIEW

NOTES:

1. DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018.
2. ALL DIMENSION ARE IN MILLIMETERS.
3. EXPOSED COPPER ALLOWED AS SHOW.

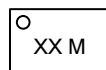
| DIM | MILLIMETERS | | |
|-----|-------------|------|-------|
| | MIN. | NOM. | MAX. |
| A | 0.34 | 0.37 | 0.40 |
| A1 | --- | 0.03 | 0.050 |
| b | 0.20 | 0.25 | 0.30 |
| D | 0.95 | 1.00 | 1.05 |
| E | 0.55 | 0.60 | 0.65 |
| e | 0.65 BSC | | |
| L | 0.45 | 0.50 | 0.55 |



RECOMMENDED MOUNTING FOOTPRINT*

* FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERM/D.

GENERIC MARKING DIAGRAM*



XX = Specific Device Code
M = Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

| | | |
|-------------------------|-------------------------------------|--|
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| DESCRIPTION: | X2DFN2 1.00x0.60x0.37, 0.65P | PAGE 1 OF 1 |

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