

MOSFET – Power, P-Channel, Single ATPAK

-60 V, -35 A, 29.5 mΩ

ATP113

特長

- オン抵抗 : $R_{DS(on)1} = 22.5 \text{ m}\Omega$ (typ)
- 4 V 駆動
- 保護ダイオード入り
- 入力容量 $C_{iss} = 2400 \text{ pF}$ (typ)
- ハロゲンフリー対応

絶対最大定格 ABSOLUTE MAXIMUM RATINGS ($T_a = 25^\circ\text{C}$)

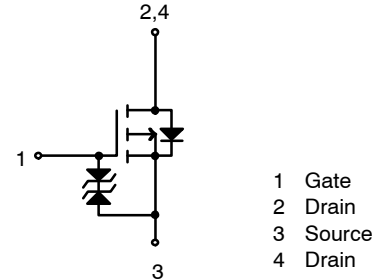
項目	記号	条件	定格値	Unit
ドレイン・ソース電圧	V_{DSS}		-60	V
ゲート・ソース電圧	V_{GSS}		± 20	V
ドレイン電流 (DC)	I_D		-35	A
ドレイン電流 ($PW \leq 10 \mu\text{s}$)	I_{DP}	$PW \leq 10 \mu\text{s}$, duty cycle $\leq 1\%$	-105	A
許容損失	P_D	$T_c = 25^\circ\text{C}$	50	W
チャネル温度	T_{ch}		150	$^\circ\text{C}$
保存周囲温度	T_{stg}		-55~ +150	$^\circ\text{C}$
アバランシェエネルギー (単発) (Note NO TAG)	E_{AS}		95	mJ
アバランシェ電流 (Note 2)	I_{AV}		-18	A

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

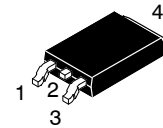
(参考訳)

最大定格を超えるストレスは、デバイスにダメージを与える危険性があります。これらの定格値を超えた場合は、デバイスの機能性を損ない、ダメージが生じたり、信頼性に影響を及ぼす危険性があります。

1. $V_{DD} = -10 \text{ V}$, $L = 500 \mu\text{H}$, $I_{AV} = -18 \text{ A}$
2. $L \leq 500 \mu\text{H}$, 1 パルス

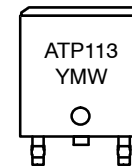


電気的接続図



DPAK (Single Gauge) / ATPAK
CASE 369AM

マーキング



ATP113 = Specific Device Code
 Y = Year of Production
 M = Assembly Operation Month
 W = Work Week in the Month

ORDERING INFORMATION

Device	パッケージ名	最小梱包単位†
ATP113-TL-H	DPAK / ATPAK (Pb-Free and Halide Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ATP113

電気的特性 ELECTRICAL CHARACTERISTICS (Ta = 25°C)

項目	記号	条件	定格値			Unit
			Min	Typ	Max	
ドレイン・ソース降伏電圧	$V_{(BR)DSS}$	$I_D = -1 \text{ mA}, V_{GS} = 0 \text{ V}$	-60	-	-	V
ドレイン・ソースシャ断電流	I_{DSS}	$V_{DS} = -60 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	-1	μA
ゲート・ソースもれ電流	I_{GSS}	$V_{GS} = +16 \text{ V}, V_{DS} = 0 \text{ V}$	-	-	± 10	μA
ゲート・ソースシャ断電圧	$V_{GS(off)}$	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	-1.2	-	-2.6	V
順伝達アドミタンス	$ y_{fs} $	$V_{DS} = -10 \text{ V}, I_D = -18 \text{ A}$	-	37	-	S
ドレイン・ソース間オン抵抗	$R_{DS(on)1}$	$I_D = -18 \text{ A}, V_{GS} = -10 \text{ V}$	-	22.5	29.5	$\text{m}\Omega$
	$R_{DS(on)2}$	$I_D = -9 \text{ A}, V_{GS} = -4.5 \text{ V}$	-	27	38	$\text{m}\Omega$
	$R_{DS(on)3}$	$I_D = -5 \text{ A}, V_{GS} = -4 \text{ V}$	-	29	44	$\text{m}\Omega$
入力容量	C_{iss}	$V_{DS} = -20 \text{ V}, f = 1 \text{ MHz}$	-	2400	-	pF
出力容量	C_{oss}		-	250	-	pF
帰還容量	C_{rss}		-	195	-	pF
ターンオン遅延時間	$t_d(on)$	指定回路において	-	15	-	ns
立ち上がり時間	t_r		-	125	-	ns
ターンオフ遅延時間	$t_d(off)$		-	250	-	ns
下降時間	t_f		-	200	-	ns
総ゲート電荷量	Q_g		$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -35 \text{ A}$	-	55	-
ゲート・ソース電荷量	Q_{gs}	-		7.5	-	nC
ゲート・ドレイン電荷量	Q_{gd}	-		12	-	nC
ダイオード順電圧	V_{SD}	$I_S = -35 \text{ A}, V_{GS} = 0 \text{ V}$	-	-0.98	-1.5	V

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

(参考訳)

製品パラメータは、特別な記述が無い限り、記載されたテスト条件に対する電気的特性で示しています。異なる条件下で製品動作を行った時には、電気的特性で示している特性を得られない場合があります。

スイッチングタイム測定回路図

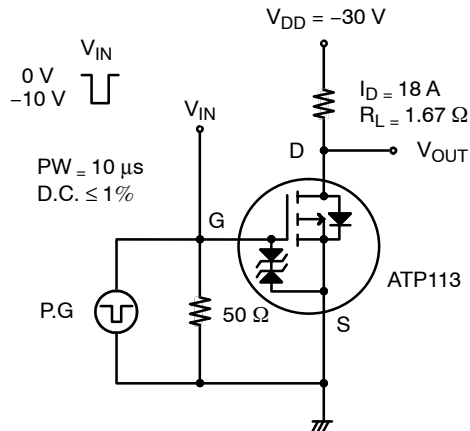


図 1. スwitchingタイム測定回路図

ATP113

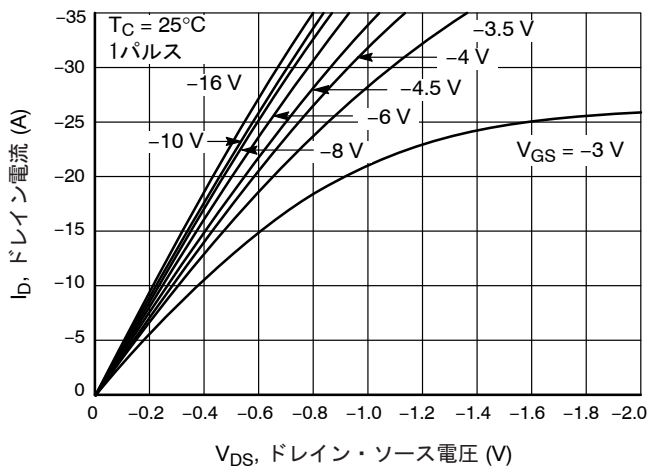


図 2. $I_D - V_{DS}$

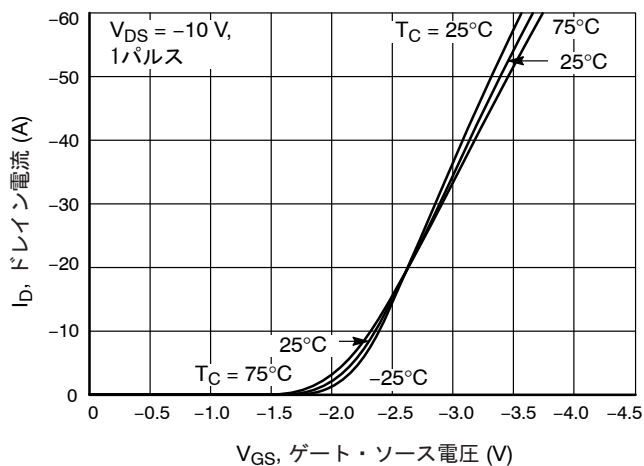


図 3. $I_D - V_{GS}$

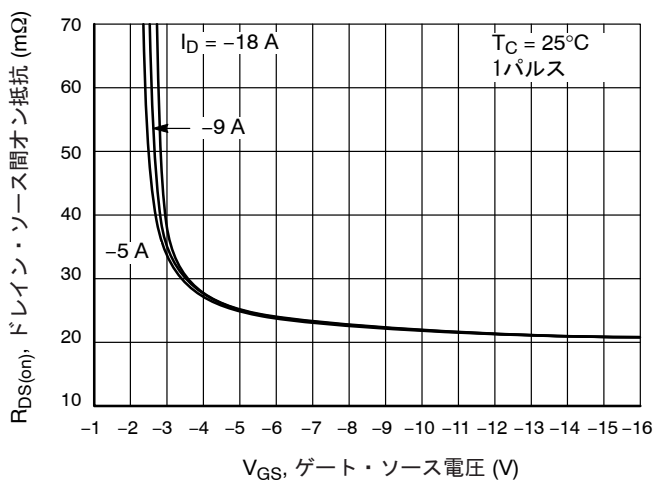


図 4. $R_{DS(on)} - V_{GS}$

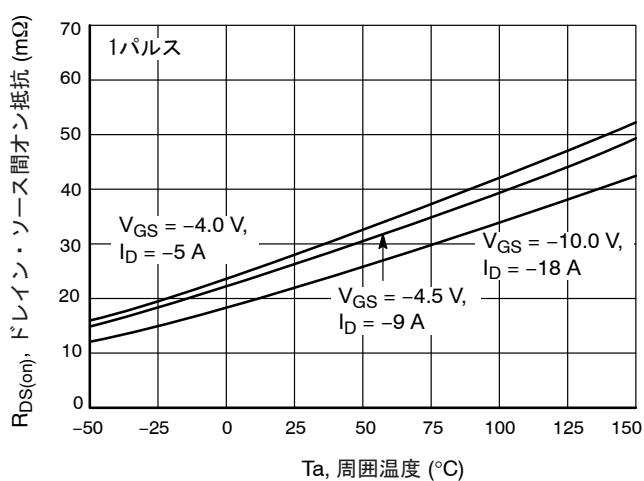


図 5. $R_{DS(on)} - T_a$

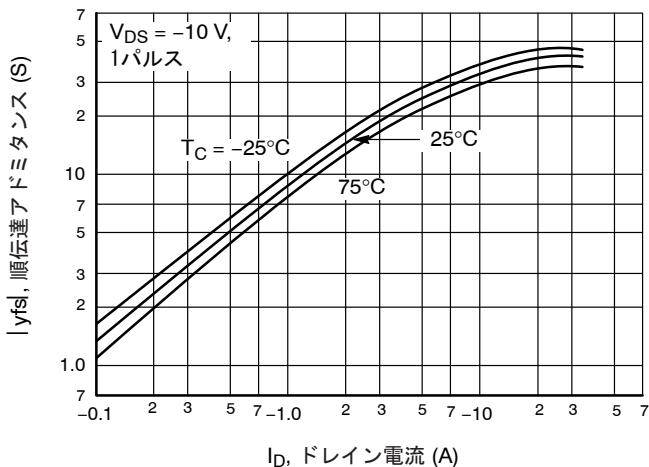


図 6. $|y_{fs}| - I_D$

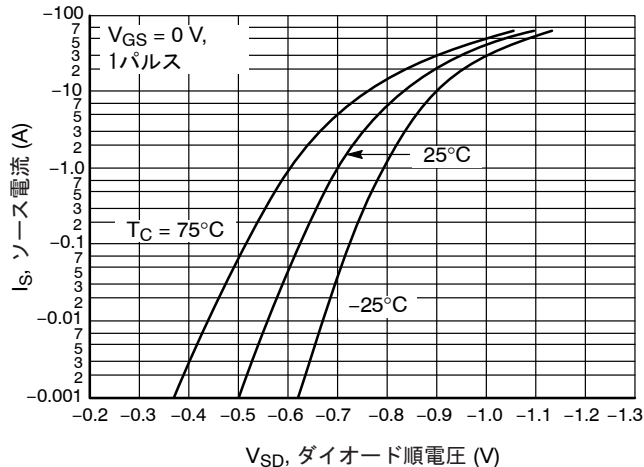


図 7. $I_S - V_{SD}$

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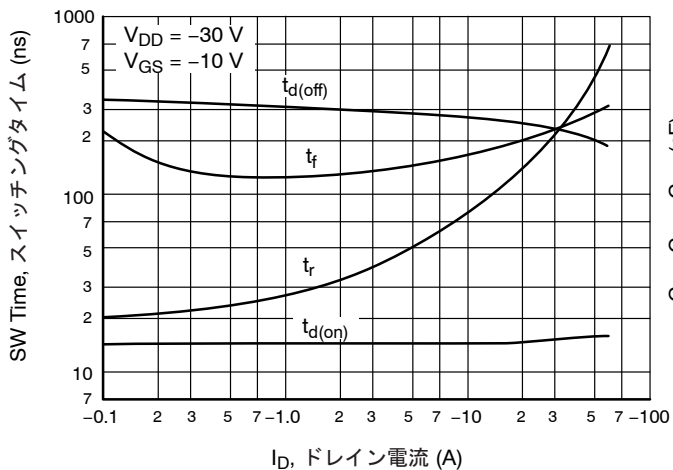


図 8. SW Time - I_D

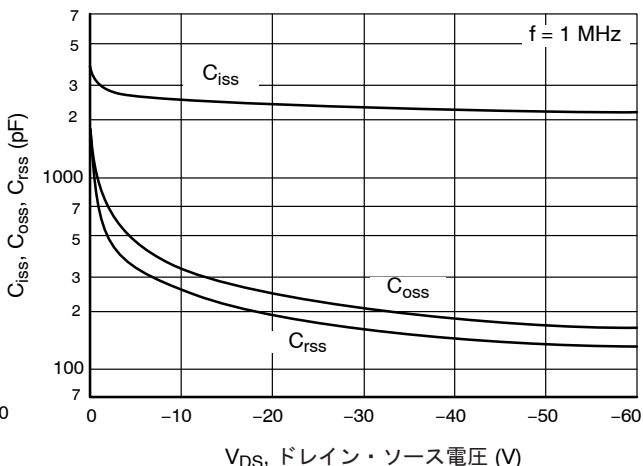


図 9. C_{iss} , C_{oss} , C_{rss} - V_{DS}

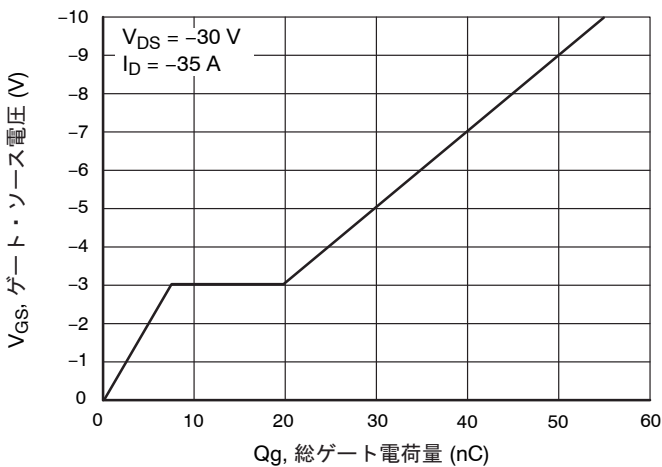


図 10. V_{GS} - Q_g

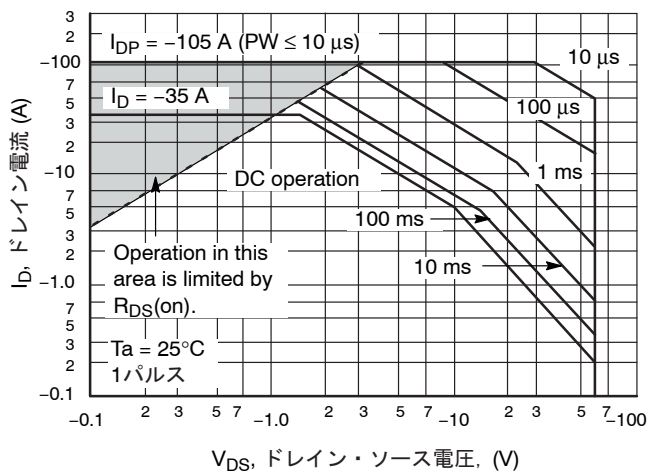


図 11. ASO

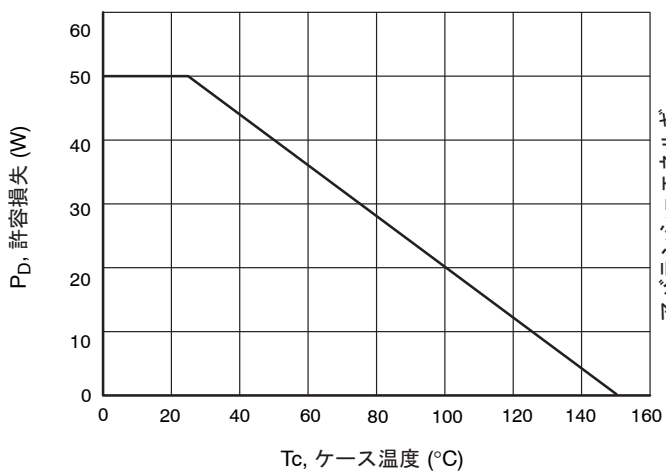


図 12. P_D - T_C

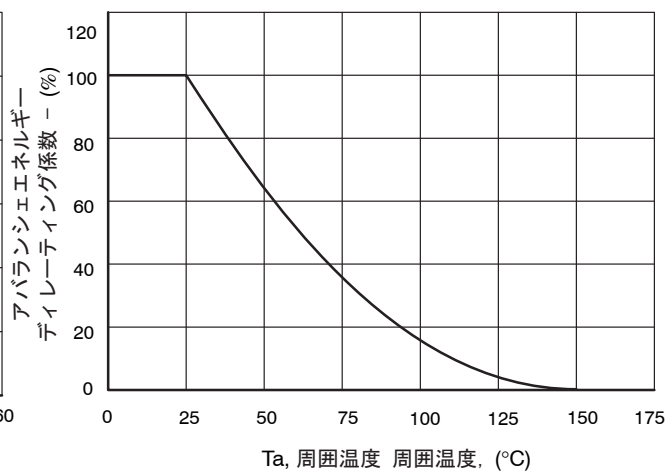


図 13. E_{AS} - T_a

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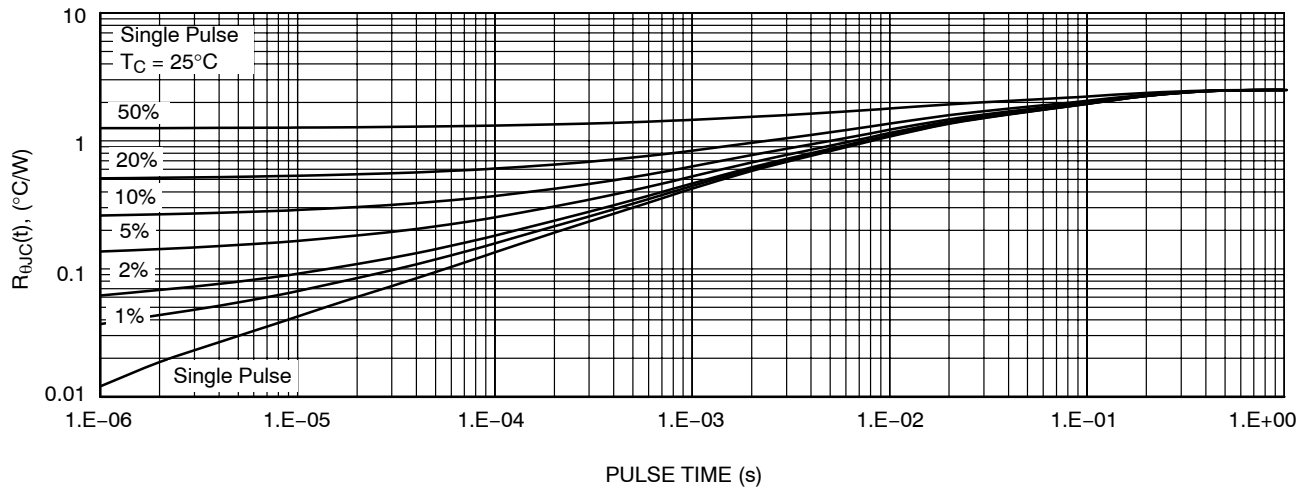


图 14. Thermal Response

MECHANICAL CASE OUTLINE

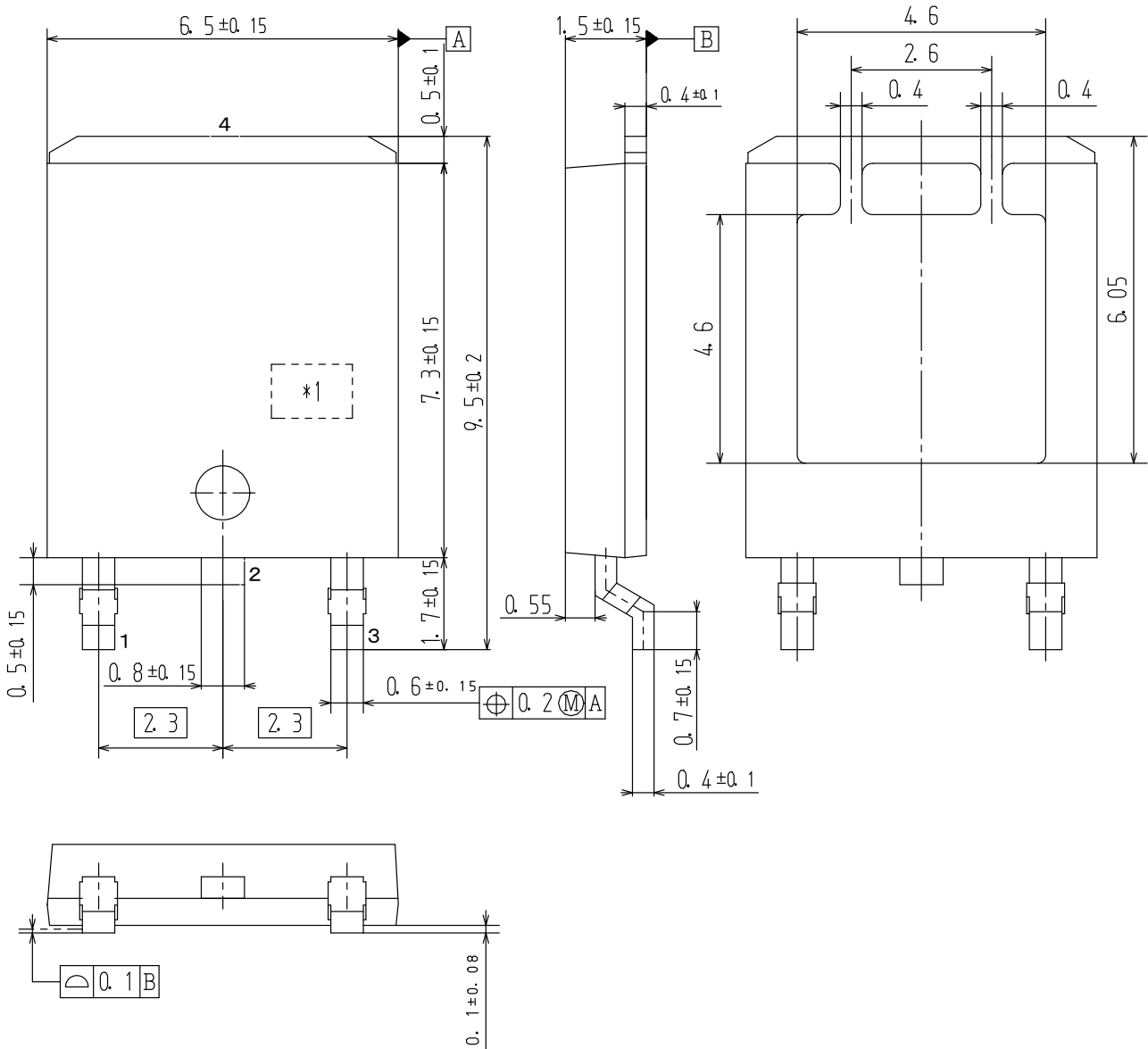
PACKAGE DIMENSIONS

ON Semiconductor®



DPAK (Single Gauge) / ATPAK CASE 369AM ISSUE O

DATE 29 FEB 2012



Pin2 is idle pin with electrical designation only carried

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