onsemi

NPN/PNP Dual General Purpose Transistor BC847BPDXV6, SBC847BPDXV6

This transistor is designed for general purpose amplifier applications. It is housed in the SOT-563 which is designed for low power surface mount applications.

Features

- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

MAXIMUM RATINGS – NPN

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	45	V
Collector – Base Voltage	V _{CBO}	50	V
Emitter – Base Voltage	V _{EBO}	6.0	V
Collector Current – Continuous	Ι _C	100	mAdc

MAXIMUM RATINGS - PNP

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V _{CEO}	-45	V
Collector – Base Voltage	V _{CBO}	-50	V
Emitter – Base Voltage	V _{EBO}	-5.0	V
Collector Current – Continuous	Ι _C	-100	mAdc

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL CHARACTERISTICS

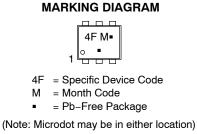
Characteristic (One Junction Heated)	Symbol	Max	Unit
Total Device Dissipation (Note 1) T _A = 25°C Derate above 25°C	P _D	357 2.9	mW mW/°C
Thermal Resistance – Junction-to-Ambient (Note 1)	$R_{\theta JA}$	350	°C/W
Characteristic (Both Junctions Heated)	Symbol	Max	Unit
Total Device Dissipation (Note 1) T _A = 25°C Derate above 25°C	P _D	500 4.0	mW mW/°C
Thermal Resistance – Junction-to-Ambient (Note 1)	$R_{\theta JA}$	250	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	–55 to +150	°C

(3) (2) (1) Q₁ Q₂ (4) (5) (6)

BC847BPDX6T1



SOT-563 CASE 463A



ORDERING INFORMATION

Device	Package	Shipping [†]
BC847BPDXV6T1G	SOT-563 (Pb-Free)	4 mm pitch 4000/Tape & Reel
SBC847BPDXV6T1G		4 mm pitch 4000/Tape & Reel
BC847BPDXV6T5G	SOT-563 (Pb-Free)	2 mm pitch 8000/Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

1. FR-4 @ Minimum Pad

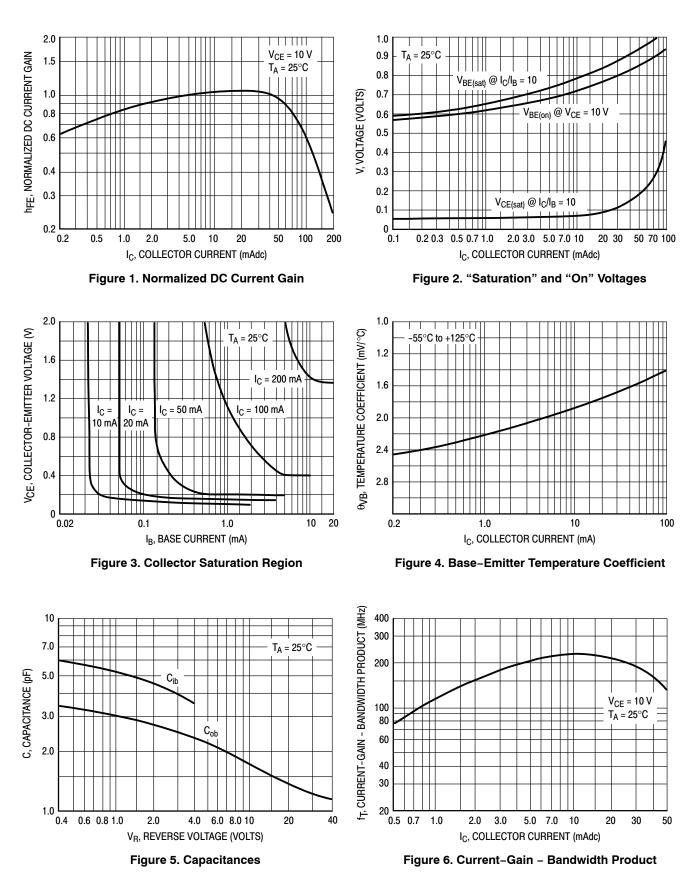
ELECTRICAL CHARACTERISTICS (NPN) (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	I			•	
Collector – Emitter Breakdown Voltage $(I_C = 10 \text{ mA})$	V _{(BR)CEO}	45	_	_	V
Collector – Emitter Breakdown Voltage $(I_C = 10 \ \mu A, \ V_{EB} = 0)$	V _{(BR)CES}	50	_	_	V
Collector – Base Breakdown Voltage $(I_C = 10 \ \mu A)$	V _{(BR)CBO}	50	_	_	V
Emitter – Base Breakdown Voltage $(I_E = 1.0 \ \mu A)$	V _{(BR)EBO}	6.0	_	_	V
Collector Cutoff Current (V _{CB} = 30 V) (V _{CB} = 30 V, T _A = 150°C)	I _{CBO}			15 5.0	nA μA
ON CHARACTERISTICS					
DC Current Gain ($I_C = 10 \ \mu A, V_{CE} = 5.0 \ V$) ($I_C = 2.0 \ mA, V_{CE} = 5.0 \ V$)	h _{FE}	_ 200	150 290	_ 475	-
Collector – Emitter Saturation Voltage (I _C = 10 mA, I _B = 0.5 mA) (I _C = 100 mA, I _B = 5.0 mA)	V _{CE(sat)}	-		0.25 0.6	V
Base – Emitter Saturation Voltage (I _C = 10 mA, I _B = 0.5 mA) (I _C = 100 mA, I _B = 5.0 mA)	V _{BE(sat)}	-	0.7 0.9		V
Base – Emitter Voltage (I _C = 2.0 mA, V _{CE} = 5.0 V) (I _C = 10 mA, V _{CE} = 5.0 V)	V _{BE(on)}	580 -	660 -	700 770	mV
SMALL-SIGNAL CHARACTERISTICS					•
Current – Gain – Bandwidth Product ($I_C = 10 \text{ mA}, V_{CE} = 5.0 \text{ Vdc}, f = 100 \text{ MHz}$)	f _T	100	_	_	MHz
Output Capacitance (V _{CB} = 10 V, f = 1.0 MHz)	C _{obo}	_	-	4.5	pF
Noise Figure (I _C = 0.2 mA, V _{CE} = 5.0 Vdc, R _S = 2.0 kΩ, f = 1.0 kHz, BW = 200 Hz)	NF	_	_	10	dB

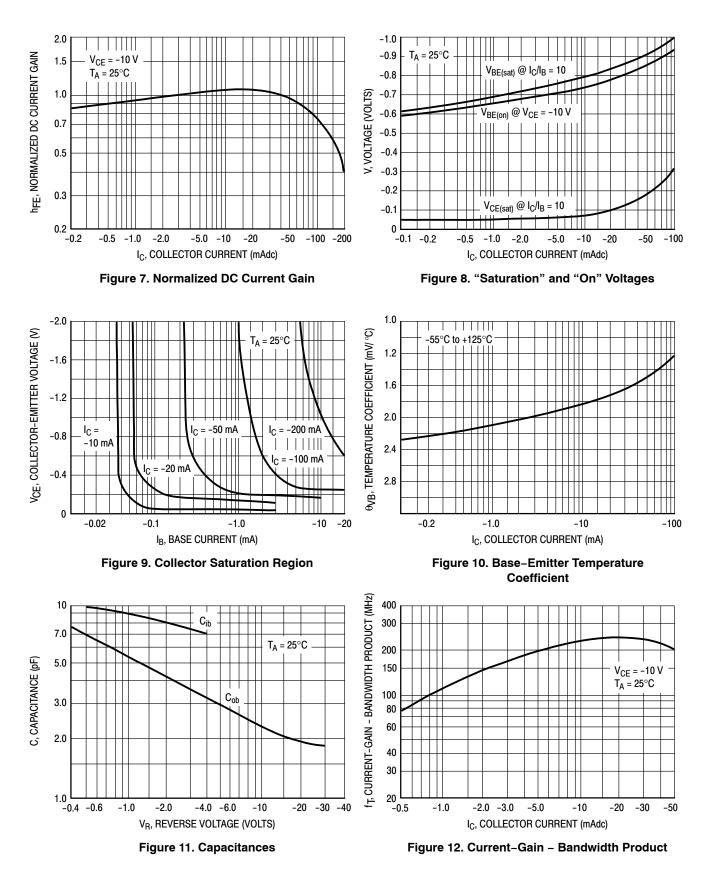
ELECTRICAL CHARACTERISTICS (PNP) (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	·				•
Collector – Emitter Breakdown Voltage $(I_{C} = -10 \text{ mA})$	V _{(BR)CEO}	-45	_	-	V
Collector – Emitter Breakdown Voltage ($I_C = -10 \ \mu$ A, V _{EB} = 0)	V _{(BR)CES}	-50	_	-	V
Collector – Base Breakdown Voltage $(I_C = -10 \ \mu A)$	V _{(BR)CBO}	-50	_	-	V
Emitter – Base Breakdown Voltage ($I_E = -1.0 \ \mu A$)	V _{(BR)EBO}	-5.0	-	-	V
Collector Cutoff Current (V _{CB} = -30 V) (V _{CB} = -30 V, T _A = 150° C)	I _{CBO}			-15 -4.0	nA μA
ON CHARACTERISTICS	·				
DC Current Gain (I _C = -10 μ A, V _{CE} = -5.0 V) (I _C = -2.0 mA, V _{CE} = -5.0 V)	h _{FE}	_ 200	150 290	_ 475	-
Collector – Emitter Saturation Voltage ($I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$) ($I_C = -100 \text{ mA}, I_B = -5.0 \text{ mA}$)	V _{CE(sat)}			-0.3 -0.65	V
Base – Emitter Saturation Voltage ($I_C = -10$ mA, $I_B = -0.5$ mA) ($I_C = -100$ mA, $I_B = -5.0$ mA)	V _{BE(sat)}		-0.7 -0.9		V
Base – Emitter On Voltage ($I_{C} = -2.0 \text{ mA}, V_{CE} = -5.0 \text{ V}$) ($I_{C} = -10 \text{ mA}, V_{CE} = -5.0 \text{ V}$)	V _{BE(on)}	-0.6		-0.75 -0.82	V
SMALL-SIGNAL CHARACTERISTICS	·				
Current – Gain – Bandwidth Product ($I_C = -10$ mA, $V_{CE} = -5.0$ Vdc, f = 100 MHz)	f _T	100	-	-	MHz
Output Capacitance ($V_{CB} = -10 \text{ V}, \text{ f} = 1.0 \text{ MHz}$)	C _{ob}	-	-	4.5	pF
Noise Figure (I _C = -0.2 mA, V _{CE} = -5.0 Vdc, R _S = 2.0 k Ω , f = 1.0 kHz, BW = 200 Hz)	NF	-	_	10	dB

TYPICAL NPN CHARACTERISTICS



TYPICAL PNP CHARACTERISTICS





SOT-563-6 1.60x1.20x0.55, 0.50P CASE 463A ISSUE J DATE 15 FEB 2024 NOTES: 1. DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018. 2. ALL DIMENSION ARE IN MILLIMETERS. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM 3 THICKNESS OF BASE MATERIAL. -A D MILLIMETERS А 6X L DIM В MIN NDM. MAX. m 0.50 0.55 А 0.60 ł 6 4 PIN b 0.17 0.22 0.27 F Н REFERENCE C 0.08 0.13 0.18 2 ັບ 1 3 D 1.50 1.60 1.70 E 1.20 1.30 1.10 -⊨ 6X b C ⊕ 0.08∭ A B е 0.50 BSC е Н 1.50 1.60 1.70 TOP VIEW SIDE VIEW L 0.10 0.20 0.30 1.30 6X 0.45 0.30 1.80 STYLE 1: STYLE 2 STYLE 3 PIN 1. EMITTER 1 2. BASE 1 PIN 1. EMITTER 1 PIN 1. CATHODE 1 2. CATHODE 1 2. EMITTER 2 3. COLLECTOR 2 3. BASE 2 3. ANDDE/ANDDE 2 4. EMITTER 2 4. COLLECTOR 2 4. CATHODE 2 0.50 5. BASE 2 5. BASE 1 5. CATHODE 2 6. COLLECTOR 1 PITCH 6. COLLECTOR 1 6. ANDDE/ANDDE 1 RECOMMENDED MOUNTING FOOTPRINT* STYLE 6: PIN 1. CATHODE 2. ANODE FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE STYLE 5 STYLE 4: 1. CATHODE 2. CATHODE PIN 1. COLLECTOR PIN 2. COLLECTOR 3. BASE 3. ANDDE 3. CATHODE 4. ANDDE 5. CATHODE 4. CATHODE 5. CATHODE 4. EMITTER MANUAL, SOLDERRM/D. 5, COLLECTOR 6. COLLECTOR 6. CATHODE 6. CATHODE GENERIC **MARKING DIAGRAM*** STYLE 7: STYLE 8 STYLE 9 PIN 1. CATHODE PIN 1. DRAIN PIN 1. SOURCE 1 2. ANDDE 2. DRAIN 2. GATE 1 XXM. 3. CATHODE 4. CATHODE 3. GATE 4. SDURCE 5. DRAIN 3. DRAIN 2 4. SDURCE 2 5. GATE 2 1 5. ANDDE 6. CATHODE 6. DRAIN 6. DRAIN 1 XX = Specific Device Code M = Month Code = Pb-Free Package STYLE 10: STYLE 11: *This information is generic. Please refer to PIN 1. CATHODE 1 PIN 1. EMITTER 2 device data sheet for actual part marking. 2. N/C 3. CATHODE 2 2. BASE 2 3. COLLECTOR 1 Pb-Free indicator, "G" or microdot "•", may 4. ANDDE 2 EMITTER 1 4. or may not be present. Some products may BASE 5. N/C 5. not follow the Generic Marking. 6. ANDDE 1 COLLECTOR 2 6. Electronic versions are uncontrolled except when accessed directly from the Document Repository. **DOCUMENT NUMBER:** 98AON11126D Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. **DESCRIPTION:** SOT-563-6 1.60x1.20x0.55, 0.50P PAGE 1 OF 1

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