

N-Channel JFET

効率が大幅にアップできる

• これは鉛フリーのデバイスです

● 最小梱包単位:3,000 pcs./reel

• JEITA, JEDEC: SC-74, SOT-26, SOT-457

25 V, 20 to 40 mA, 40 mS, Dual CPH6

● CPH6 にJ-FET を2素子内蔵した複合タイプであり、実装基板

● CPH6904 は、CPH3910 相当のチップを2 個同一ケース内に収

CPH6904

容したものである

製品と外形に伴う情報・ パッケージ名: CPH6

特長

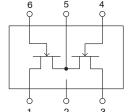


MARKING DIAGRAM

CASE 318BD



ELECTRICAL CONNECTION



- 1 : Drain 1 2 : NC 3 : Drain 2
- 3 : Drain 2 4 : Gate 2
- 5 : Source 1 / Source 2
- 6 : Gate 1

絶対最大定格 (T_A = 25°C)

記号	項目	条件	定格値	単位
V _{DSX}	ドレイン・ソース電圧		25	V
V _{GDS}	ゲート・ドレイン電圧		-25	V
I _G	ゲート電流		10	mA
I _D	ドレイン電流		50	mA
P_{D}	許容損失	1 unit	400	mW
P _T	全損失		700	mW
T _{ch}	チャネル温度		150	°C
T _{stg}	保存周囲温度		−55 ~ +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

(参考訳)

(マンス) 最大定格を超えるストレスは、デバイスにダメージを与える危険性がありま す。これらの定格値を超えた場合は、デバイスの機能性を損ない、ダメージ が生じたり、信頼性に影響を及ぼす危険性があります。

ORDERING INFORMATION

Device	Package	Shipping [†]
CPH6904-TL-E	CPH6	3 000 /
	(Pb-Free)	Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

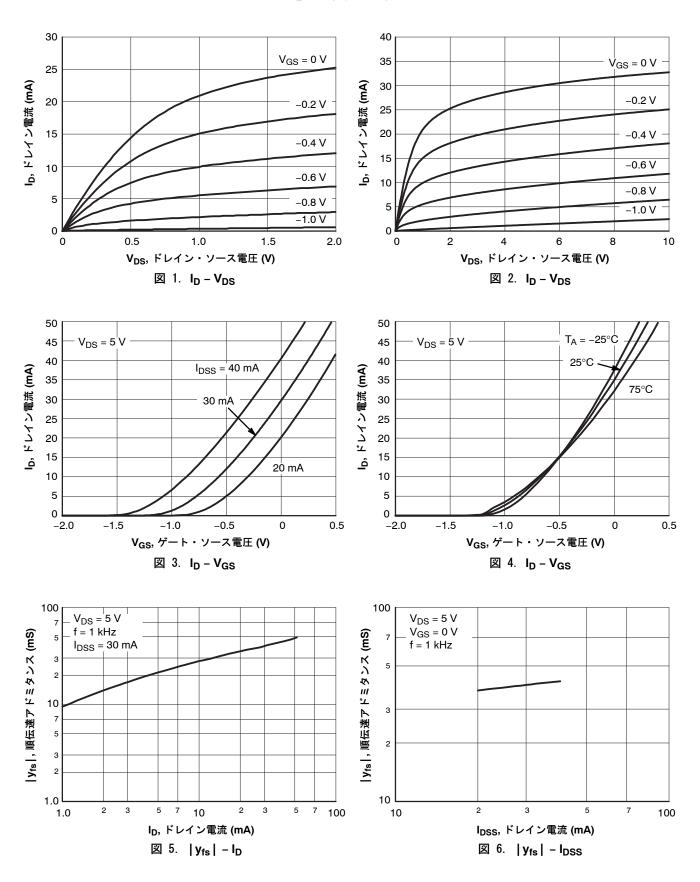
電気的特性 (T_A = 25°C)

記号	項目	条件	最小	標準値	最大	単位
V _{(BR)GDS}	ゲート・ドレイン降伏電圧	$I_G = -10 \mu A, V_{DS} = 0 V$	-25			V
I _{GSS}	ゲートしゃ断電流	$V_{GS} = -10 \text{ V}, V_{DS} = 0 \text{ V}$			-1.0	nA
V _{GS} (off)	ゲート・ソースしゃ断電圧	$V_{DS} = 5 \text{ V}, I_D = 100 \mu\text{A}$	-0.6	-1.2	-1.8	V
I _{DSS}	ドレイン電流	V _{DS} = 5 V, V _{GS} = 0 V	20.0		40.0	mA
y _{fs}	順伝達アドミタンス	$V_{DS} = 5 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ kHz}$	30	40		mS
C _{iss}	入力容量	V _{DS} = 5 V, V _{GS} = 0 V, f = 1 MHz		6.0		pF
C _{rss}	帰還容量	V _{DS} = 5 V, V _{GS} = 0 V, f = 1 MHz		2.3		pF
N _F	雑音指数	$V_{DS} = 5 \text{ V}, V_{GS} = 0 \text{ V}, f = 100 \text{ MHz}$		2.1	2.8	dB

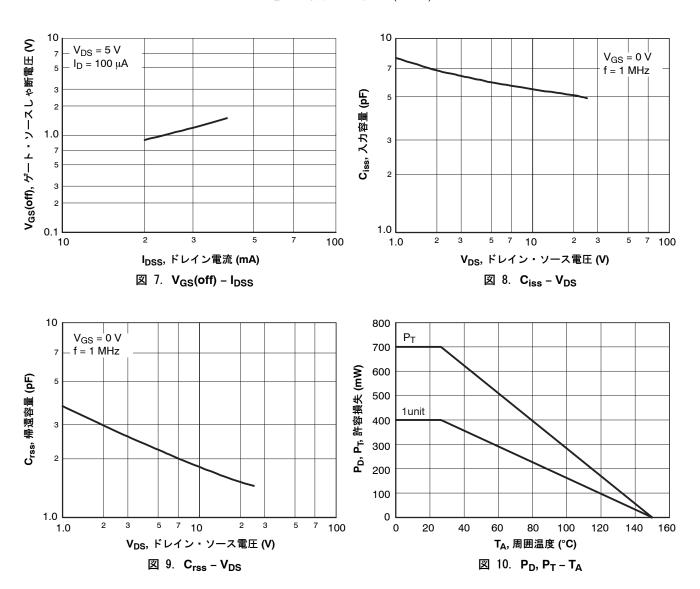
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. (参考訳)

(参考訳) 製品パラメータは、特別な記述が無い限り、記載されたテスト条件に対する電気的特性で示しています。異なる条件下で製品動作を行った時には、電気的特性で示している特性を得られない場合があります。

電気的特性の代表例



電気的特性の代表例 (つづき)



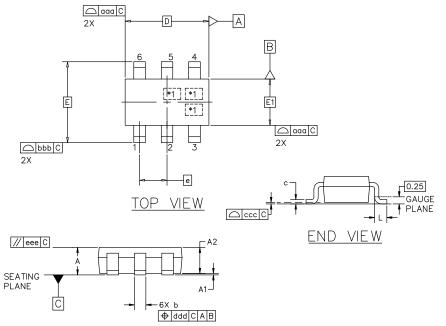






CPH6 2.90x1.60x0.90, 0.95P CASE 318BD **ISSUE A**

DATE 20 SEPT 2024



MILLIMETERS				
DIM	MIN	NOM	MAX	
Α	0.85	0.95	1.05	
A1	0.00	0.05	0.10	
A2	0.85	0.90	0.95	
b	0.30	0.40	0.50	
С	0.10	0.15	0.25	
D	2.90 BSC			
Е	2.80 BSC			
E1	1.60 BSC			
е	0.95 BSC			
L	0.10	0.20	0.30	
TOLER	ANCE FOR	M AND PC	SITION	
aaa	0.10			
bbb	0.15			
ссс	0.05			
ddd	0.10			
eee	0.10			

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018. CONTROLLING DIMENSION: MILLIMETERS

SIDE VIEW

- *1 IS FOR LOT INDICATION

GENERIC MARKING DIAGRAM*

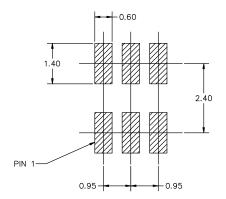


XXX = Specific Device Code

= Date Code M = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present. Some products may not follow the Generic Marking.



RECOMMENDED MOUNTING FOOTPRINT

For additional information on our Pb-Free strategy and soldering details, please downloadd the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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DESCRIPTION:	CPH6 2.90x1.60x0.90, 0.95P		PAGE 1 OF 1	

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