# Onsemi

## MOSFET – N-Channel, UniFET<sup>™</sup>, FRFET<sup>®</sup>

| V <sub>DS</sub> | R <sub>DS(ON)</sub> MAX | I <sub>D</sub> MAX |
|-----------------|-------------------------|--------------------|
| 500 V           | 260 m $\Omega$ @ 10 V   | 20 A               |

## **500 V, 20 A, 260 m**Ω

FDP20N50F / FDPF20N50FT

### Description

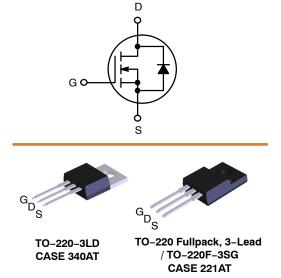
UniFET MOSFET is onsemi's high voltage MOSFET family based on planar stripe and DMOS technology. This MOSFET is tailored to reduce on-state resistance, and to provide better switching performance and higher avalanche energy strength. The body diode's reverse recovery performance of UniFET FRFET MOSFET has been enhanced by lifetime control. Its t<sub>rr</sub> is less than 100 ns and the reverse dv/dt immunity is 15 V/ns while normal planar MOSFET's have over 200 ns and 4.5 V/ns respectively. Therefore, it can remove additional component and improve system reliability in certain applications in which the performance of MOSFET's body diode is significant. This device family is suitable for switching power converter applications such as power factor correction (PFC), flat panel display (FPD) TV power, ATX and electronic lamp ballasts.

### Features

- $R_{DS(on)} = 210 \text{ m}\Omega \text{ (Typ.)} @ V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$
- Low Gate Charge (Typ. 50 nC)
- Low C<sub>rss</sub> (Typ. 27 pF)
- 100% Avalanche Tested
- Improve dv/dt Capability
- These Devices are Pb-Free and are RoHS Compliant

## Applications

- LCD/LED TV
- Lighting
- Uninterruptible Power Supply
- AC–DC Power Supply



## MARKING DIAGRAM

| FDP<br>20N50F<br>AYWWZZ  |           | FDPF20<br>N50FT<br>AYWWZZ                                |
|--------------------------|-----------|--|
| 20N50F,<br>F20N50FT<br>/ | = Assembl | Device Code<br>ly Location<br>de (Year & Week)<br>ly Lot |

#### **ORDERING INFORMATION**

| Device      | e Package Shippin |                   |
|-------------|-------------------|-------------------|
| FDP20N50F   | TO-220            | 1000 Units / Tube |
| FDPF20N50FT | TO-220F           | 1000 Units / Tube |

| Symbol                            | Para  | meter   | FDP20N50F  | FDPF20N50FT  | Unit      |
|-----------------------------------|---|---|------------|--------------|-----------|
| V <sub>DSS</sub>                  | Drain to Source Voltage   |   | 5          | 500          |           |
| V <sub>GSS</sub>                  | Gate to Source Voltage  |   | ±          | ±30          |           |
| I <sub>D</sub>                    | Drain Current –   | – Continuous (T <sub>C</sub> = 25°C)<br>– Continuous (T <sub>C</sub> = 100°C) | 20<br>12.9 | 20*<br>12.9* | А         |
| I <sub>DM</sub>                   | Drain Current   | – Pulsed (Note 1)   | 80         | 80*          | А         |
| E <sub>AS</sub>                   | Single Pulsed Avalanche Energy (Note 2)                                 |   | 11         | 1110         |           |
| I <sub>AR</sub>                   | Avalanche Current (Note 1)  |   | 20         |              | А         |
| E <sub>AR</sub>                   | Repetitive Avalanche Energy (Note 1)                                    |   | 25         |              | mJ        |
| dv/dt                             | Peak Diode Recovery dv/dt (Note 3)                                      |   | 2          | 0            | V/ns      |
| P <sub>D</sub>                    | Power Dissipation   | (T <sub>C</sub> = 25°C)<br>– Derate Above 25°C                                | 250<br>2.0 | 38.5<br>0.3  | W<br>W/°C |
| T <sub>J</sub> , T <sub>STG</sub> | Operating and Storage Temperature Range                                 |   | –55 to     | o +150       | °C        |
| ΤL                                | Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Second 300 |   | 00         | °C           |           |

#### MOSFET MAXIMUM RATINGS (T<sub>C</sub> = 25°C unless otherwise noted)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. \*Drain current limited by maximum junction temperature 1. Repetitive Rating: Pulse width limited by maximum junction temperature. 2. L = 5 mH, I<sub>AS</sub> = 20 A, V<sub>DD</sub> = 50 V, R<sub>G</sub> = 25  $\Omega$ , Starting T<sub>J</sub> = 25°C 3. I<sub>SD</sub> ≤ 20 A, di/dt ≤ 200 A/µs, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, Starting T<sub>J</sub> = 25°C

#### **THERMAL CHARACTERISTICS**

 $\mathsf{Q}_{\mathsf{gs}}$ 

Q<sub>gd</sub>

| Symbol | Parameter                                     | FDP20N50F | FDPF20N50FT | Unit |
|--------|---|-----------|-------------|------|
| Rejc   | Thermal Resistance, Junction-to-Case, Max.    | 0.5       | 3.3         | °C/W |
| Recs   | Thermal Resistance, Case-to-sink, Typ.        | 0.5       | -           | °C/W |
| Reja   | Thermal Resistance, Junction-to-Ambient, Max. | 62.5      | 62.5        | °C/W |

#### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = $25^{\circ}$ C unless otherwise noted)

Gate to Source Gate Charge

Gate to Drain "Miller" Charge

| $\begin{array}{c c c c c c c c c c c c c c c c c c c $  | Parameter  | Symbol                            | Test Condition   | Min | Тур  | Max  | Unit |
|---|--|-----------------------------------|--|-----|------|------|------|
| $ \begin{array}{c c c c c c c c c c c c c c c c c c c $   | OFF CHARAC   | TERISTICS                         |  |     |      |      |      |
| $\begin{array}{c c c c c c c c c c c c c c c c c c c $  | BV <sub>DSS</sub>  | Drain-Source Breakdown Voltage    | $I_D$ = 250 $\mu$ A, $V_{GS}$ = 0 V, $T_J$ = 25°C              | 500 | -    | -    | V    |
| $\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$  | $\Delta \text{BV}_{\text{DSS}} / \Delta \text{T}_{\text{J}}$ |                                   | $I_D = 250 \ \mu\text{A}$ , Referenced to $25^{\circ}\text{C}$ | -   | 0.7  | _    | V/°C |
| IGSSGate-Body Leakage Current $V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$ $\pm 1$ ON CHARACTERISTICS $V_{GS}(th)$ Gate Threshold Voltage $V_{GS} = V_{DS}, I_D = 250 \mu A$ $3.0$ - $5$ $R_{DS}(on)$ Static Drain-Source On Resistance $V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$ - $0.22$ $0.$ $g_{FS}$ Forward Transconductance $V_{DS} = 20 \text{ V}, I_D = 10 \text{ A}$ - $25$ $-$ DYNAMIC CHARACTERISTICS $C_{iss}$ Input Capacitance $V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$ - $2550  33$ $C_{oss}$ Output Capacitance $ 350  44$ $C_{rss}$ Reverse Transfer Capacitance- $27  44$  | I <sub>DSS</sub>   | Zero Gate Voltage Drain Current   | $V_{DS} = 500 \text{ V}, V_{GS} = 0 \text{ V}$                 | -   | -    | 10   | μA   |
| Note that the second problem is the s |  |                                   | $V_{DS}$ = 400 V, $T_{C}$ = 125°C                              | -   | -    | 100  |      |
| $\begin{array}{c c c c c c c c c c c c c c c c c c c $  | I <sub>GSS</sub>   | Gate-Body Leakage Current         | $V_{GS} = \pm 30$ V, $V_{DS} = 0$ V                            | _   | -    | ±100 | nA   |
| $\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$  | ON CHARACT   | ERISTICS                          |  |     |      |      |      |
| $g_{FS}$ Forward Transconductance $V_{DS} = 20 \text{ V}, I_D = 10 \text{ A}$ -25DYNAMIC CHARACTERISTICS $C_{iss}$ Input Capacitance $V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$ -255033 $C_{oss}$ Output Capacitance $-$ 35044 $C_{rss}$ Reverse Transfer Capacitance-2744  | V <sub>GS(th)</sub>  | Gate Threshold Voltage            | $V_{GS} = V_{DS}, I_D = 250 \ \mu A$                           | 3.0 | -    | 5.0  | V    |
| DYNAMIC CHARACTERISTICS $C_{iss}$ Input Capacitance $C_{oss}$ Output Capacitance $C_{rss}$ Reverse Transfer Capacitance   | R <sub>DS(on)</sub>  | Static Drain-Source On Resistance | $V_{GS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ A}$          | -   | 0.22 | 0.26 | Ω    |
| $ \begin{array}{c c} C_{iss} & \text{Input Capacitance} \\ \hline C_{oss} & \text{Output Capacitance} \\ \hline C_{rss} & \text{Reverse Transfer Capacitance} \end{array} \begin{array}{c c} V_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz} \\ \hline - & 350 & 44 \\ \hline - & 27 & 4 \\ \hline \end{array} $   | <b>9</b> FS  | Forward Transconductance          | $V_{DS} = 20 \text{ V}, \text{ I}_{D} = 10 \text{ A}$          | -   | 25   | -    | S    |
| CossOutput Capacitance-35044CrssReverse Transfer Capacitance-274  | DYNAMIC CHA  | ARACTERISTICS                     |  |     |      |      |      |
| C <sub>rss</sub> Reverse Transfer Capacitance – 27 4  | C <sub>iss</sub>   | Input Capacitance                 | $V_{DS}$ = 25 V, $V_{GS}$ = 0 V, f = 1 MHz                     | _   | 2550 | 3390 | pF   |
|   | C <sub>oss</sub>   | Output Capacitance                |  | -   | 350  | 465  | pF   |
| $Q_{g(tot)}$ Total Gate Charge at 10 V $V_{DS} = 400 \text{ V}, I_D = 20 \text{ A}, - 50 \text{ G}$   | C <sub>rss</sub>   | Reverse Transfer Capacitance      |  | _   | 27   | 40   | pF   |
|   | Q <sub>g(tot)</sub>  | Total Gate Charge at 10 V         | V <sub>DS</sub> = 400 V, I <sub>D</sub> = 20 A,                | -   | 50   | 65   | nC   |

V<sub>GS</sub> = 10 V (Note 4)

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14

20

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nC

nC

## **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = $25^{\circ}$ C unless otherwise noted) (continued)

| Parameter           | Symbol   | Test Condition  | Min | Тур | Max | Unit |
|---------------------|--|---|-----|-----|-----|------|
| SWITCHING C         | HARACTERISTICS                                       |   |     |     |     |      |
| t <sub>d(on)</sub>  | Turn-On Delay Time                                   | $V_{DD} = 250 \text{ V}, \text{ I}_{D} = 20 \text{ A},$                         | -   | 45  | 100 | ns   |
| t <sub>r</sub>      | Turn-On Rise Time                                    | R <sub>G</sub> = 25 Ω<br>(Note 4)   | -   | 120 | 250 | ns   |
| t <sub>d(off)</sub> | Turn-Off Delay Time                                  | ·   | -   | 100 | 210 | ns   |
| t <sub>f</sub>      | Turn-Off Fall Time                                   |   | -   | 60  | 130 | ns   |
| DRAIN-SOUR          | CE DIODE CHARACTERISTICS                             |   |     |     |     |      |
| I <sub>S</sub>      | Maximum Continuous Drain to Source Dio               | de Forward Current  | -   | -   | 20  | А    |
| I <sub>SM</sub>     | Maximum Pulsed Drain to Source Diode Forward Current |   | -   | -   | 80  | А    |
| V <sub>SD</sub>     | Drain to Source Diode Forward Voltage                | $V_{GS} = 0 \text{ V}, \text{ I}_{SD} = 20 \text{ A}$                           | -   | -   | 1.5 | V    |
| t <sub>rr</sub>     | Reverse Recovery Time                                | V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 20 A<br>dI <sub>F</sub> /dt = 100 A/μs | -   | 154 | _   | ns   |
| Q <sub>rr</sub>     | Reverse Recovery Charge                              |   | -   | 0.5 | _   | μC   |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially Independent of Operating Temperature Typical Characteristics

## **TYPICAL PERFORMANCE CHARACTERISTICS**

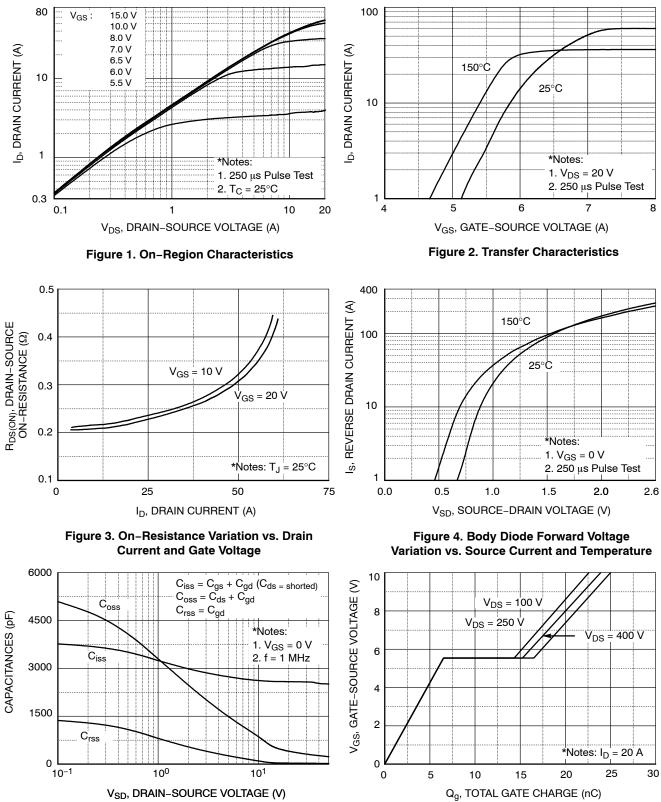
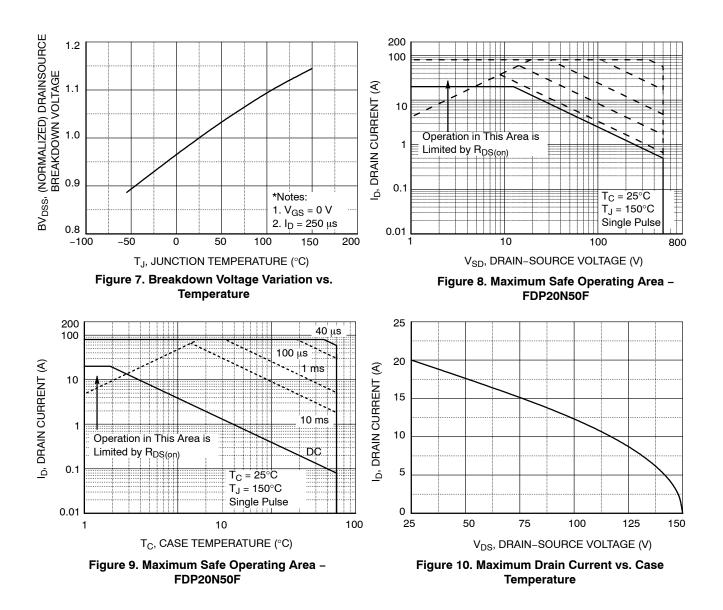


Figure 5. Capacitance Characteristics

Figure 6. Gate Charge Characteristics

## TYPICAL CHARACTERISTICS (continued)



## Typical Performance Characteristics (continued)

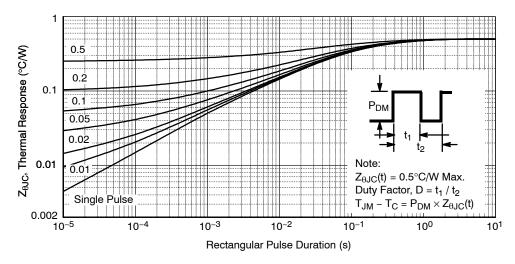


Figure 11. Transient Thermal Response Curve for FDP20N50F

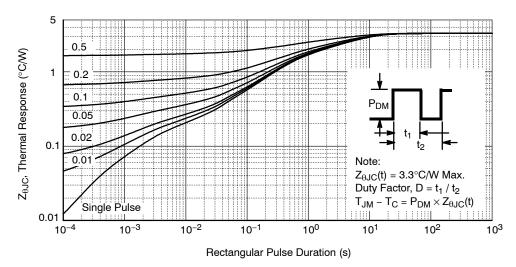
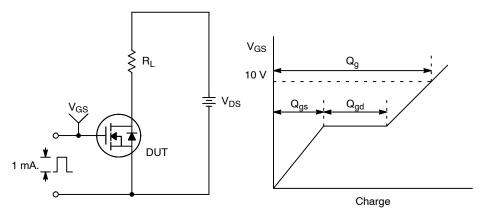


Figure 12. Transient Thermal Response Curve for FDPF20N50FT





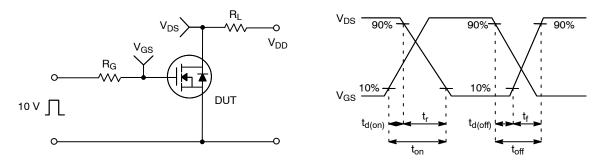
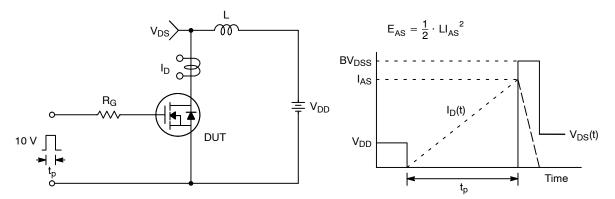


Figure 14. Resistive Switching Test Circuit & Waveforms





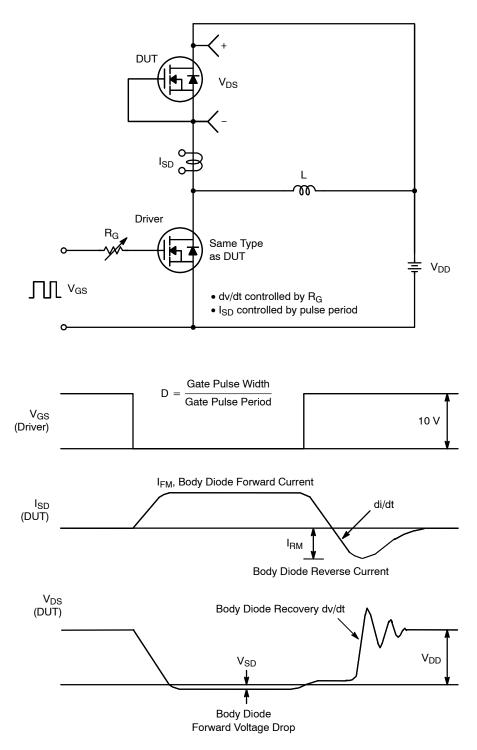
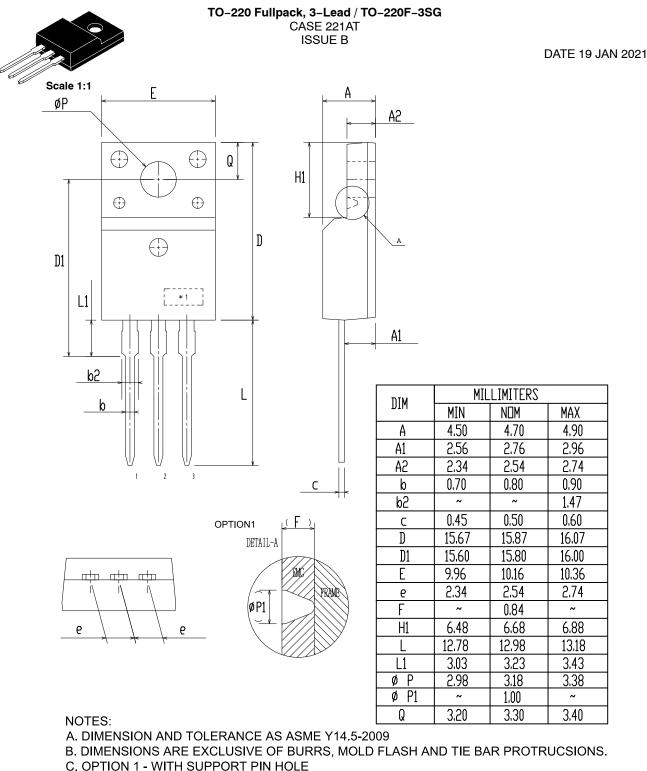


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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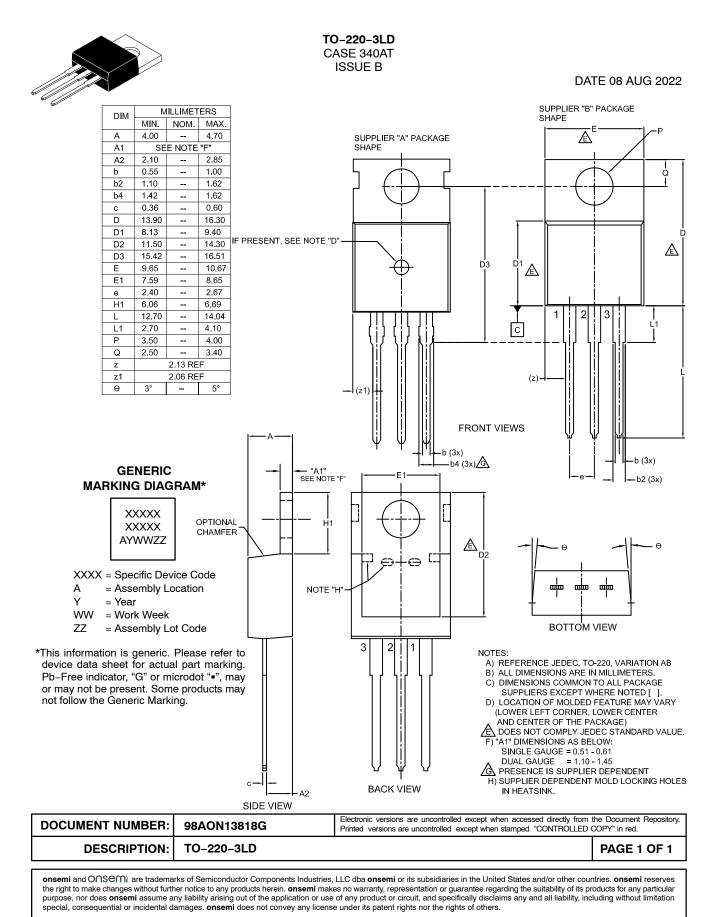


OPTION 2 - NO SUPPORT PIN HOLE

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|                  |  |  |             |  |

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