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May 2015



FFP15S60S — STEALTH™ II Diode

FFP15S60S

15 A, 600 V, STEALTH™ II Diode

Features

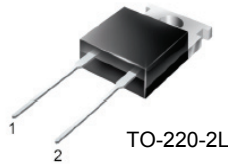
- Stealth Recovery $T_{rr} = 35 \text{ ns}$ (@ $I_F = 15 \text{ A}$)
- Max Forward Voltage, $V_F = 2.6 \text{ V}$ (@ $T_C = 25^\circ\text{C}$)
- 600 V Reverse Voltage and High Reliability
- Improved dv/dt Capability
- RoHS compliant

Description

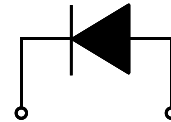
The FFP15S60S is a STEALTH™ II diode with soft recovery characteristics. It is silicon nitride passivated ion-implanted epitaxial planar construction. This device is intended for use as freewheeling of boost diode in switching power supplies and other power switching applications. Their low stored charge and hyperfast soft recovery minimize ringing and electrical noise in many power switching circuits reducing power loss in the switching transistors.

Applications

- General Purpose
- SMPS, Power Switching Circuits
- Boost Diode in Continuous Mode Power Factor Corrections



1. Cathode 2. Anode



1. Cathode 2. Anode

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	FFP15S60S	Unit
V_{RRM}	Peak Repetitive Reverse Voltage	600	V
V_{RWM}	Working Peak Reverse Voltage	600	V
V_R	DC Blocking Voltage	600	V
$I_{F(AV)}$	Average Rectified Forward Current @ $T_C = 123^\circ\text{C}$	15	A
I_{FSM}	Non-repetitive Peak Surge Current 60Hz Single Half-Sine Wave	150	A
T_J, T_{STG}	Operating and Storage Temperature Range	-65 to +175	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	FFP15S60S	Unit
$R_{\theta JC}$	Maximum Thermal Resistance, Junction to Case	1.3	$^\circ\text{C/W}$

Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FFP15S60STU	F15S60S	TO-220-2L	Tube	N/A	N/A	50

Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Min.	Typ.	Max.	Unit
V_{F1}	$I_F = 15\text{ A}$ $I_F = 15\text{ A}$	-	2.1 1.6	2.6 -	V
I_{R1}	$V_R = 600\text{ V}$ $V_R = 600\text{ V}$	-	-	100 500	μA
t_{rr}	$I_F = 1\text{ A}$, $di/dt = 100\text{ A}/\mu\text{s}$, $V_R = 30\text{ V}$	-	21	30	ns
t_{rr} I_{rr} S factor Q_{rr}	$I_F = 15\text{ A}$, $di/dt = 200\text{ A}/\mu\text{s}$, $V_R = 390\text{ V}$	-	23 2.5 0.7 29	35 - - -	ns A nC
t_{rr} I_{rr} S factor Q_{rr}	$I_F = 15\text{ A}$, $di/dt = 200\text{ A}/\mu\text{s}$, $V_R = 390\text{ V}$	-	55 4.3 1.1 118	- - - -	ns A nC
W_{AVL}	Avalanche Energy ($L = 40\text{ mH}$)	20	-	-	mJ

Notes:

1: Pulse: Test Pulse width = 300 μs , Duty Cycle = 2%

Test Circuit and Waveforms

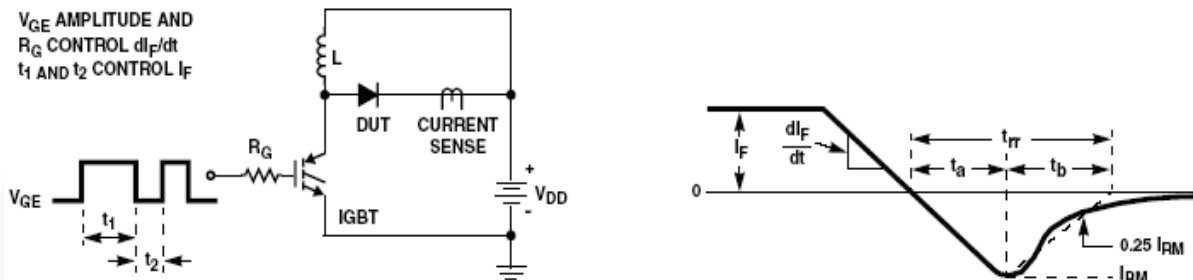


Figure 1. Diode Reverse Recovery Test Circuit & Waveform

$L = 40\text{mH}$
 $R < 0.1\Omega$
 $V_{DD} = 50\text{V}$

$E_{AVL} = 1/2LI^2 [V_{R(AVL)}/(V_{R(AVL)} - V_{DD})]$
 $Q1 = \text{IGBT } (BV_{CES} > \text{DUT } V_{R(AVL)})$

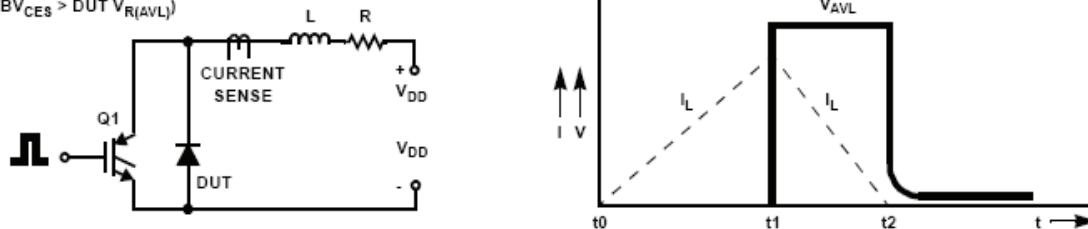


Figure 2. Unclamped Inductive Switching Test Circuit & Waveform

Typical Performance Characteristics

Figure 3. Typical Forward Voltage Drop vs. Forward Current

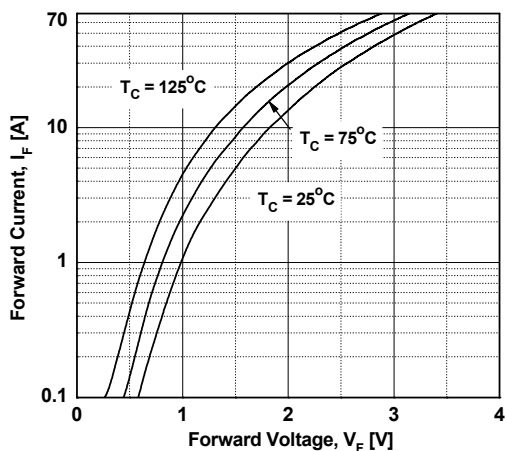


Figure 4. Typical Reverse Current vs. Reverse Voltage

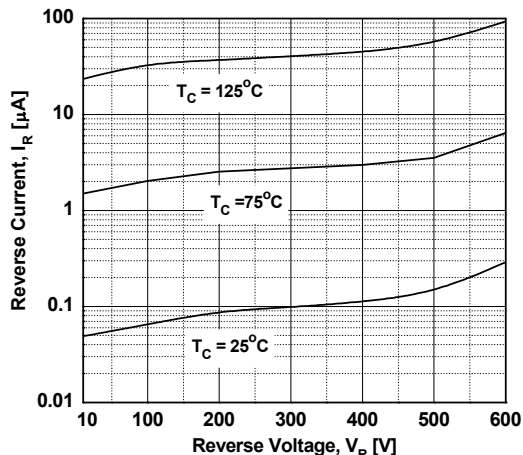


Figure 5. Typical Junction Capacitance

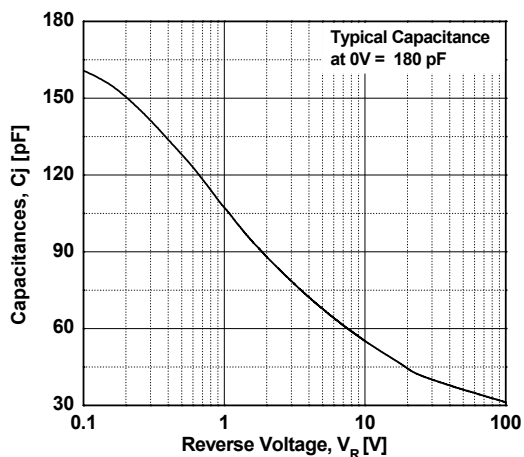


Figure 6. Typical Reverse Recovery Time vs. di/dt

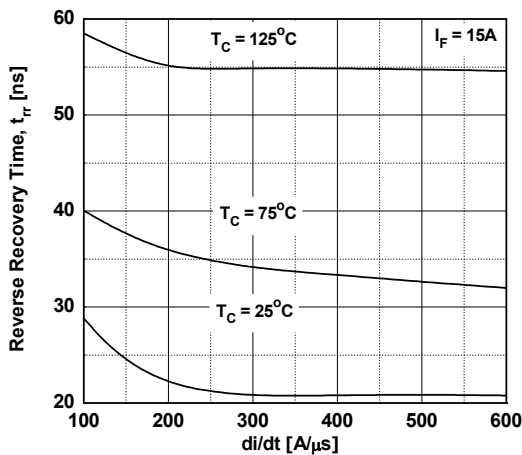


Figure 7. Typical Reverse Recovery Current vs. di/dt

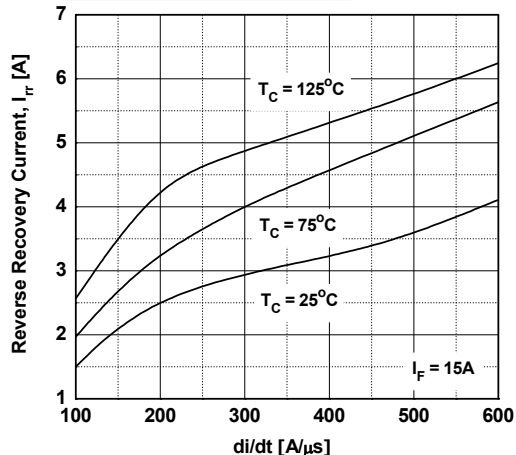
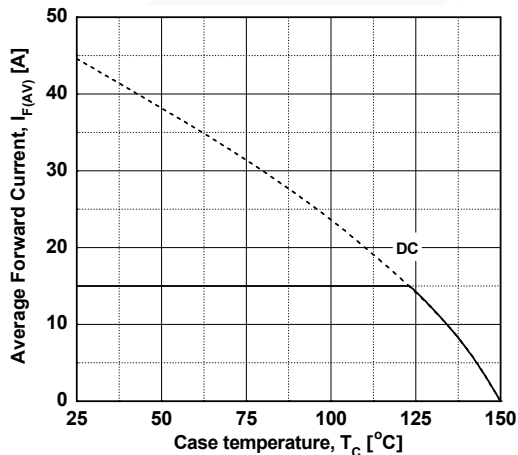
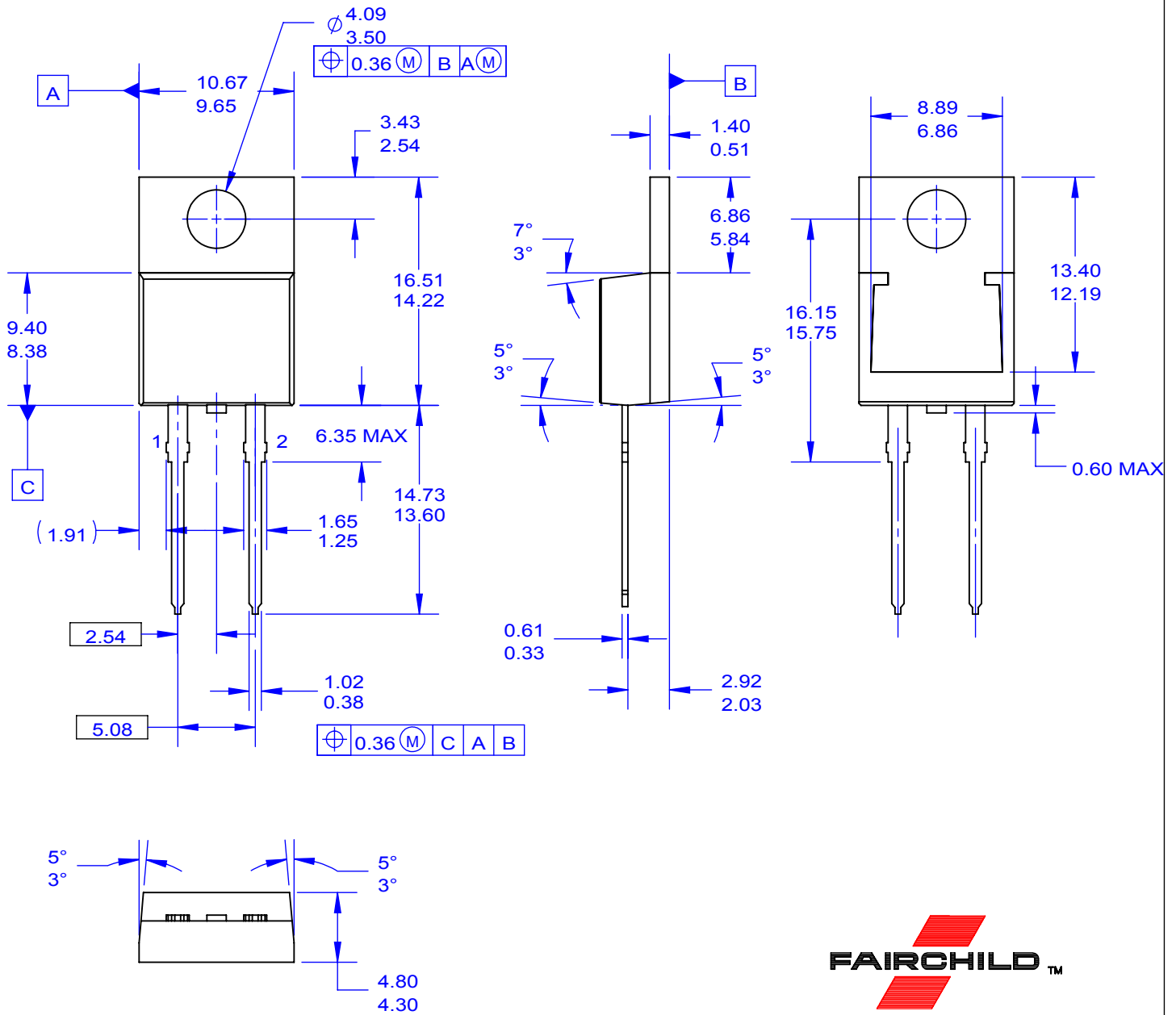


Figure 8. Forward Current Derating Curve





NOTES:

- A. PACKAGE REFERENCE: JEDEC TO220,ISSUE K, VARIATION AC,DATED APRIL 2002.
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