## MMSD103T1G, SMMSD103T1G

## High Voltage Switching Diode

## Features

- AEC-Q101 Qualified and PPAP Capable
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These Devices are $\mathrm{Pb}-$ Free, Halogen Free/BFR Free and are RoHS Compliant*


## MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Continuous Reverse Voltage | $\mathrm{V}_{\mathrm{R}}$ | 250 | V |
| Peak Forward Current | $\mathrm{I}_{\mathrm{F}}$ | 200 | mA |
| Peak Forward Surge Current | $\mathrm{I}_{\mathrm{FM}}$ (surge) | 625 | mA |

## THERMAL CHARACTERISTICS

| Characteristic | Symbol | Value | Unit |
| :---: | :---: | :---: | :---: |
| Forward Power Dissipation, FR-5 Board (Note 1) @ $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ Derate above $25^{\circ} \mathrm{C}$ | $\mathrm{P}_{\mathrm{F}}$ | $\begin{gathered} 400 \\ 3.2 \end{gathered}$ | $\underset{\mathrm{mW} /{ }^{\circ} \mathrm{C}}{\mathrm{~mW}}$ |
| Thermal Resistance, Junction-to-Case | $\mathrm{R}_{\text {өJL }}$ | 174 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Thermal Resistance, Junction-to-Ambient | $\mathrm{R}_{\text {өJA }}$ | 492 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Junction and Storage Temperature Range | $\mathrm{T}_{\mathrm{J},} \mathrm{T}_{\text {stg }}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. $\mathrm{FR}-5=1.0 \times 0.75 \times 0.062 \mathrm{in}$.

ON Semiconductor ${ }^{\circledR}$
http://onsemi.com


SOD-123
CASE 425
STYLE 1

| 10 |  |
| :---: | :---: |
| Cathode | 2 |
| Anode |  |

MARKING DIAGRAM


JS = Device Code
M = Date Code

- = Pb-Free Package
(Note: Microdot may be in either location)

ORDERING INFORMATION

| Device | Package | Shipping $^{\dagger}$ |
| :---: | :---: | :---: |
| MMSD103T1G | SOD-123 <br> (Pb-Free) | $3,000 /$ Tape \& Reel |
| SMMSD103T1G | SOD-123 <br> (Pb-Free) | $3,000 /$ Tape \& Reel |

$\dagger$ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.
*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS $\left(\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}\right.$ unless otherwise noted)

| Characteristic | Symbol | Min | Max | Unit |
| :---: | :---: | :---: | :---: | :---: |
| OFF CHARACTERISTICS |  |  |  |  |
| $\begin{aligned} & \text { Reverse Voltage Leakage Current } \\ & \left(V_{R}=200 \mathrm{~V}\right) \\ & \left(\mathrm{V}_{R}=200 \mathrm{~V}, \mathrm{~T}_{J}=150^{\circ} \mathrm{C}\right) \end{aligned}$ | $\mathrm{I}_{\mathrm{R}}$ | - | $\begin{aligned} & 1.0 \\ & 100 \end{aligned}$ | $\mu \mathrm{A}$ |
| Reverse Breakdown Voltage $\left(\mathrm{I}_{\mathrm{BR}}=100 \mu \mathrm{~A}\right)$ | $\mathrm{V}_{\text {(BR) }}$ | 250 | - | V |
| $\begin{gathered} \hline \text { Forward Voltage } \\ \left(I_{F}=100 \mathrm{~mA}\right) \\ \left(I_{F}=200 \mathrm{~mA}\right) \end{gathered}$ | $\mathrm{V}_{\mathrm{F}}$ | - | $\begin{aligned} & 1000 \\ & 1250 \end{aligned}$ | mV |
| Diode Capacitance $\left(\mathrm{V}_{\mathrm{R}}=0, \mathrm{f}=1.0 \mathrm{MHz}\right)$ | $\mathrm{C}_{\mathrm{D}}$ | - | 5.0 | pF |
| $\begin{aligned} & \text { Reverse Recovery Time } \\ & \quad\left(I_{F}=I_{R}=30 \mathrm{~mA}, R_{L}=100 \Omega\right) \end{aligned}$ | $\mathrm{trr}_{\text {r }}$ | - | 50 | ns |



Notes: 1. A $2.0 \mathrm{k} \Omega$ variable resistor adjusted for a Forward Current ( $l_{F}$ ) of 30 mA .
2. Input pulse is adjusted so $\mathrm{I}_{\mathrm{R}(\text { peak })}$ is equal to 30 mA .
3. $t_{p}$ " $t_{r r}$

Figure 1. Recovery Time Equivalent Test Circuit


Figure 2. Forward Voltage


Figure 3. Reverse Leakage


Figure 4. Diode Capacitance

SOD-123 2L 1.60x2.69x1.16
CASE 425
ISSUE H
DATE 29 FEB 2024

TIP VIEW

NDTES:

1. DIMENSIUN AND TQLERANCING PER ASME Y14.5M, 2018
2. CDNTRDLLING DIMENSIDN: MILLIMETERS

|  | MILLIMETER |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| DIM | MIN. | NDM. | MAX. |  |
| A | 0.94 | 1.17 | 1.35 |  |
| A1 | 0.00 | 0.05 | 0.10 |  |
| A2 | 1.16 REF. |  |  |  |
| b | 0.51 | 0.61 | 0.71 |  |
| C | - | - | 0.15 |  |
| D | 1.40 | 1.60 | 1.80 |  |
| E | 2.54 | 2.69 | 2.84 |  |
| H | 3.56 | 3.68 | 3.86 |  |
| L | 0.25 REF. |  |  |  |
| ¿ | $0^{\circ}$ |  |  |  |
| O1 | $0^{\circ}$ | $10^{\circ}$ |  |  |




## GENERIC

 MARKING DIAGRAM*

XXX = Specific Device Code
M = Date Code

- = Pb-Free Package
(Note: Microdot may be in either location)
*This information is generic. Please refer to device data sheet for actual part marking. $\mathrm{Pb}-$ Free indicator, " $G$ " or microdot " r ", may or may not be present. Some products may not follow the Generic Marking.

STYLE 1 :
PIN 1. CATHODE 2. ANODE

RECIMMENDED MDUNTING FIDTPRINT *For additional information on or Pb -Free strategy and soldering details, please download the GN Semiconductor Soldering and Mounting Techniques Reference manual SLDERRM/D.

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DESCRIPTION: $\quad$ SOD-123 2L 1.60x2.69x1.16
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