

# LDO Regulator - Ultra-Low $I_Q$ , CMOS

150 mA

## NCV8170

The NCV8170 series of CMOS low dropout regulators are designed specifically for continuous on battery-powered applications which require ultra-low quiescent current. The ultra-low consumption of typ. 500 nA ensures long battery life and dynamic transient boost feature improves device transient response for wireless communication applications. The device is available in small 1 × 1 mm xDFN4 and SOT-563 packages.

### Features

- Operating Input Voltage Range: 2.2 V to 5.5 V
- Output Voltage Range: 1.2 V to 3.6 V (0.1 V Steps)
- Ultra-Low Quiescent Current Typ. 0.5  $\mu$ A
- Low Dropout: 170 mV Typ. at 150 mA
- High Output Voltage Accuracy  $\pm 1\%$
- Stable with Ceramic Capacitors 1  $\mu$ F
- Over-Current Protection
- Thermal Shutdown Protection
- NCV8170A for Active Discharge Option
- Available in Small 1 × 1 mm xDFN4 and SOT-563 Packages
- NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

### Typical Applications

- Telematics and Infotainment Systems
- Automotive Keyless Entry Systems
- ADAS Camera Modules
- Navigation Systems

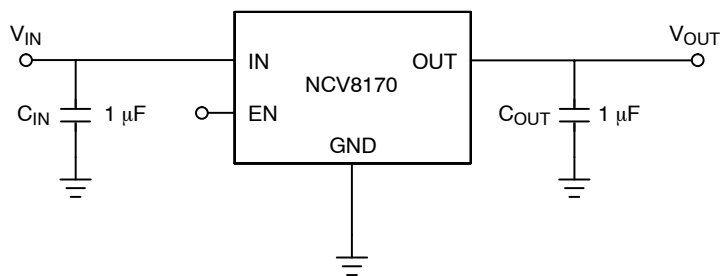
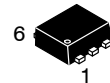


Figure 1. Typical Application Schematic



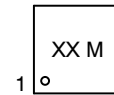
XDFN4  
MX SUFFIX  
CASE 711AJ



SOT-563  
XV SUFFIX  
CASE 463A

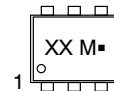
### MARKING DIAGRAMS

#### XDFN4



XX = Specific Device Code  
M = Date Code

#### SOT-563



XX = Specific Device Code  
M = Month Code  
■ = Pb-Free Package

### ORDERING INFORMATION

See detailed ordering, marking and shipping information on page 19 of this data sheet.

## PIN FUNCTION DESCRIPTION

Pin No. XDFN4	Pin No. SOT-563	Pin Name	Description
4	1	IN	Power Supply Input Voltage
2	2	GND	Power Supply Ground
3	6	EN	Chip Enable Pin (Active "H")
1	3	OUT	Output Pin
EPAD		EPAD	Internally Connected to GND
	4	NC	No Connect
	5	GND	Power Supply Ground

## ABSOLUTE MAXIMUM RATINGS

Symbol	Rating	Value	Unit
$V_{IN}$	Input Voltage (Note 1)	6.0	V
$V_{OUT}$	Output Voltage	-0.3 to $V_{IN} + 0.3$	V
$V_{CE}$	Chip Enable Input	-0.3 to 6.0	V
$T_{J(MAX)}$	Maximum Junction Temperature	125	°C
$T_{STG}$	Storage Temperature	-55 to 150	°C
ESD <sub>HBM</sub>	ESD Capability, Human Body Model (Note 2)	2000	V
ESD <sub>MM</sub>	ESD Capability, Machine Model (Note 2)	200	V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Refer to ELECTRICAL CHARACTERISTICS and APPLICATION INFORMATION for Safe Operating Area.

2. This device series incorporates ESD protection and is tested by the following methods:

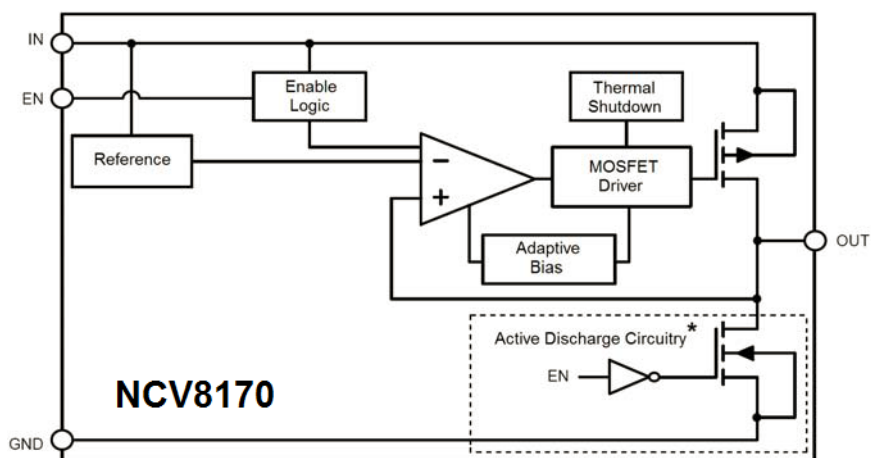
ESD Human Body Model tested per AEC-Q100-002 (EIA/JESD22-A114)

ESD Machine Model tested per AEC-Q100-003 (EIA/JESD22-A115)

Latchup Current Maximum Rating tested per JEDEC standard: JESD78

## THERMAL CHARACTERISTICS

Symbol	Rating	Value	Unit
$R_{\theta JA}$	Thermal Characteristics, Thermal Resistance, Junction-to-Air XDFN4 1 × 1 mm SOT-563	250 200	°C/W



\* Active discharge feature is present only in NCV8170AxxvvvTyG devices

Figure 2. Simplified Block Diagram

**ELECTRICAL CHARACTERISTICS – VOLTAGE VERSION 1.2 V**

( $-40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$ ;  $V_{IN} = 2.5\text{ V}$ ;  $I_{OUT} = 1\text{ mA}$ ,  $C_{IN} = C_{OUT} = 1.0\text{ }\mu\text{F}$ , unless otherwise noted. Typical values are at  $T_A = +25^{\circ}\text{C}$ .) (Note 3)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
$V_{IN}$	Operating Input Voltage		2.2	–	5.5	V
$V_{OUT}$	Output Voltage	$T_A = +25^{\circ}\text{C}$	1.188	1.2	1.212	V
		$-40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$	1.176	1.2	1.224	
$\text{Line}_{\text{Reg}}$	Line Regulation	$2.5\text{ V} < V_{IN} \leq 5.5\text{ V}$ , $I_{OUT} = 1\text{ mA}$	–	0.05	0.20	%/V
$\text{Load}_{\text{Reg}}$	Load Regulation	$0\text{ mA} < I_{OUT} \leq 150\text{ mA}$ , $V_{IN} = 2.5\text{ V}$	–20	1	20	mV
$V_{DO}$	Dropout Voltage	(Note 4)	–	–	–	mV
$I_{OUT}$	Output Current	(Note 5)	150	–	–	mA
$I_{SC}$	Short Circuit Current Limit	$V_{OUT} = 0\text{ V}$	–	225	–	mA
$I_Q$	Quiescent Current	$I_{OUT} = 0\text{ mA}$	–	0.5	0.9	$\mu\text{A}$
$I_{STB}$	Standby Current	$V_{EN} = 0\text{ V}$ , $T_J = 25^{\circ}\text{C}$	–	0.1	0.5	$\mu\text{A}$
$V_{ENH}$	EN Pin Threshold Voltage	EN Input Voltage “H”	1.2	–	–	V
$V_{ENL}$	EN Pin Threshold Voltage	EN Input Voltage “L”	–	–	0.4	V
$I_{EN}$	EN Pin Current	$V_{EN} \leq V_{IN} \leq 5.5\text{ V}$ (Note 6)	–	10	–	nA
PSRR	Power Supply Rejection Ratio	$f = 1\text{ kHz}$ , $V_{IN} = 2.2\text{ V} + 200\text{ mVpp}$ Modulation	–	57	–	dB
		$I_{OUT} = 150\text{ mA}$ $I_{OUT} = 10\text{ mA}$	– 63	– –	– –	
$V_{NOISE}$	Output Noise Voltage	$V_{IN} = 5.5\text{ V}$ , $I_{OUT} = 1\text{ mA}$ , $f = 100\text{ Hz to } 1\text{ MHz}$ , $C_{OUT} = 1\text{ }\mu\text{F}$	–	85	–	$\mu\text{V}_{\text{rms}}$
$R_{LOW}$	Active Output Discharge Resistance (A option only)	$V_{IN} = 5.5\text{ V}$ , $V_{EN} = 0\text{ V}$ (Note 6)	–	100	–	$\Omega$
$T_{SD}$	Thermal Shutdown Temperature	Temperature Increasing from $T_J = +25^{\circ}\text{C}$ (Note 6)	–	175	–	$^{\circ}\text{C}$
$T_{SDH}$	Thermal Shutdown Hysteresis	Temperature Falling from $T_{SD}$ (Note 6)	–	25	–	$^{\circ}\text{C}$

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. Performance guaranteed over the indicated operating temperature range by design and/or characterization production tested at  $T_J = T_A = 25^{\circ}\text{C}$ . Low duty cycle pulse techniques are used during testing to maintain the junction temperature as close to ambient as possible.

4. Not Characterized at  $V_{IN} = 2.2\text{ V}$ ,  $V_{OUT} = 1.2\text{ V}$ ,  $I_{OUT} = 150\text{ mA}$ .

5. Respect SOA.

6. Guaranteed by design and characterization.

**ELECTRICAL CHARACTERISTICS – VOLTAGE VERSION 1.5 V**

( $-40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$ ;  $V_{IN} = 2.5\text{ V}$ ;  $I_{OUT} = 1\text{ mA}$ ,  $C_{IN} = C_{OUT} = 1.0\text{ }\mu\text{F}$ , unless otherwise noted. Typical values are at  $T_A = +25^{\circ}\text{C}$ .) (Note 7)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
$V_{IN}$	Operating Input Voltage		2.2	–	5.5	V
$V_{OUT}$	Output Voltage	$T_A = +25^{\circ}\text{C}$	1.485	1.5	1.515	V
		$-40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$	1.470	1.5	1.530	
$\text{Line}_{\text{Reg}}$	Line Regulation	$4.3\text{ V} < V_{IN} \leq 5.5\text{ V}$ , $I_{OUT} = 1\text{ mA}$	–	0.05	0.20	%/V
$\text{Load}_{\text{Reg}}$	Load Regulation	$0\text{ mA} < I_{OUT} \leq 150\text{ mA}$ , $V_{IN} = 4.3\text{ V}$	–20	–	20	mV
$V_{DO}$	Dropout Voltage	$I_{OUT} = 150\text{ mA}$ (Note 8)	–	–	–	mV
$I_{OUT}$	Output Current	(Note 9)	150	–	–	mA
$I_{SC}$	Short Circuit Current Limit	$V_{OUT} = 0\text{ V}$	–	225	–	mA
$I_Q$	Quiescent Current	$I_{OUT} = 0\text{ mA}$	–	0.5	0.9	$\mu\text{A}$
$I_{STB}$	Standby Current	$V_{EN} = 0\text{ V}$ , $T_J = 25^{\circ}\text{C}$	–	0.1	0.5	$\mu\text{A}$
$V_{ENH}$	EN Pin Threshold Voltage	EN Input Voltage “H”	1.2	–	–	V
$V_{ENL}$	EN Pin Threshold Voltage	EN Input Voltage “L”	–	–	0.4	V
$I_{EN}$	EN Pin Current	$V_{EN} \leq V_{IN} \leq 5.5\text{ V}$ (Note 10)	–	10	–	nA
PSRR	Power Supply Rejection Ratio	$f = 1\text{ kHz}$ , $V_{IN} = 2.5\text{ V} + 200\text{ mVpp}$ Modulation $I_{OUT} = 150\text{ mA}$	–	57	–	dB
$V_{NOISE}$	Output Noise Voltage	$V_{IN} = 5.5\text{ V}$ , $I_{OUT} = 1\text{ mA}$ , $f = 100\text{ Hz}$ to $1\text{ MHz}$ , $C_{OUT} = 1\text{ }\mu\text{F}$	–	90	–	$\mu\text{V}_{\text{rms}}$
$R_{LOW}$	Active Output Discharge Resistance (A option only)	$V_{IN} = 5.5\text{ V}$ , $V_{EN} = 0\text{ V}$ (Note 10)	–	100	–	$\Omega$
$T_{SD}$	Thermal Shutdown Temperature	Temperature Increasing from $T_J = +25^{\circ}\text{C}$ (Note 10)	–	175	–	$^{\circ}\text{C}$
$T_{SDH}$	Thermal Shutdown Hysteresis	Temperature Falling from $T_{SD}$ (Note 10)	–	25	–	$^{\circ}\text{C}$

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

7. Performance guaranteed over the indicated operating temperature range by design and/or characterization production tested at  $T_J = T_A = 25^{\circ}\text{C}$ . Low duty cycle pulse techniques are used during testing to maintain the junction temperature as close to ambient as possible.

8. Not Characterized at  $V_{IN} = 2.2\text{ V}$ ,  $V_{OUT} = 1.5\text{ V}$ ,  $I_{OUT} = 150\text{ mA}$ .

9. Respect SOA.

10. Guaranteed by design and characterization.

**ELECTRICAL CHARACTERISTICS – VOLTAGE VERSION 1.8 V**

( $-40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$ ;  $V_{IN} = 2.8\text{ V}$ ;  $I_{OUT} = 1\text{ mA}$ ,  $C_{IN} = C_{OUT} = 1.0\text{ }\mu\text{F}$ , unless otherwise noted. Typical values are at  $T_A = +25^{\circ}\text{C}$ .) (Note 11)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
$V_{IN}$	Operating Input Voltage		2.2	–	5.5	V
$V_{OUT}$	Output Voltage	$T_A = +25^{\circ}\text{C}$	1.782	1.8	1.818	V
		$-40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$	1.764	1.8	1.836	
$\text{Line}_{\text{Reg}}$	Line Regulation	$2.8\text{ V} < V_{IN} \leq 5.5\text{ V}$ , $I_{OUT} = 1\text{ mA}$	–	0.05	0.20	%/V
$\text{Load}_{\text{Reg}}$	Load Regulation	$0\text{ mA} < I_{OUT} \leq 150\text{ mA}$ , $V_{IN} = 2.8\text{ V}$	–20	1	20	mV
$V_{DO}$	Dropout Voltage	$I_{OUT} = 150\text{ mA}$ (Note 12)	–	350	500	mV
$I_{OUT}$	Output Current	(Note 13)	150	–	–	mA
$I_{SC}$	Short Circuit Current Limit	$V_{OUT} = 0\text{ V}$	–	225	–	mA
$I_Q$	Quiescent Current	$I_{OUT} = 0\text{ mA}$	–	0.5	0.9	$\mu\text{A}$
$I_{STB}$	Standby Current	$V_{EN} = 0\text{ V}$ , $T_J = 25^{\circ}\text{C}$	–	0.1	0.5	$\mu\text{A}$
$V_{ENH}$	EN Pin Threshold Voltage	EN Input Voltage “H”	1.2	–	–	V
$V_{ENL}$	EN Pin Threshold Voltage	EN Input Voltage “L”	–	–	0.4	V
$I_{EN}$	EN Pull Down Current	$V_{EN} \leq V_{IN} \leq 5.5\text{ V}$ (Note 14)	–	10	–	nA
PSRR	Power Supply Rejection Ratio	$f = 1\text{ kHz}$ , $V_{IN} = 2.8\text{ V} + 200\text{ mVpp}$ Modulation $I_{OUT} = 150\text{ mA}$	–	57	–	dB
$V_{NOISE}$	Output Noise Voltage	$V_{IN} = 5.5\text{ V}$ , $I_{OUT} = 1\text{ mA}$ $f = 100\text{ Hz to }1\text{ MHz}$ , $C_{OUT} = 1\text{ }\mu\text{F}$	–	95	–	$\mu\text{V}_{\text{rms}}$
$R_{LOW}$	Active Output Discharge Resistance (A option only)	$V_{IN} = 5.5\text{ V}$ , $V_{EN} = 0\text{ V}$ (Note 14)	–	100	–	$\Omega$
$T_{SD}$	Thermal Shutdown Temperature	Temperature Increasing from $T_J = +25^{\circ}\text{C}$ (Note 14)	–	175	–	$^{\circ}\text{C}$
$T_{SDH}$	Thermal Shutdown Hysteresis	Temperature Falling from $T_{SD}$ (Note 14)	–	25	–	$^{\circ}\text{C}$

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

11. Performance guaranteed over the indicated operating temperature range by design and/or characterization production tested at  $T_J = T_A = 25^{\circ}\text{C}$ . Low duty cycle pulse techniques are used during testing to maintain the junction temperature as close to ambient as possible.

12. Characterized when  $V_{OUT}$  falls 54 mV below the regulated voltage and only for devices with  $V_{OUT} = 1.8\text{ V}$ .

13. Respect SOA.

14. Guaranteed by design and characterization.

**ELECTRICAL CHARACTERISTICS – VOLTAGE VERSION 2.5 V**

( $-40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$ ;  $V_{IN} = 3.5\text{ V}$ ;  $I_{OUT} = 1\text{ mA}$ ,  $C_{IN} = C_{OUT} = 1.0\text{ }\mu\text{F}$ , unless otherwise noted. Typical values are at  $T_A = +25^{\circ}\text{C}$ .) (Note 15)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
$V_{IN}$	Operating Input Voltage		2.2	–	5.5	V
$V_{OUT}$	Output Voltage	$T_A = +25^{\circ}\text{C}$	2.475	2.5	2.525	V
		$-40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$	2.450	2.5	2.550	
$\text{Line}_{\text{Reg}}$	Line Regulation	$3.5\text{ V} < V_{IN} \leq 5.5\text{ V}$ , $I_{OUT} = 1\text{ mA}$	–	0.05	0.20	%/V
$\text{Load}_{\text{Reg}}$	Load Regulation	$0\text{ mA} < I_{OUT} \leq 150\text{ mA}$ , $V_{IN} = 3.5\text{ V}$	–20	1	20	mV
$V_{DO}$	Dropout Voltage	$I_{OUT} = 150\text{ mA}$ (Note 16)	–	240	350	mV
$I_{OUT}$	Output Current	(Note 17)	150	–	–	mA
$I_{SC}$	Short Circuit Current Limit	$V_{OUT} = 0\text{ V}$	–	225	–	mA
$I_Q$	Quiescent Current	$I_{OUT} = 0\text{ mA}$	–	0.5	0.9	$\mu\text{A}$
$I_{STB}$	Standby Current	$V_{EN} = 0\text{ V}$ , $T_J = 25^{\circ}\text{C}$	–	0.1	0.5	$\mu\text{A}$
$V_{ENH}$	EN Pin Threshold Voltage	EN Input Voltage “H”	1.2	–	–	V
$V_{ENL}$	EN Pin Threshold Voltage	EN Input Voltage “L”	–	–	0.4	V
$I_{EN}$	EN Pull Down Current	$V_{EN} \leq V_{IN} \leq 5.5\text{ V}$ (Note 18)	–	10	–	nA
PSRR	Power Supply Rejection Ratio	$f = 1\text{ kHz}$ , $V_{IN} = 3.5\text{ V} + 200\text{ mVpp}$ Modulation $I_{OUT} = 150\text{ mA}$	–	57	–	dB
$V_{NOISE}$	Output Noise Voltage	$V_{IN} = 5.5\text{ V}$ , $I_{OUT} = 1\text{ mA}$ $f = 100\text{ Hz}$ to $1\text{ MHz}$ , $C_{OUT} = 1\text{ }\mu\text{F}$	–	125	–	$\mu\text{V}_{\text{rms}}$
$R_{LOW}$	Active Output Discharge Resistance (A option only)	$V_{IN} = 5.5\text{ V}$ , $V_{EN} = 0\text{ V}$ (Note 18)	–	100	–	$\Omega$
$T_{SD}$	Thermal Shutdown Temperature	Temperature Increasing from $T_J = +25^{\circ}\text{C}$ (Note 18)	–	175	–	$^{\circ}\text{C}$
$T_{SDH}$	Thermal Shutdown Hysteresis	Temperature Falling from $T_{SD}$ (Note 18)	–	25	–	$^{\circ}\text{C}$

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

15. Performance guaranteed over the indicated operating temperature range by design and/or characterization production tested at  $T_J = T_A = 25^{\circ}\text{C}$ . Low duty cycle pulse techniques are used during testing to maintain the junction temperature as close to ambient as possible.

16. Characterized when  $V_{OUT}$  falls 75 mV below the regulated voltage and only for devices with  $V_{OUT} = 2.5\text{ V}$ .

17. Respect SOA.

18. Guaranteed by design and characterization.

**ELECTRICAL CHARACTERISTICS – VOLTAGE VERSION 2.8 V**

( $-40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$ ;  $V_{IN} = 3.8\text{ V}$ ;  $I_{OUT} = 1\text{ mA}$ ,  $C_{IN} = C_{OUT} = 1.0\text{ }\mu\text{F}$ , unless otherwise noted. Typical values are at  $T_A = +25^{\circ}\text{C}$ .) (Note 19)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
$V_{IN}$	Operating Input Voltage		2.2	–	5.5	V
$V_{OUT}$	Output Voltage	$T_A = +25^{\circ}\text{C}$	2.772	2.8	2.828	V
		$-40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$	2.744	2.8	2.856	
$\text{Line}_{\text{Reg}}$	Line Regulation	$3.8\text{ V} < V_{IN} \leq 5.5\text{ V}$ , $I_{OUT} = 1\text{ mA}$	–	0.05	0.20	%/V
$\text{Load}_{\text{Reg}}$	Load Regulation	$0\text{ mA} < I_{OUT} \leq 150\text{ mA}$ , $V_{IN} = 3.8\text{ V}$	–20	1	20	mV
$V_{DO}$	Dropout Voltage	$I_{OUT} = 150\text{ mA}$ (Note 20)	–	210	315	mV
$I_{OUT}$	Output Current	(Note 21)	150	–	–	mA
$I_{SC}$	Short Circuit Current Limit	$V_{OUT} = 0\text{ V}$	–	195	–	mA
$I_Q$	Quiescent Current	$I_{OUT} = 0\text{ mA}$	–	0.5	0.9	$\mu\text{A}$
$I_{STB}$	Standby Current	$V_{EN} = 0\text{ V}$ , $T_J = 25^{\circ}\text{C}$	–	0.1	0.5	$\mu\text{A}$
$V_{ENH}$	EN Pin Threshold Voltage	EN Input Voltage “H”	1.2	–	–	V
$V_{ENL}$	EN Pin Threshold Voltage	EN Input Voltage “L”	–	–	0.4	V
$I_{EN}$	EN Pull Down Current	$V_{EN} \leq V_{IN} \leq 5.5\text{ V}$ (Note 22)	–	10	–	nA
PSRR	Power Supply Rejection Ratio	$f = 1\text{ kHz}$ , $V_{IN} = 3.8\text{ V} + 200\text{ mVpp}$ Modulation $I_{OUT} = 150\text{ mA}$	–	40	–	dB
$V_{NOISE}$	Output Noise Voltage	$V_{IN} = 5.5\text{ V}$ , $I_{OUT} = 1\text{ mA}$ $f = 100\text{ Hz to }1\text{ MHz}$ , $C_{OUT} = 1\text{ }\mu\text{F}$	–	125	–	$\mu\text{V}_{\text{rms}}$
$R_{LOW}$	Active Output Discharge Resistance (A option only)	$V_{IN} = 5.5\text{ V}$ , $V_{EN} = 0\text{ V}$ (Note 22)	–	100	–	$\Omega$
$T_{SD}$	Thermal Shutdown Temperature	Temperature Increasing from $T_J = +25^{\circ}\text{C}$ (Note 22)	–	175	–	$^{\circ}\text{C}$
$T_{SDH}$	Thermal Shutdown Hysteresis	Temperature Falling from $T_{SD}$ (Note 22)	–	25	–	$^{\circ}\text{C}$

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

19. Performance guaranteed over the indicated operating temperature range by design and/or characterization production tested at  $T_J = T_A = 25^{\circ}\text{C}$ . Low duty cycle pulse techniques are used during testing to maintain the junction temperature as close to ambient as possible.

20. Characterized when  $V_{OUT}$  falls 84 mV below the regulated voltage and only for devices with  $V_{OUT} = 2.8\text{ V}$ .

21. Respect SOA.

22. Guaranteed by design and characterization.

**ELECTRICAL CHARACTERISTICS – VOLTAGE VERSION 3.0 V**

( $-40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$ ;  $V_{IN} = 4.0\text{ V}$ ;  $I_{OUT} = 1\text{ mA}$ ,  $C_{IN} = C_{OUT} = 1.0\text{ }\mu\text{F}$ , unless otherwise noted. Typical values are at  $T_A = +25^{\circ}\text{C}$ .) (Note 23)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
$V_{IN}$	Operating Input Voltage		2.2	–	5.5	V
$V_{OUT}$	Output Voltage	$T_A = +25^{\circ}\text{C}$	2.97	3.0	3.03	V
		$-40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$	2.94	3.0	3.06	
$\text{Line}_{\text{Reg}}$	Line Regulation	$4.0\text{ V} < V_{IN} \leq 5.5\text{ V}$ , $I_{OUT} = 1\text{ mA}$	–	0.05	0.20	%/V
$\text{Load}_{\text{Reg}}$	Load Regulation	$0\text{ mA} < I_{OUT} \leq 150\text{ mA}$ , $V_{IN} = 4\text{ V}$	–20	1	20	mV
$V_{DO}$	Dropout Voltage	$I_{OUT} = 150\text{ mA}$ (Note 24)	–	190	260	mV
$I_{OUT}$	Output Current	(Note 25)	150	–	–	mA
$I_{SC}$	Short Circuit Current Limit	$V_{OUT} = 0\text{ V}$	–	195	–	mA
$I_Q$	Quiescent Current	$I_{OUT} = 0\text{ mA}$	–	0.5	0.9	$\mu\text{A}$
$I_{STB}$	Standby Current	$V_{EN} = 0\text{ V}$ , $T_J = 25^{\circ}\text{C}$	–	0.1	0.5	$\mu\text{A}$
$V_{ENH}$	EN Pin Threshold Voltage	EN Input Voltage “H”	1.2	–	–	V
$V_{ENL}$	EN Pin Threshold Voltage	EN Input Voltage “L”	–	–	0.4	V
$I_{EN}$	EN Pull Down Current	$V_{EN} \leq V_{IN} \leq 5.5\text{ V}$ (Note 26)	–	10	–	nA
PSRR	Power Supply Rejection Ratio	$f = 1\text{ kHz}$ , $V_{IN} = 4.0\text{ V} + 200\text{ mVpp}$ Modulation $I_{OUT} = 150\text{ mA}$	–	47	–	dB
$V_{NOISE}$	Output Noise Voltage	$V_{IN} = 5.5\text{ V}$ , $I_{OUT} = 1\text{ mA}$ $f = 100\text{ Hz to }1\text{ MHz}$ , $C_{OUT} = 1\text{ }\mu\text{F}$	–	120	–	$\mu\text{V}_{\text{rms}}$
$R_{LOW}$	Active Output Discharge Resistance (A option only)	$V_{IN} = 5.5\text{ V}$ , $V_{EN} = 0\text{ V}$ (Note 26)	–	100	–	$\Omega$
$T_{SD}$	Thermal Shutdown Temperature	Temperature Increasing from $T_J = +25^{\circ}\text{C}$ (Note 26)	–	175	–	$^{\circ}\text{C}$
$T_{SDH}$	Thermal Shutdown Hysteresis	Temperature Falling from $T_{SD}$ (Note 26)	–	25	–	$^{\circ}\text{C}$

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

23. Performance guaranteed over the indicated operating temperature range by design and/or characterization production tested at  $T_J = T_A = 25^{\circ}\text{C}$ . Low duty cycle pulse techniques are used during testing to maintain the junction temperature as close to ambient as possible.

24. Characterized when  $V_{OUT}$  falls 90 mV below the regulated voltage and only for devices with  $V_{OUT} = 3.0\text{ V}$ .

25. Respect SOA.

26. Guaranteed by design and characterization.



**ELECTRICAL CHARACTERISTICS – VOLTAGE VERSION 3.3 V**

( $-40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$ ;  $V_{IN} = 4.3\text{ V}$ ;  $I_{OUT} = 1\text{ mA}$ ,  $C_{IN} = C_{OUT} = 1.0\text{ }\mu\text{F}$ , unless otherwise noted. Typical values are at  $T_A = +25^{\circ}\text{C}$ .) (Note 27)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
$V_{IN}$	Operating Input Voltage		2.2	–	5.5	V
$V_{OUT}$	Output Voltage	$T_A = +25^{\circ}\text{C}$	3.267	3.3	3.333	V
		$-40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$	3.234	3.3	3.366	
$\text{Line}_{\text{Reg}}$	Line Regulation	$4.3\text{ V} < V_{IN} \leq 5.5\text{ V}$ , $I_{OUT} = 1\text{ mA}$	–	0.05	0.20	%/V
$\text{Load}_{\text{Reg}}$	Load Regulation	$0\text{ mA} < I_{OUT} \leq 150\text{ mA}$ , $V_{IN} = 4.3\text{ V}$	–20	1	20	mV
$V_{DO}$	Dropout Voltage	$I_{OUT} = 150\text{ mA}$ (Note 28)	–	180	250	mV
$I_{OUT}$	Output Current	(Note 29)	150	–	–	mA
$I_{SC}$	Short Circuit Current Limit	$V_{OUT} = 0\text{ V}$	–	195	–	mA
$I_Q$	Quiescent Current	$I_{OUT} = 0\text{ mA}$	–	0.5	0.9	$\mu\text{A}$
$I_{STB}$	Standby Current	$V_{EN} = 0\text{ V}$ , $T_J = 25^{\circ}\text{C}$	–	0.1	0.5	$\mu\text{A}$
$V_{ENH}$	EN Pin Threshold Voltage	EN Input Voltage “H”	1.2	–	–	V
$V_{ENL}$	EN Pin Threshold Voltage	EN Input Voltage “L”	–	–	0.4	V
$I_{EN}$	EN Pull Down Current	$V_{EN} \leq V_{IN} \leq 5.5\text{ V}$ (Note 30)	–	10	–	nA
PSRR	Power Supply Rejection Ratio	$f = 1\text{ kHz}$ , $V_{IN} = 4.3\text{ V} + 200\text{ mVpp}$ Modulation $I_{OUT} = 150\text{ mA}$	–	41	–	dB
$V_{NOISE}$	Output Noise Voltage	$V_{IN} = 5.5\text{ V}$ , $I_{OUT} = 1\text{ mA}$ $f = 100\text{ Hz}$ to $1\text{ MHz}$ , $C_{OUT} = 1\text{ }\mu\text{F}$	–	125	–	$\mu\text{V}_{\text{rms}}$
$R_{LOW}$	Active Output Discharge Resistance (A option only)	$V_{IN} = 5.5\text{ V}$ , $V_{EN} = 0\text{ V}$ (Note 30)	–	100	–	$\Omega$
$T_{SD}$	Thermal Shutdown Temperature	Temperature Increasing from $T_J = +25^{\circ}\text{C}$ (Note 30)	–	175	–	$^{\circ}\text{C}$
$T_{SDH}$	Thermal Shutdown Hysteresis	Temperature Falling from $T_{SD}$ (Note 30)	–	25	–	$^{\circ}\text{C}$

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

27. Performance guaranteed over the indicated operating temperature range by design and/or characterization production tested at  $T_J = T_A = 25^{\circ}\text{C}$ . Low duty cycle pulse techniques are used during testing to maintain the junction temperature as close to ambient as possible.

28. Characterized when  $V_{OUT}$  falls 99 mV below the regulated voltage and only for devices with  $V_{OUT} = 3.3\text{ V}$ .

29. Respect SOA.

30. Guaranteed by design and characterization.

# **ELECTRICAL CHARACTERISTICS – VOLTAGE VERSION 3.6 V**

( $-40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$ ;  $V_{\text{IN}} = 4.6\text{ V}$ ;  $I_{\text{OUT}} = 1\text{ mA}$ ,  $C_{\text{IN}} = C_{\text{OUT}} = 1.0\text{ }\mu\text{F}$ , unless otherwise noted. Typical values are at  $T_A = +25^{\circ}\text{C}$ .) (Note 31)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
$V_{\text{IN}}$	Operating Input Voltage		2.2	–	5.5	V
$V_{\text{OUT}}$	Output Voltage	$T_A = +25^{\circ}\text{C}$	3.564	3.6	3.636	V
		$-40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}$	3.528	3.6	3.672	
$\text{Line}_{\text{Reg}}$	Line Regulation	$4.6\text{ V} < V_{\text{IN}} \leq 5.5\text{ V}$ , $I_{\text{OUT}} = 1\text{ mA}$	–	0.05	0.20	%/V
$\text{Load}_{\text{Reg}}$	Load Regulation	$0\text{ mA} < I_{\text{OUT}} \leq 150\text{ mA}$ , $V_{\text{IN}} = 4.6\text{ V}$	–20	1	20	mV
$V_{\text{DO}}$	Dropout Voltage	$I_{\text{OUT}} = 150\text{ mA}$ (Note 32)	–	170	240	mV
$I_{\text{OUT}}$	Output Current	(Note 33)	150	–	–	mA
$I_{\text{SC}}$	Short Circuit Current Limit	$V_{\text{OUT}} = 0\text{ V}$	–	195	–	mA
$I_{\text{Q}}$	Quiescent Current	$I_{\text{OUT}} = 0\text{ mA}$	–	0.5	0.9	$\mu\text{A}$
$I_{\text{STB}}$	Standby Current	$V_{\text{EN}} = 0\text{ V}$ , $T_J = 25^{\circ}\text{C}$	–	0.1	0.5	$\mu\text{A}$
$V_{\text{ENH}}$	EN Pin Threshold Voltage	EN Input Voltage “H”	1.2	–	–	V
$V_{\text{ENL}}$	EN Pin Threshold Voltage	EN Input Voltage “L”	–	–	0.4	V
$I_{\text{EN}}$	EN Pull Down Current	$V_{\text{EN}} \leq V_{\text{IN}} \leq 5.5\text{ V}$ (Note 34)	–	10	–	nA
PSRR	Power Supply Rejection Ratio	$f = 1\text{ kHz}$ , $V_{\text{IN}} = 4.6\text{ V} + 200\text{ mVpp}$ Modulation $I_{\text{OUT}} = 150\text{ mA}$	–	30	–	dB
$V_{\text{NOISE}}$	Output Noise Voltage	$V_{\text{IN}} = 5.5\text{ V}$ , $I_{\text{OUT}} = 1\text{ mA}$ $f = 100\text{ Hz to }1\text{ MHz}$ , $C_{\text{OUT}} = 1\text{ }\mu\text{F}$	–	130	–	$\mu\text{V}_{\text{rms}}$
$R_{\text{LOW}}$	Active Output Discharge Resistance (A option only)	$V_{\text{IN}} = 5.5\text{ V}$ , $V_{\text{EN}} = 0\text{ V}$ (Note 34)	–	100	–	$\Omega$
$T_{\text{SD}}$	Thermal Shutdown Temperature	Temperature Increasing from $T_J = +25^{\circ}\text{C}$ (Note 34)	–	175	–	$^{\circ}\text{C}$
$T_{\text{SDH}}$	Thermal Shutdown Hysteresis	Temperature Falling from $T_{\text{SD}}$ (Note 34)	–	25	–	$^{\circ}\text{C}$

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

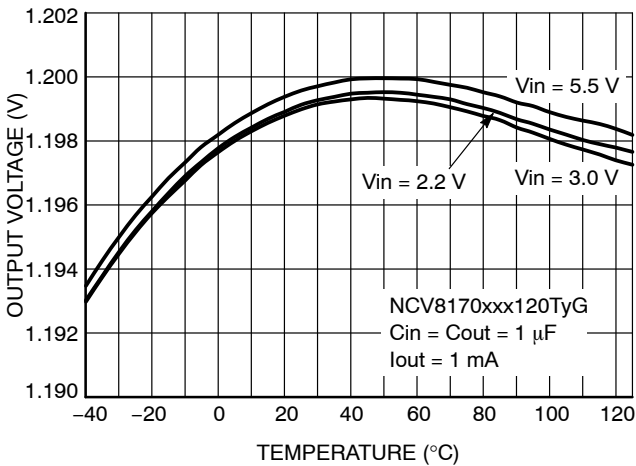
31. Performance guaranteed over the indicated operating temperature range by design and/or characterization production tested at  $T_J = T_A = 25^{\circ}\text{C}$ . Low duty cycle pulse techniques are used during testing to maintain the junction temperature as close to ambient as possible.

32. Characterized when  $V_{\text{OUT}}$  falls 108 mV below the regulated voltage and only for devices with  $V_{\text{OUT}} = 3.6\text{ V}$ .

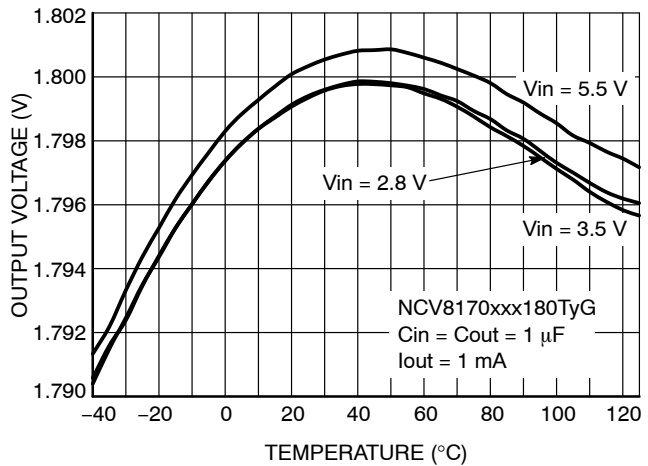
33. Respect SOA.

34. Guaranteed by design and characterization.

## **TYPICAL CHARACTERISTICS**



**Figure 3. Output Voltage vs. Temperature,  
Vout = 1.2 V**



**Figure 4. Output Voltage vs. Temperature,  
Vout = 1.8 V**

TYPICAL CHARACTERISTICS

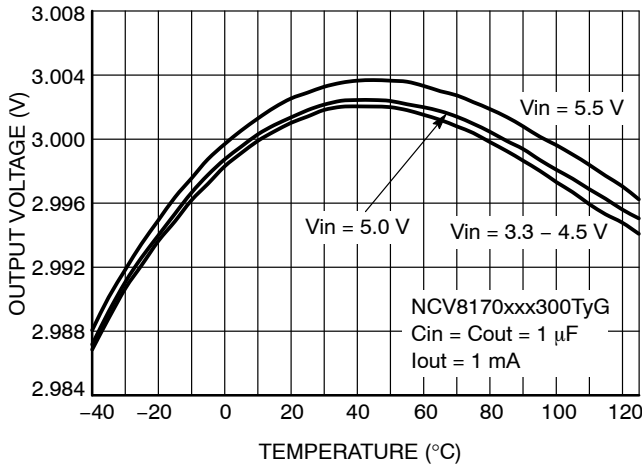


Figure 5. Output Voltage vs. Temperature,  
Vout = 3.0 V

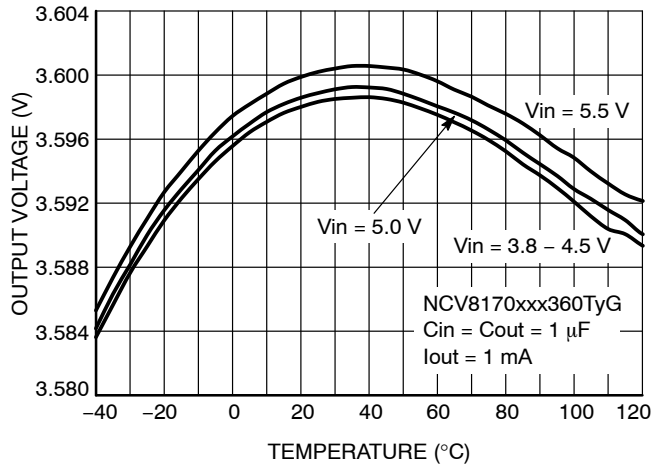


Figure 6. Output Voltage vs. Temperature,  
Vout = 3.6 V

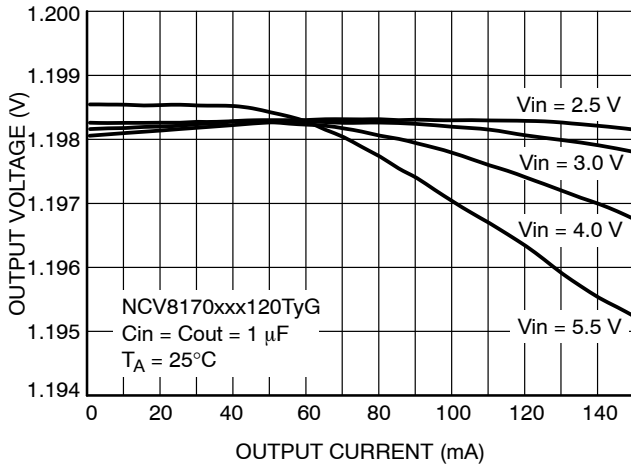


Figure 7. Output Voltage vs. Output Current,  
Vout = 1.2 V

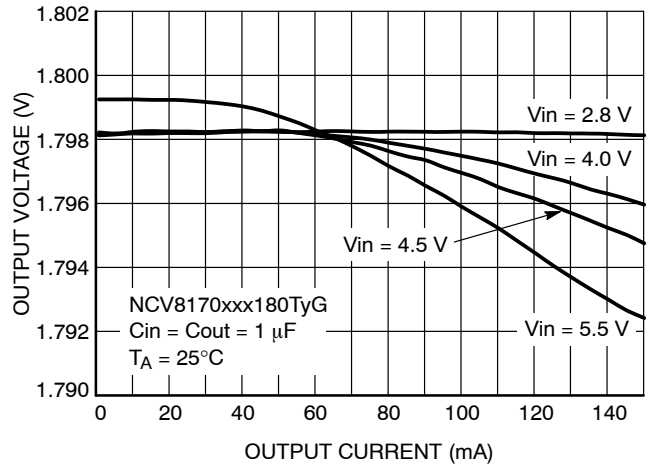


Figure 8. Output Voltage vs. Output Current,  
Vout = 1.8 V

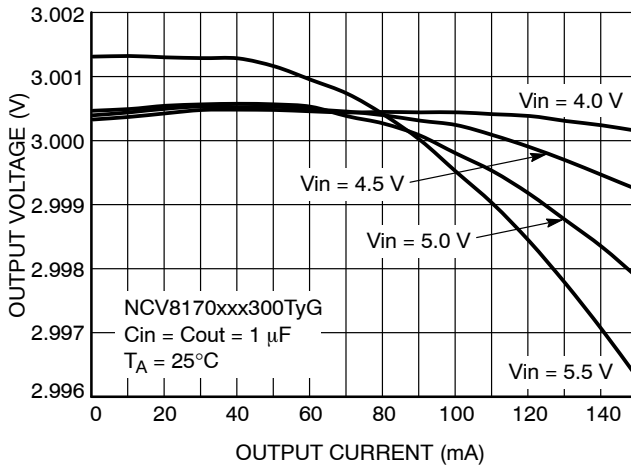


Figure 9. Output Voltage vs. Output Current,  
Vout = 3.0 V

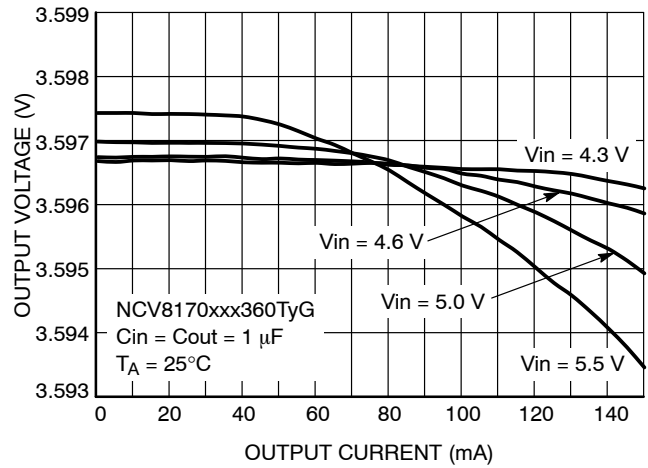


Figure 10. Output Voltage vs. Output Current,  
Vout = 3.6 V

TYPICAL CHARACTERISTICS

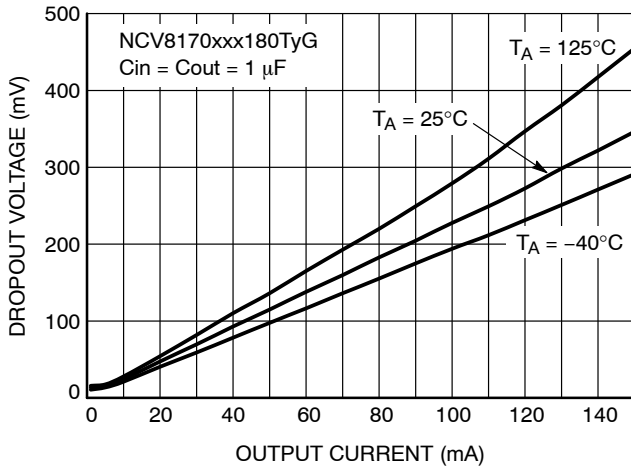


Figure 11. Dropout Voltage vs. Output Current,  
Vout = 1.8 V

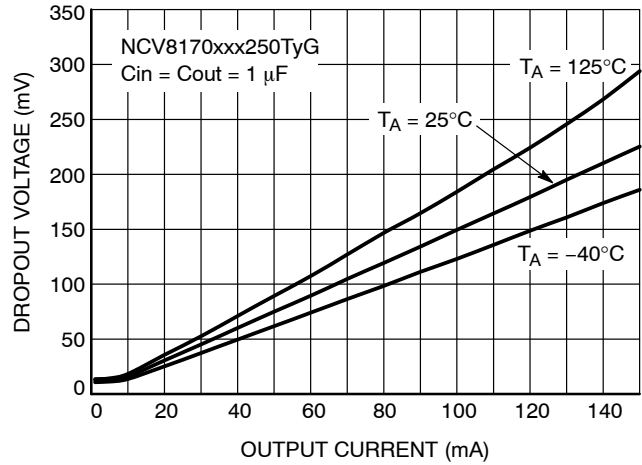


Figure 12. Dropout Voltage vs. Output Current,  
Vout = 2.5 V

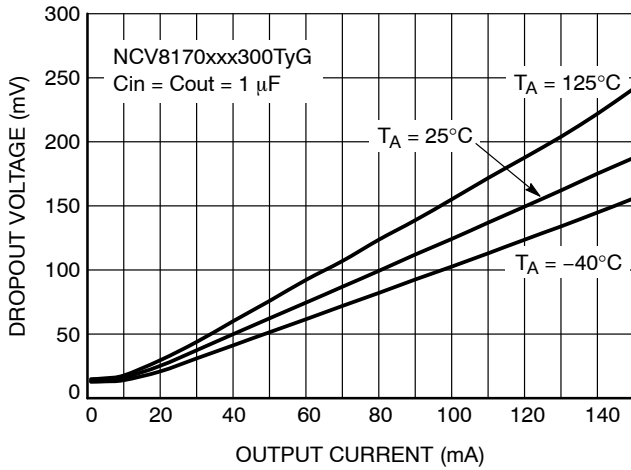


Figure 13. Dropout Voltage vs. Output Current,  
Vout = 3.0 V

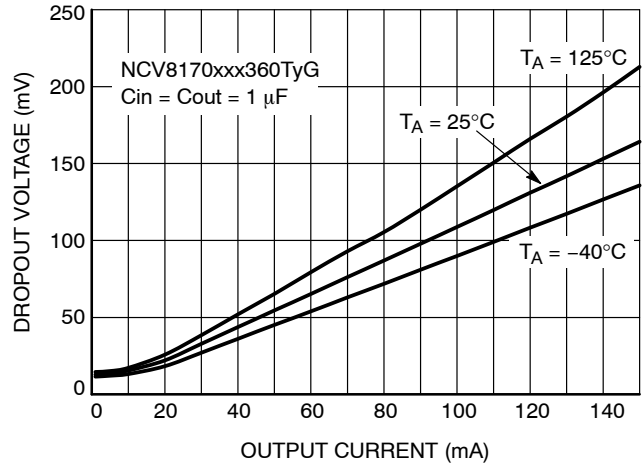


Figure 14. Dropout Voltage vs. Output Current,  
Vout = 3.6 V

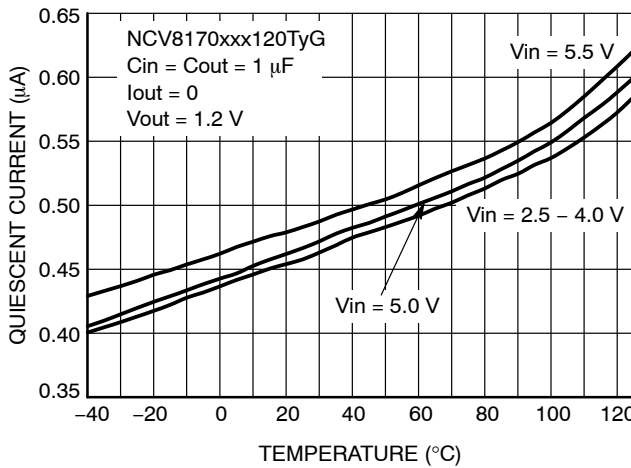


Figure 15. Quiescent Current vs. Temperature,  
Vout = 1.2 V

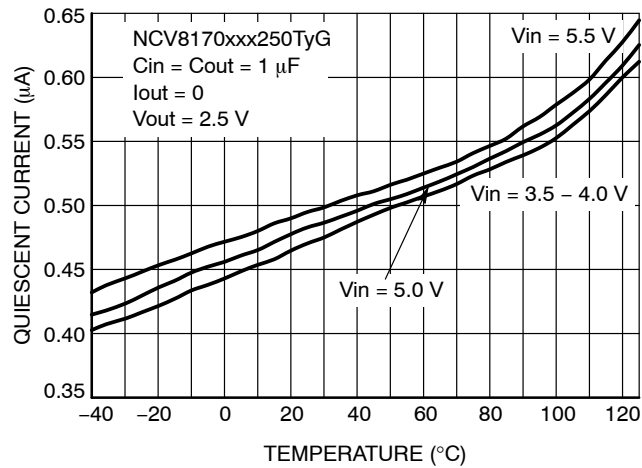


Figure 16. Quiescent Current vs. Temperature,  
Vout = 2.5 V

TYPICAL CHARACTERISTICS

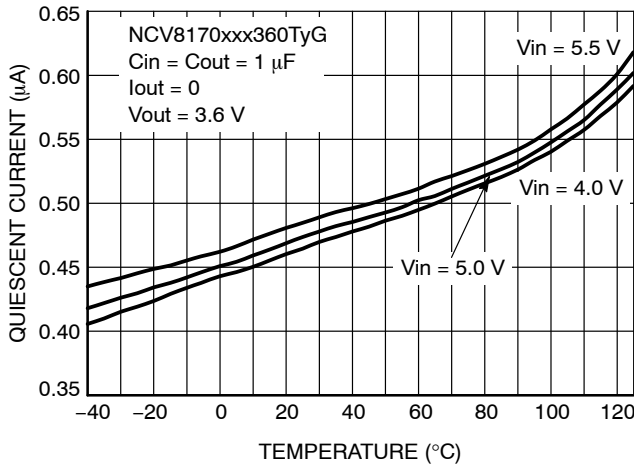


Figure 17. Quiescent Current vs. Temperature, Vout = 3.6 V

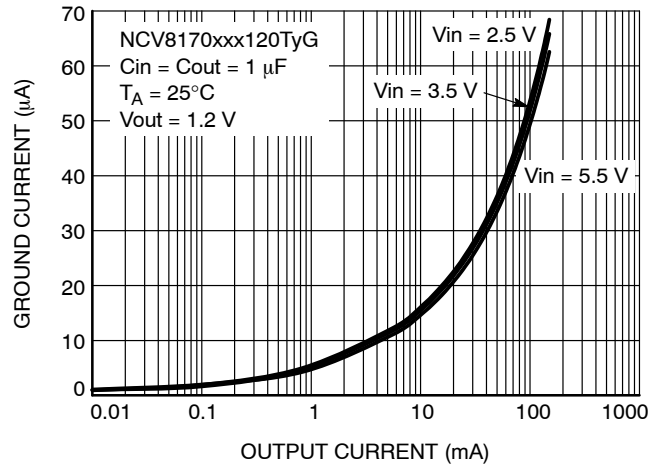


Figure 18. Ground Current vs. Output Current, Vout = 1.2 V

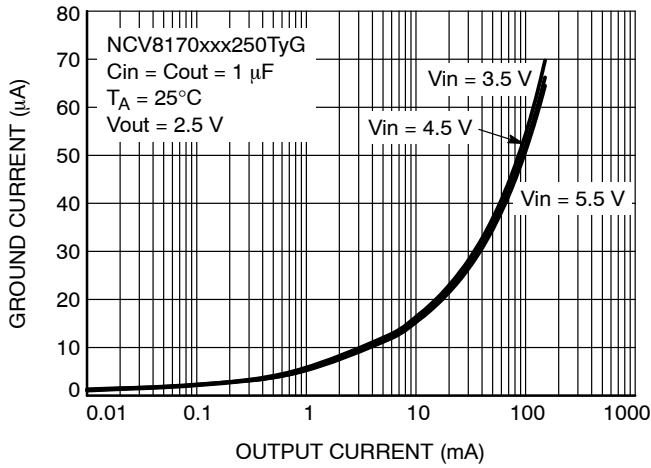


Figure 19. Ground Current vs. Output Current, Vout = 2.5 V

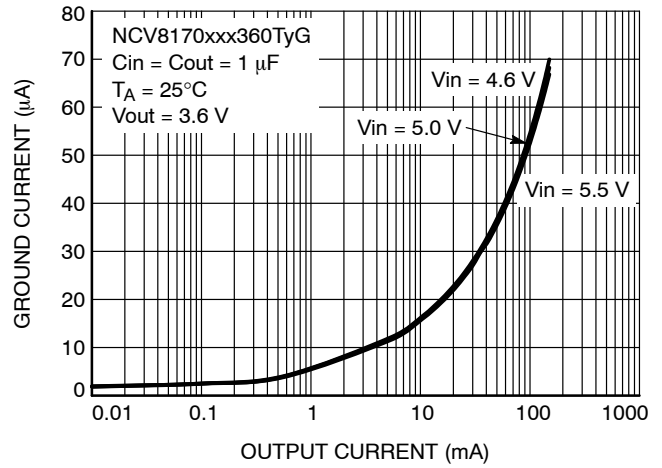


Figure 20. Ground Current vs. Output Current, Vout = 3.6 V

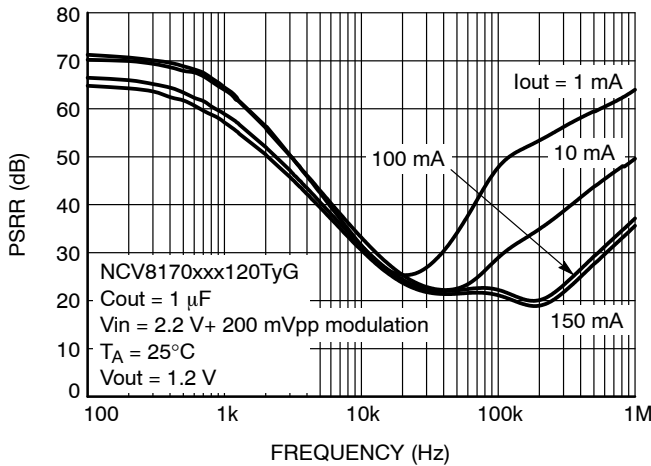


Figure 21. PSRR vs. Frequency, Vout = 1.2 V

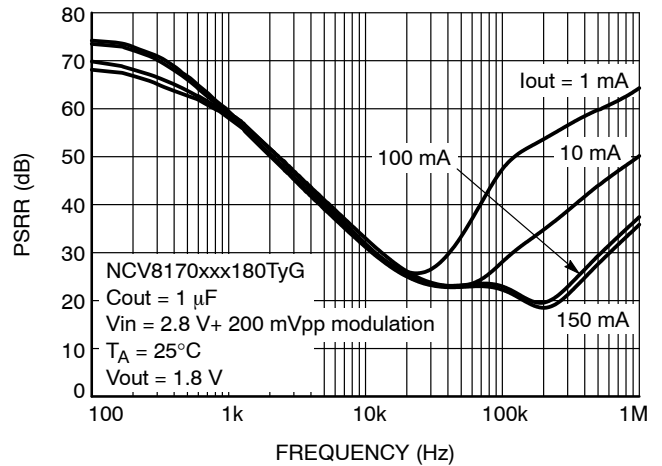


Figure 22. PSRR vs. Frequency, Vout = 1.8 V

TYPICAL CHARACTERISTICS

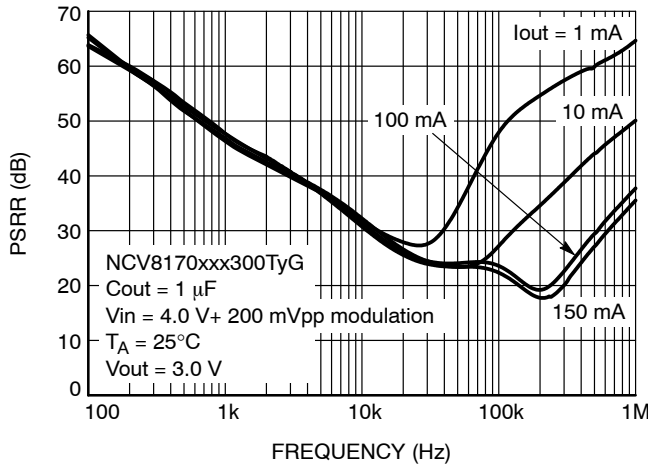


Figure 23. PSRR vs. Frequency, Vout = 3.0 V

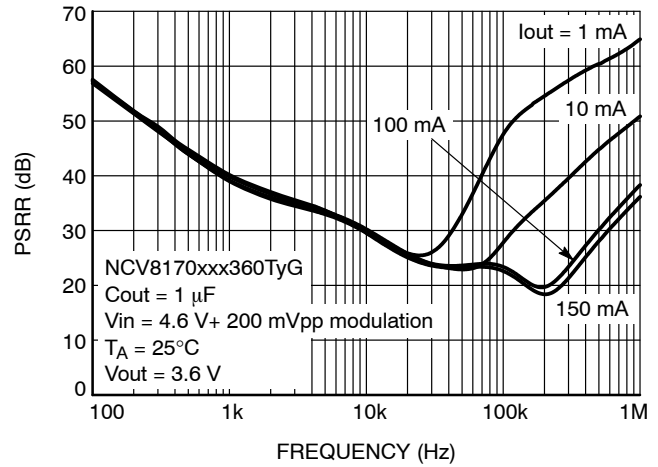


Figure 24. PSRR vs. Frequency, Vout = 3.6 V

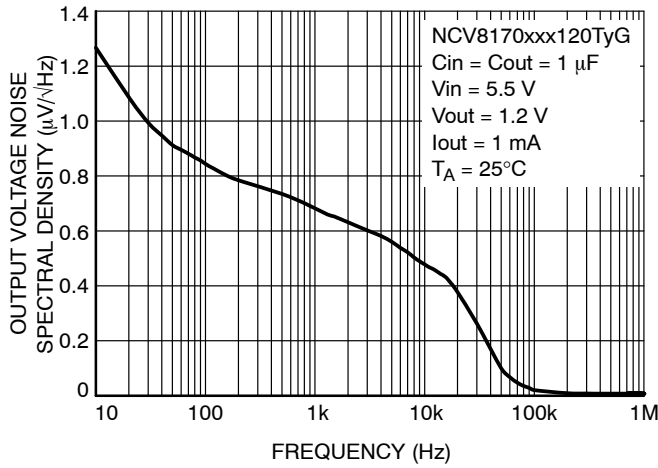


Figure 25. Output Voltage Noise Spectral Density, Vout = 1.2 V

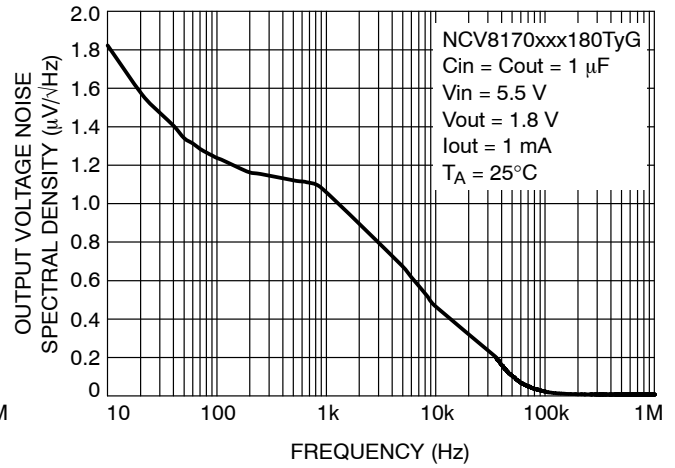


Figure 26. Output Voltage Noise Spectral Density, Vout = 1.8 V

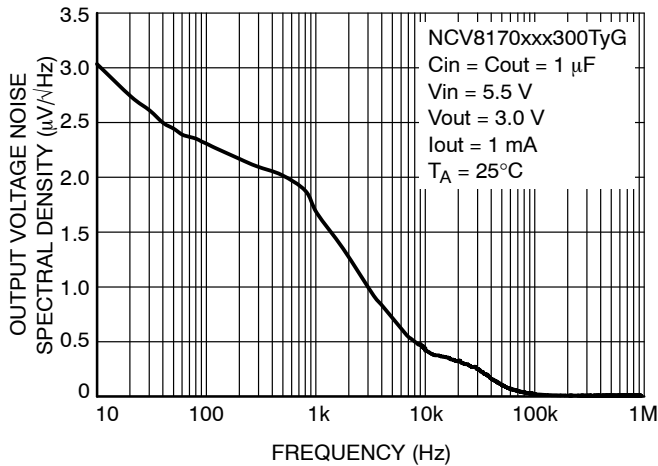


Figure 27. Output Voltage Noise Spectral Density, Vout = 3.0 V

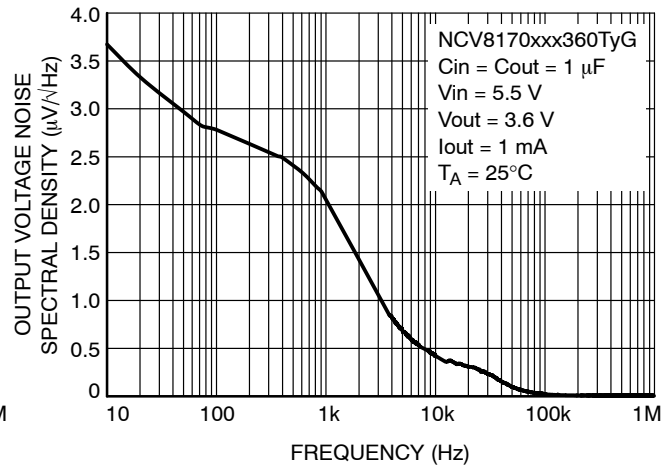


Figure 28. Output Voltage Noise Spectral Density, Vout = 3.6 V

## TYPICAL CHARACTERISTICS

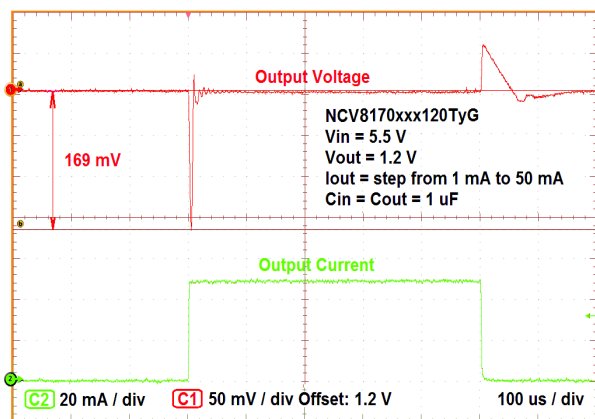


Figure 29. Load Transient Response at Load Step from 1 mA to 50 mA, Vout = 1.2 V

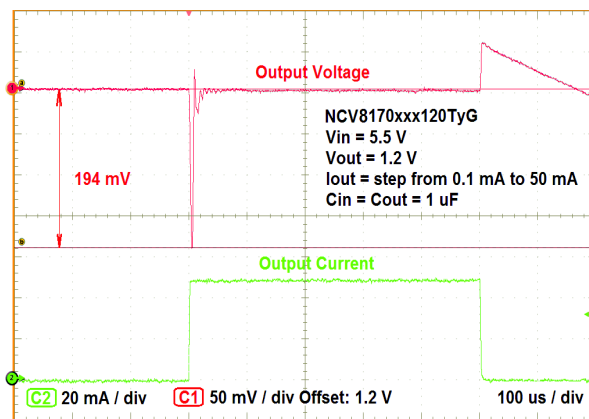


Figure 30. Load Transient Response at Load Step from 0.1 mA to 50 mA, Vout = 1.2 V

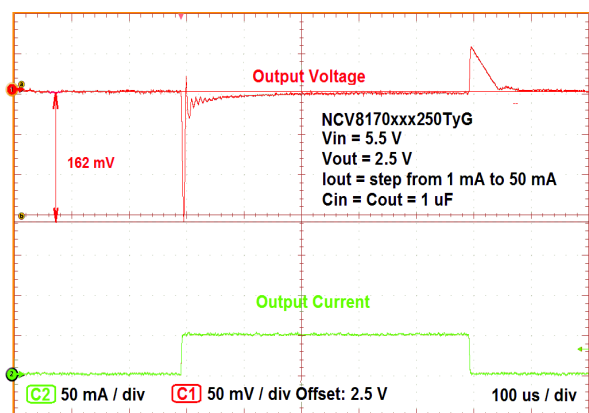


Figure 31. Load Transient Response at Load Step from 1 mA to 50 mA, Vout = 2.5 V

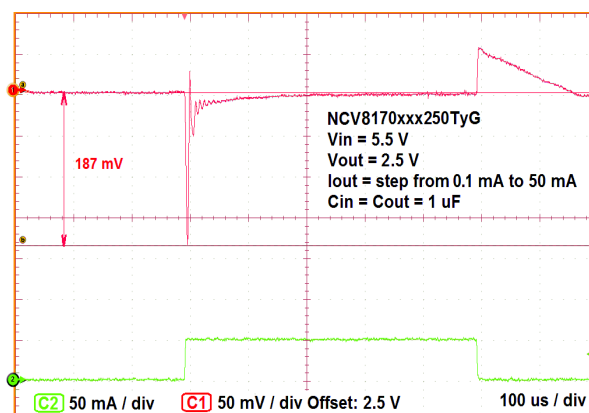


Figure 32. Load Transient Response at Load Step from 0.1 mA to 50 mA, Vout = 2.5 V

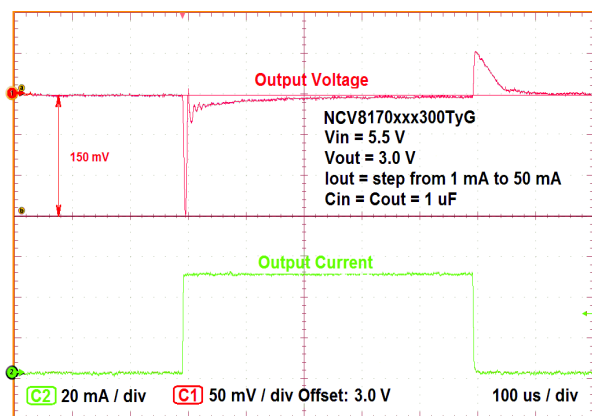


Figure 33. Load Transient Response at Load Step from 1 mA to 50 mA, Vout = 3.0 V

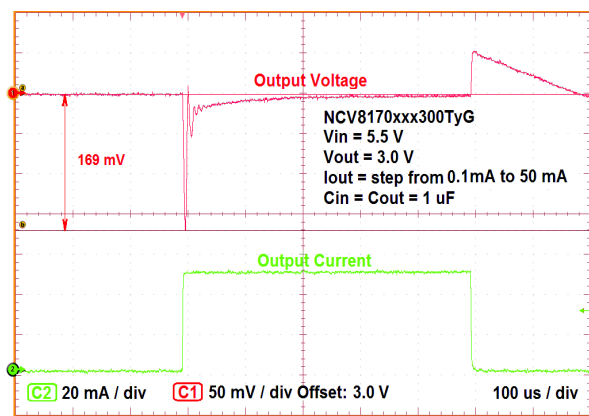


Figure 34. Load Transient Response at Load Step from 0.1 mA to 50 mA, Vout = 3.0 V

TYPICAL CHARACTERISTICS

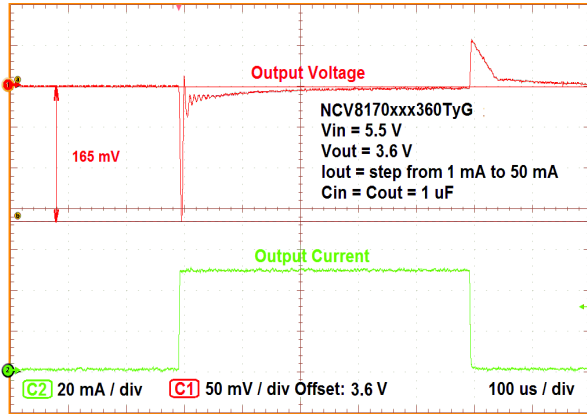


Figure 35. Load Transient Response at Load Step from 1mA to 50 mA, Vout= 3.6 V

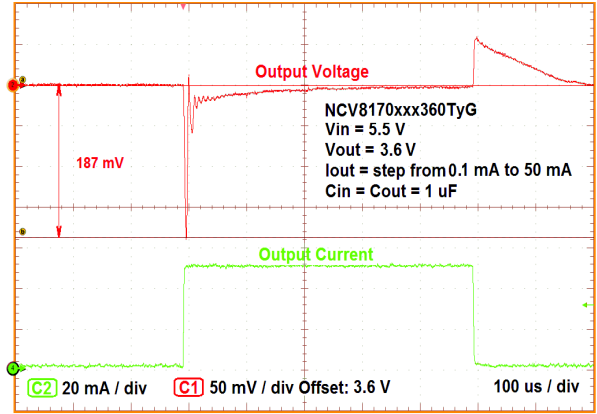


Figure 36. Load Transient Response at Load Step from 0.1 mA to 50 mA, Vout = 3.6 V

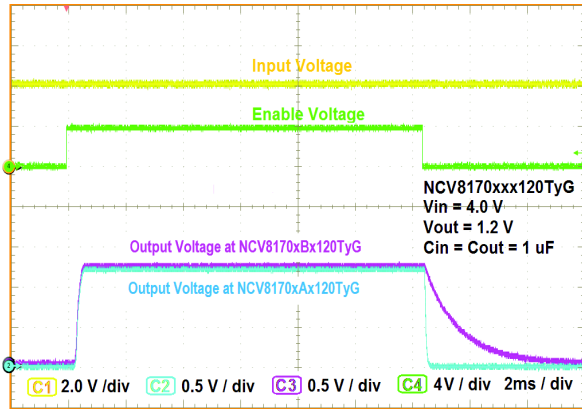


Figure 37. Output Voltage With and Without Active Discharge Feature, Vout = 1.2 V

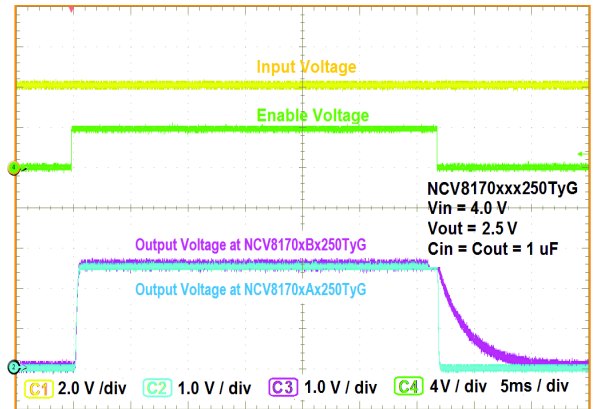


Figure 38. Output Voltage With and Without Active Discharge Feature, Vout = 2.5 V

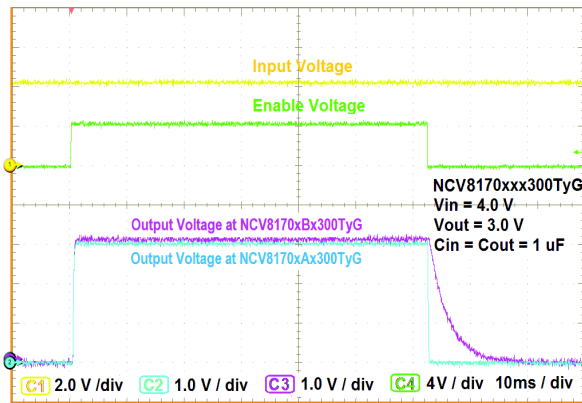


Figure 39. Output Voltage With and Without Active Discharge Feature, Vout = 3.0 V

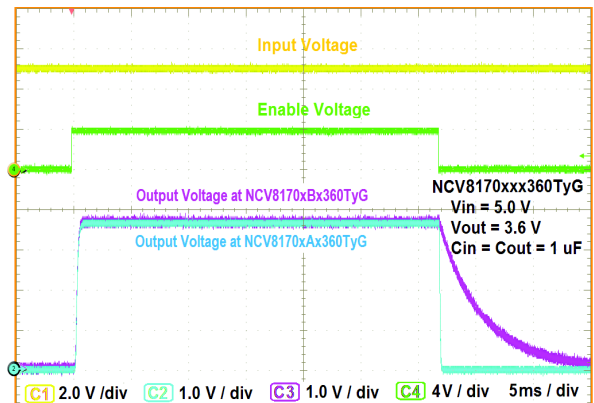


Figure 40. Output Voltage With and Without Active Discharge Feature, Vout = 3.6 V



TYPICAL CHARACTERISTICS

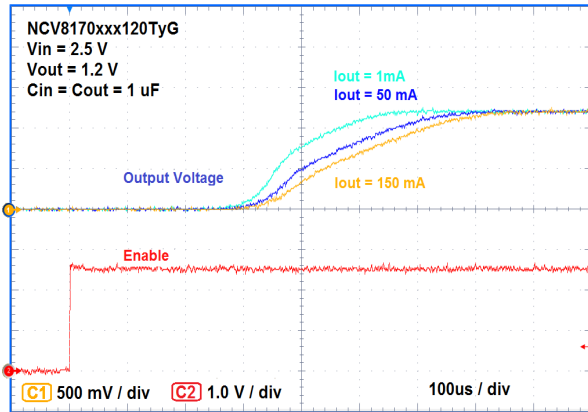


Figure 41. Enable Turn-on Response at  
 $V_{out} = 1.2\text{ V}$

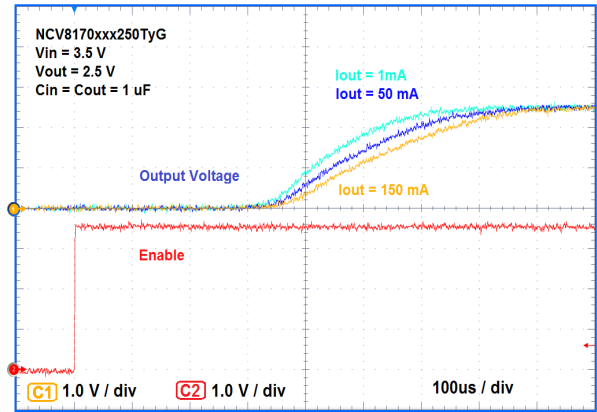


Figure 42. Enable Turn-on Response at  
 $V_{out} = 2.5\text{ V}$

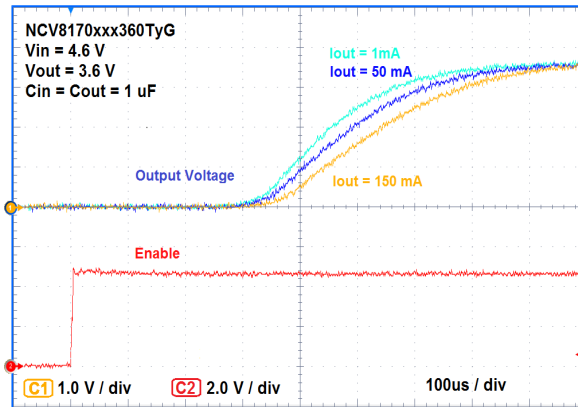


Figure 43. Enable Turn-on Response at  
 $V_{out} = 3.6\text{ V}$

## APPLICATIONS INFORMATION

### General

The NCV8170 is a high performance 150 mA Linear Regulator with Ultra Low IQ. This device delivers low Noise and high Power Supply Rejection Ratio with excellent dynamic performance due to employing the Dynamic Quiescent Current adjustment which assure ultra low  $I_Q$  consumption at no – load state. These parameters make this device very suitable for various battery powered applications.

### Input Decoupling ( $C_{IN}$ )

It is recommended to connect at least a 1  $\mu$ F Ceramic X5R or X7R capacitor between IN and GND pins of the device. This capacitor will provide a low impedance path for any unwanted AC signals or Noise superimposed onto constant Input Voltage. The good input capacitor will limit the influence of input trace inductances and source resistance during sudden load current changes.

Higher capacitance and lower ESR Capacitors will improve the overall line transient response.

### Output Decoupling ( $C_{OUT}$ )

The NCV8170 does not require a minimum Equivalent Series Resistance (ESR) for the output capacitor. The device is designed to be stable with standard ceramics capacitors with values of 1.0  $\mu$ F or greater up to 10  $\mu$ F. The X5R and X7R types have the lowest capacitance variations over temperature thus they are recommended. There is recommended connect the output capacitor as close as possible to the output pin of the regulator.

### Enable Operation

The NCV8170 uses the EN pin to enable /disable its device and to activate /deactivate the active discharge function at devices with this feature. If the EN pin voltage is pulled below 0.4 V the device is guaranteed to be disable. The active discharge transistor at the devices with Active Discharge Feature is activated and the output voltage VOUT is pulled to GND through an internal circuitry with effective resistance about 100 ohms.

If the EN pin voltage is higher than 1.2 V the device is guaranteed to be enabled. The internal active discharge

circuitry is switched off and the desired output voltage is available at output pin. In case the Enable function is not required the EN pin should be connected directly to input pin.

### Thermal Shutdown

When the die temperature exceeds the Thermal Shutdown point ( $T_{SD} = 175^\circ\text{C}$  typical) the device goes to disabled state and the output voltage is not delivered until the die temperature decreases to  $150^\circ\text{C}$ . The Thermal Shutdown feature provides a protection from a catastrophic device failure at accidental overheating. This protection is not intended to be used as a substitute for proper heat sinking.

### Power Dissipation and Heat sinking

The maximum power dissipation supported by the device is dependent upon board design and layout. Mounting pad configuration on the PCB, the board material, and the ambient temperature affect the rate of junction temperature rise for the part. The maximum power dissipation the NCV8170 device can handle is given by:

$$P_{D(MAX)} = \frac{[T_{J(MAX)} - T_A]}{R_{\theta JA}} \quad (\text{eq. 1})$$

The power dissipated by the NCV8170 device for given application conditions can be calculated from the following equations:

$$P_D \approx V_{IN}(I_{GND} + I_{OUT}) + I_{OUT}(V_{IN} - V_{OUT}) \quad (\text{eq. 2})$$

or

$$V_{IN(MAX)} \approx \frac{P_{D(MAX)} + (V_{OUT} \times I_{OUT})}{I_{OUT} + I_{GND}} \quad (\text{eq. 3})$$

### Hints

$V_{IN}$  and GND printed circuit board traces should be as wide as possible. When the impedance of these traces is high, there is a chance to pick up noise or cause the regulator to malfunction. Place external components, especially the output capacitor, as close as possible to the NCV8170, and make traces as short as possible.

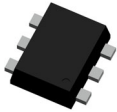
# NCV8170

## ORDERING INFORMATION

Device	Nominal Output Voltage	Marking	Active Discharge	Package	Shipping <sup>†</sup>
NCV8170AMX120TCG (Note 35)	1.2	CC	Yes	XDFN4 1.0 x 1.0 (Pb-Free)	3000 or 5000 / Tape & Reel (Note 35)
NCV8170AMX150TCG (Note 35)	1.5	CJ			
NCV8170AMX180TCG (Note 35)	1.8	CD			
NCV8170AMX250TCG (Note 35)	2.5	CE			
NCV8170AMX280TCG (Note 35)	2.8	CF			
NCV8170AMX300TCG (Note 35)	3.0	CA			
NCV8170AMX310TCG	3.1	CN			
NCV8170AMX330TCG (Note 35)	3.3	CG			
NCV8170AMX360TCG (Note 35)	3.6	CM			
NCV8170BMX120TCG (Note 35)	1.2	3C	No		
NCV8170BMX150TCG (Note 35)	1.5	3J			
NCV8170BMX180TCG (Note 35)	1.8	3D			
NCV8170BMX250TCG (Note 35)	2.5	3E			
NCV8170BMX280TCG (Note 35)	2.8	3F			
NCV8170BMX300TCG	3.0	3A			
NCV8170BMX310TCG (Note 35)	3.1	3Y			
NCV8170BMX330TCG (Note 35)	3.3	3G			
NCV8170BMX360TCG (Note 35)	3.6	3M			
NCV8170AXV120T2G	1.2	CC			
NCV8170AXV150T2G	1.5	CJ			
NCV8170AXV180T2G	1.8	CD			
NCV8170AXV250T2G	2.5	CE			
NCV8170AXV280T2G	2.8	CF			
NCV8170AXV300T2G	3.0	CA			
NCV8170AXV310T2G	3.1	CN			
NCV8170AXV330T2G	3.3	CG			
NCV8170AXV360T2G	3.6	CM			
NCV8170BXV120T2G	1.2	3C	No		
NCV8170BXV150T2G	1.5	3J			
NCV8170BXV180T2G	1.8	3D			
NCV8170BXV250T2G	2.5	3E			
NCV8170BXV280T2G	2.8	3F			
NCV8170BXV300T2G	3.0	3A			
NCV8170BXV310T2G	3.1	3Y			
NCV8170BXV330T2G	3.3	3G			
NCV8170BXV360T2G	3.6	3N			

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

35. Products processed after October 1, 2022 are shipped with quantity 5000 units / tape & reel.

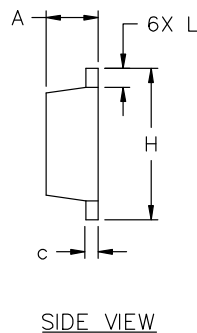
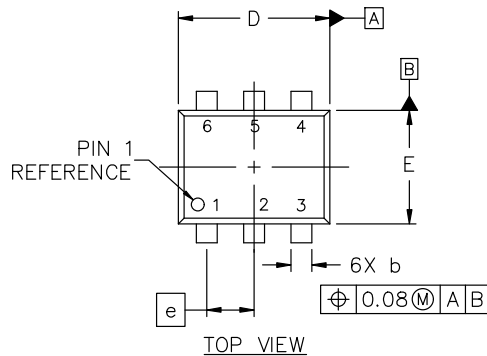


**SOT-563-6 1.60x1.20x0.55, 0.50P**  
**CASE 463A**  
**ISSUE J**

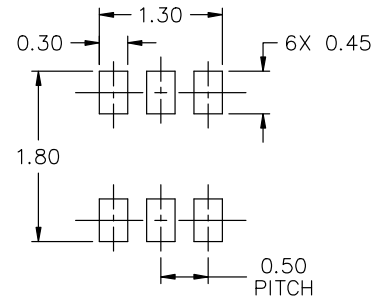
DATE 15 FEB 2024

NOTES:

1. DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018.
2. ALL DIMENSION ARE IN MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.



DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.50	0.55	0.60
b	0.17	0.22	0.27
c	0.08	0.13	0.18
D	1.50	1.60	1.70
E	1.10	1.20	1.30
e	0.50 BSC		
H	1.50	1.60	1.70
L	0.10	0.20	0.30



RECOMMENDED MOUNTING FOOTPRINT\*

\* FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERM/D.

STYLE 1:  
PIN 1. EMITTER 1  
2. BASE 1  
3. COLLECTOR 2  
4. EMITTER 2  
5. BASE 2  
6. COLLECTOR 1

STYLE 2:  
PIN 1. EMITTER 1  
2. EMITTER 2  
3. BASE 2  
4. COLLECTOR 2  
5. BASE 1  
6. COLLECTOR 1

STYLE 3:  
PIN 1. CATHODE 1  
2. CATHODE 1  
3. ANODE/ANODE 2  
4. CATHODE 2  
5. CATHODE 2  
6. ANODE/ANODE 1

STYLE 4:  
PIN 1. COLLECTOR  
2. COLLECTOR  
3. BASE  
4. EMITTER  
5. COLLECTOR  
6. COLLECTOR

STYLE 5:  
PIN 1. CATHODE  
2. CATHODE  
3. ANODE  
4. ANODE  
5. CATHODE  
6. CATHODE

STYLE 6:  
PIN 1. CATHODE  
2. ANODE  
3. CATHODE  
4. CATHODE  
5. CATHODE  
6. CATHODE

STYLE 7:  
PIN 1. CATHODE  
2. ANODE  
3. CATHODE  
4. CATHODE  
5. ANODE  
6. CATHODE

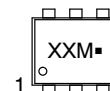
STYLE 8:  
PIN 1. DRAIN  
2. DRAIN  
3. GATE  
4. SOURCE  
5. DRAIN  
6. DRAIN

STYLE 9:  
PIN 1. SOURCE 1  
2. GATE 1  
3. DRAIN 2  
4. SOURCE 2  
5. GATE 2  
6. DRAIN 1

STYLE 10:  
PIN 1. CATHODE 1  
2. N/C  
3. CATHODE 2  
4. ANODE 2  
5. N/C  
6. ANODE 1

STYLE 11:  
PIN 1. EMITTER 2  
2. BASE 2  
3. COLLECTOR 1  
4. EMITTER 1  
5. BASE 1  
6. COLLECTOR 2

**GENERIC  
MARKING DIAGRAM\***



XX = Specific Device Code  
M = Month Code  
▪ = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

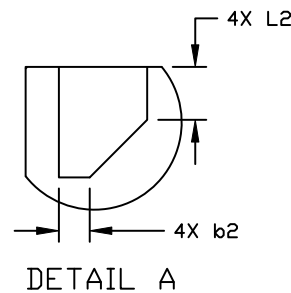
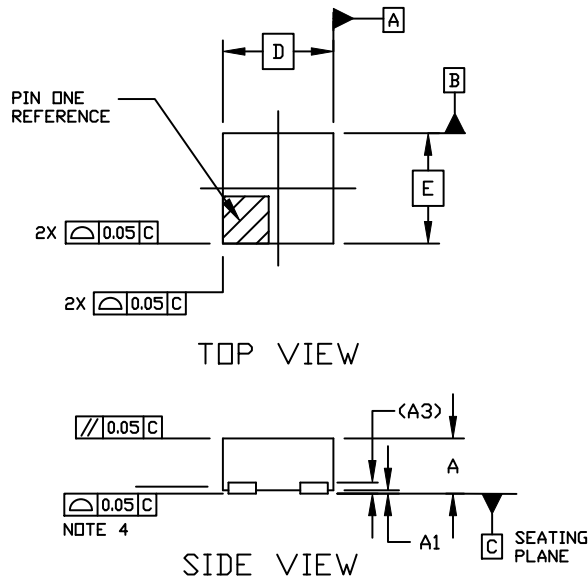
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XDFN4 1.0x1.0, 0.65P  
CASE 711AJ  
ISSUE C

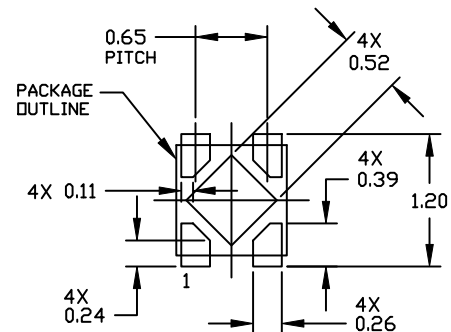
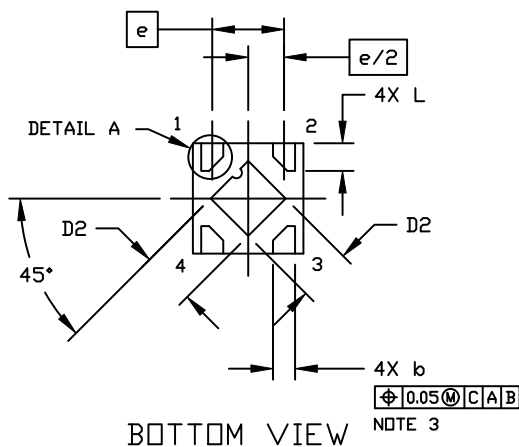
DATE 08 MAR 2022



NOTES:

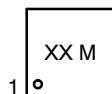
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSION  $b$  APPLIES TO THE PLATED TERMINALS AND IS MEASURED BETWEEN 0.15 AND 0.20 FROM THE TERMINAL TIPS.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

DIM	MILLIMETERS		
	MIN	NOM	MAX
A	0.33	0.38	0.43
A1	0.00	---	0.05
A3	0.10 REF		
b	0.15	0.20	0.25
b2	0.02	0.07	0.12
D	0.90	1.00	1.10
D2	0.43	0.48	0.53
E	0.90	1.00	1.10
e	0.65 BSC		
L	0.20		0.30
L2	0.07		0.17



\* FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ONSEMI SOLDERING AND MOUNT TECHNIQUES REFERENCE MANUAL, SOLDERM/D

GENERIC  
MARKING DIAGRAM\*



XX = Specific Device Code  
M = Date Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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