# NSR30CM3

# Schottky Barrier Diodes, Dual Common Cathode

These Schottky barrier diodes are designed for high speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand-held and portable applications where space is limited.

### **Features**

- Extremely Fast Switching Speed
- Low Forward Voltage -0.35 V (Typ) @  $I_F = 10 \text{ mA}$
- NSVR Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable
- This is a Pb-Free Device

## **MAXIMUM RATINGS** (T<sub>.1</sub> = 125°C unless otherwise noted)

Rating	Symbol	Value	Unit
Reverse Voltage	$V_R$	30	Volts
Forward Power Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>F</sub>	190 1.9	mW mW/°C
Forward Current (DC)	I <sub>F</sub>	200 Max	mA
Junction Temperature	$T_J$	125 Max	°C
Storage Temperature Range	T <sub>stg</sub>	-55 to +150	°C
Thermal Resistance Junction–to–Ambient (Note 1)	$R_{\theta JA}$	525	°C/W

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. FR-4 board with minimum mounting pad.



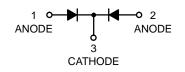
## ON Semiconductor®

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# 30 VOLTS DUAL COMMON CATHODE SCHOTTKY BARRIER DIODES



SOT-723 CASE 631AA STYLE 3



#### **MARKING DIAGRAM**



5C = Specific Device Code

D = Date Code

### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NSR30CM3T5G	SOT-723 (Pb-Free)	8000/Tape & Reel
NSVR30CM3T5G	SOT-723 (Pb-Free)	8000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

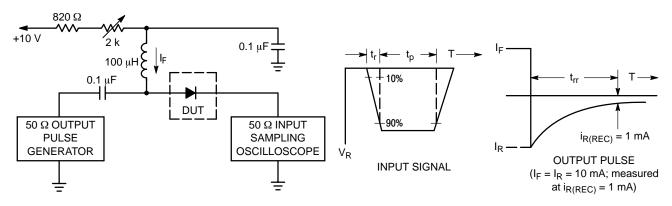
# NSR30CM3

# **ELECTRICAL CHARACTERISTICS** ( $T_A = 25$ °C unless otherwise noted) (EACH DIODE)

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Breakdown Voltage ( $I_R = 10 \mu A$ )	$V_{(BR)R}$	30	-	-	V
Total Capacitance (V <sub>R</sub> = 1.0 V, f = 1.0 MHz)	C <sub>T</sub>	_	7.6	10	pF
Reverse Leakage (V <sub>R</sub> = 25 V)	I <sub>R</sub>	-	0.5	2.0	μΑ
Forward Voltage $(I_F = 0.1 \text{ mA})$ $(I_F = 1.0 \text{ mA})$ $(I_F = 10 \text{ mA})$ $(I_F = 30 \text{ mA})$ $(I_F = 30 \text{ mA})$ $(I_F = 100 \text{ mA})$	V <sub>F</sub>	- - - -	0.22 0.29 0.35 0.41 0.52	0.24 0.32 0.40 0.50 0.80	V
Reverse Recovery Time $(I_F = I_R = 10 \text{ mA}, I_{R(REC)} = 1.0 \text{ mA}, Figure 1)$	t <sub>rr</sub>	-	-	5.0	ns
Forward Current (DC)	I <sub>F</sub>	-	-	200	mA
Repetitive Peak Forward Current	I <sub>FRM</sub>	_	-	300	mA
Non-Repetitive Peak Forward Current (t < 1.0 s)	I <sub>FSM</sub>	_	_	600	mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## NSR30CM3



Notes: 1. A 2.0  $k\Omega$  variable resistor adjusted for a Forward Current (I<sub>F</sub>) of 10 mA.

- 2. Input pulse is adjusted so I<sub>R(peak)</sub> is equal to 10 mA.
- 3. t<sub>p</sub> » t<sub>rr</sub>

Figure 1. Recovery Time Equivalent Test Circuit

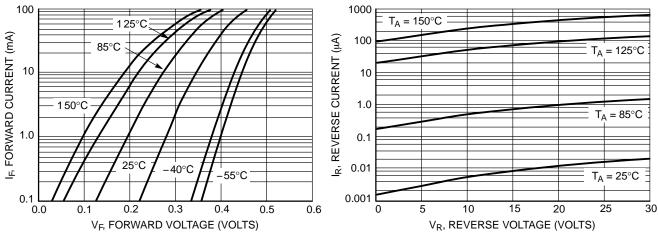


Figure 2. Forward Voltage

Figure 3. Leakage Current

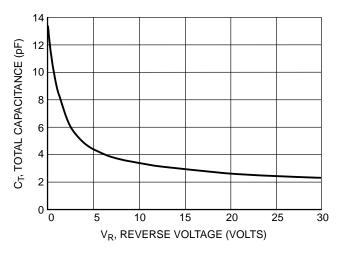


Figure 4. Total Capacitance





### SOT-723 1.20x0.80x0.50, 0.40P CASE 631AA ISSUE E

**DATE 24 JAN 2024** 

MAX.

0.55

0.27

0.37

0.17

1.25

0.85

1.25

MILLIMETERS

 $N\square M$ .

0.50

0.21

0.31

0.12

1.20

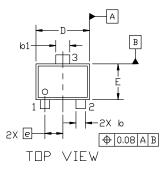
0.80

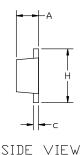
0.40 BSC

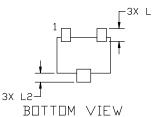
1.20

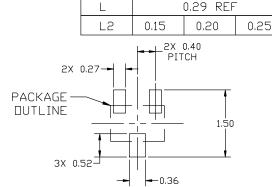
#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018.
- 2. CONTROLLING DIMENSION: MILLIMETERS.
- 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH, MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
- 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.









DIM

Α

b

b1

c D

Ε

e H MIN.

0.45

0.15

0.25

0.07

1.15

0.75

1.15

# RECOMMENDED MOUNTING FOOTPRINT

\*For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.

# GENERIC MARKING DIAGRAM\*



XX = Specific Device Code M = Date Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

STYLE 1:	STYLE 2:	STYLE 3:	STYLE 4:	STYLE 5:
PIN 1. BASE	PIN 1. ANODE	PIN 1. ANODE	PIN 1. CATHODE	PIN 1. GATE
2. EMITTER	2. N/C	2. ANODE	2. CATHODE	<ol><li>SOURCE</li></ol>
<ol><li>COLLECTOR</li></ol>	<ol><li>CATHODE</li></ol>	<ol><li>CATHODE</li></ol>	<ol><li>ANODE</li></ol>	<ol><li>DRAIN</li></ol>

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DESCRIPTION:	SOT-723 1.20x0.80x0.50, 0.40P		PAGE 1 OF 1	

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