

# 40 V, 2.0 A, Low V<sub>CE(sat)</sub> **PNP Transistor**

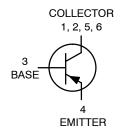
# NSS40200UW6T1G, **NSV40200UW6T1G**

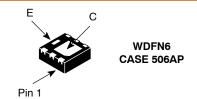
onsemi's e<sup>2</sup>PowerEdge family of low V<sub>CE(sat)</sub> transistors are miniature surface mount devices featuring ultra low saturation voltage (V<sub>CE(sat)</sub>) and high current gain capability. These are designed for use in low voltage, high speed switching applications where affordable efficient energy control is important.

Typical applications are DC-DC converters and power management in portable and battery powered products such as cellular and cordless phones, PDAs, computers, printers, digital cameras and MP3 players. Other applications are low voltage motor controls in mass storage products such as disc drives and tape drives. In the automotive industry they can be used in air bag deployment and in the instrument cluster. The high current gain allows e<sup>2</sup>PowerEdge devices to be driven directly from PMU's control outputs, and the Linear Gain (Beta) makes them ideal components in analog amplifiers.

- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

# -40 VOLTS **2.0 AMPS** PNP LOW V<sub>CE(sat)</sub> TRANSISTOR EQUIVALENT $R_{DS(on)}$ 100 m $\Omega$





#### MARKING DIAGRAM



VA = Specific Device Code

= Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

#### ORDERING INFORMATION

Device	Package	Shipping <sup>†</sup>
NSS40200UW6T1G	WDFN6 (Pb-Free)	3000/ Tape & Reel
NSV40200UW6T1G	WDFN6 (Pb-Free)	3000/ Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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#### **MAXIMUM RATINGS** $(T_A = 25^{\circ}C)$

Rating	Symbol	Max	Unit
Collector-Emitter Voltage	$V_{CEO}$	-40	Vdc
Collector-Base Voltage	$V_{CBO}$	-40	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	-7.0	Vdc
Collector Current – Continuous	Ι <sub>C</sub>	-2.0	Adc
Collector Current - Peak	I <sub>CM</sub>	-4.0	Α
Electrostatic Discharge	ESD	HBM Class 3B MM Class C	

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation, T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub> (Note 1)	875 7.0	mW mW/°C
Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub> (Note 1)	143	°C/W
Total Device Dissipation, T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub> (Note 2)	1.5 11.8	W mW/°C
Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub> (Note 2)	85	°C/W
Thermal Resistance, Junction-to-Lead #1	R <sub>θJL</sub> (Note 2)	23	°C/W
Total Device Dissipation (Single Pulse < 10 sec)	P <sub>Dsingle</sub> (Notes 2 & 3)	3.0	W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	–55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. FR-4 @ 100 mm², 1 oz copper traces.
2. FR-4 @ 500 mm², 1 oz copper traces.
3. Thermal response.

#### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typical	Max	Unit
OFF CHARACTERISTICS	<u> </u>		•	•	•
Collector – Emitter Breakdown Voltage (I <sub>C</sub> = -10 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	-40	-	-	Vdc
Collector – Base Breakdown Voltage (I <sub>C</sub> = -0.1 mAdc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	-40	-	-	Vdc
Emitter – Base Breakdown Voltage $(I_E = -0.1 \text{ mAdc}, I_C = 0)$	V <sub>(BR)EBO</sub>	-7.0	-	-	Vdc
Collector Cutoff Current (V <sub>CB</sub> = -40 Vdc, I <sub>E</sub> = 0)	Ісво	_	-	-0.1	μAdc
Emitter Cutoff Current (V <sub>EB</sub> = -7.0 Vdc)	I <sub>EBO</sub>	_	-	-0.1	μAdc
ON CHARACTERISTICS					
DC Current Gain (Note 4) ( $I_C = -10 \text{ mA}, V_{CE} = -2.0 \text{ V}$ ) ( $I_C = -500 \text{ mA}, V_{CE} = -2.0 \text{ V}$ ) ( $I_C = -1.0 \text{ A}, V_{CE} = -2.0 \text{ V}$ ) ( $I_C = -2.0 \text{ A}, V_{CE} = -2.0 \text{ V}$ )	h <sub>FE</sub>	150 150 150 150	- - - -	- - - -	
Collector – Emitter Saturation Voltage (Note 4) ( $I_C = -0.1 \text{ A}$ , $I_B = -0.010 \text{ A}$ ) (Note 5) ( $I_C = -1.0 \text{ A}$ , $I_B = -0.100 \text{ A}$ ) ( $I_C = -1.0 \text{ A}$ , $I_B = -0.010 \text{ A}$ ) ( $I_C = -2.0 \text{ A}$ , $I_B = -0.020 \text{ A}$ )	V <sub>CE(sat)</sub>	- - - -	- -0.100 - -	-0.020 -0.120 -0.200 -0.300	V
Base – Emitter Saturation Voltage (Note 4) (I <sub>C</sub> = -1.0 A, I <sub>B</sub> = -0.01 A)	V <sub>BE(sat)</sub>	_	-0.76	-0.900	V
Base – Emitter Turn–on Voltage (Note 4) $(I_C = -2.0 \text{ A}, V_{CE} = -3.0 \text{ V})$	V <sub>BE(on)</sub>	_	-0.80	-0.900	V
Cutoff Frequency ( $I_C = -100 \text{ mA}$ , $V_{CE} = -5.0 \text{ V}$ , $f = 100 \text{ MHz}$ )	f <sub>T</sub>	140	-	-	MHz
Input Capacitance (V <sub>EB</sub> = -0.5 V, f = 1.0 MHz)	Cibo	-		500	pF
Output Capacitance (V <sub>CB</sub> = -3.0 V, f = 1.0 MHz)	Cobo	_		100	pF
SWITCHING CHARACTERISTICS					
Delay (V <sub>CC</sub> = 30 V, I <sub>C</sub> = 750 mA, I <sub>B1</sub> = 15 mA)	t <sub>d</sub>	1	_	70	ns
Rise ( $V_{CC} = 30 \text{ V}, I_C = 750 \text{ mA}, I_{B1} = 15 \text{ mA}$ )	t <sub>r</sub>	-	-	150	ns
Storage ( $V_{CC} = 30 \text{ V}, I_{C} = 750 \text{ mA}, I_{B1} = 15 \text{ mA}$ )	t <sub>s</sub>	-	-	525	ns
Fall (V <sub>CC</sub> = 30 V, I <sub>C</sub> = 750 mA, I <sub>B1</sub> = 15 mA)	t <sub>f</sub>	-	-	155	ns
			_		

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Pulsed Condition: Pulse Width = 300 μsec, Duty Cycle ≤ 2%.

5. Guaranteed by design but not tested.

#### TYPICAL CHARACTERISTICS

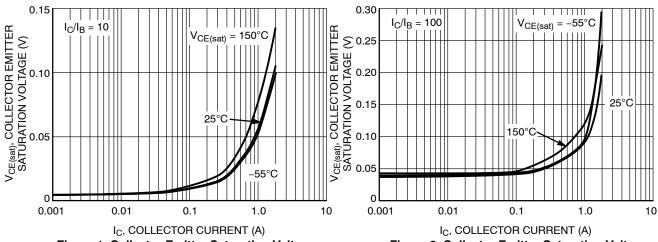


Figure 1. Collector Emitter Saturation Voltage vs. Collector Current

Figure 2. Collector Emitter Saturation Voltage vs. Collector Current

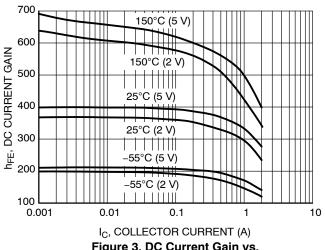


Figure 3. DC Current Gain vs. **Collector Current** 

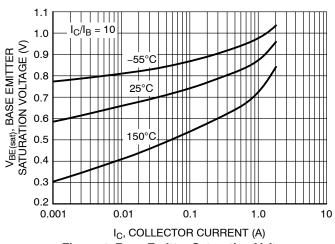


Figure 4. Base Emitter Saturation Voltage vs. **Collector Current** 

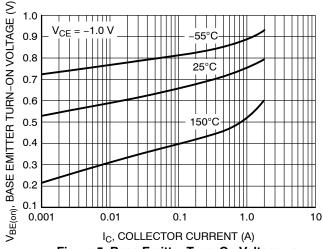


Figure 5. Base Emitter Turn-On Voltage vs. **Collector Current** 

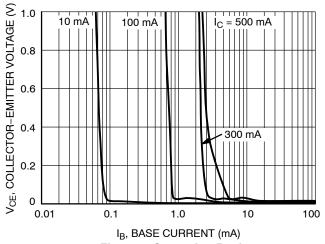
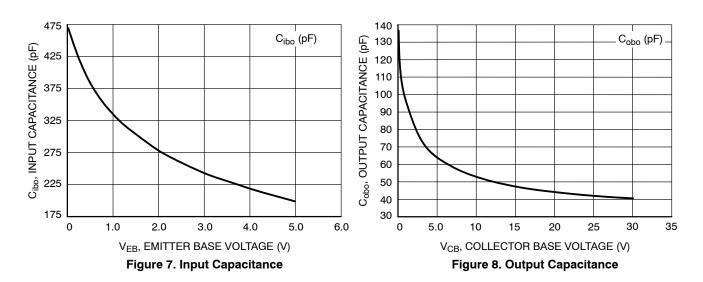


Figure 6. Saturation Region

#### **TYPICAL CHARACTERISTICS**



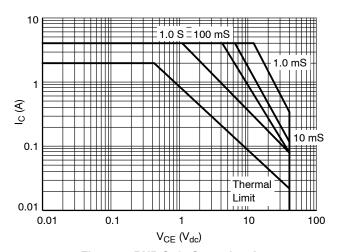


Figure 9. PNP Safe Operating Area



SCALE 4:1

**WDFN6 2x2** CASE 506AP **ISSUE B** 

**DATE 26 APR 2006** 

- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ASME
- 2. CONTROLLING DIMENSION: MILLIMETERS.
- 3. DIMENSION & APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.20mm FROM TERMINAL.
- 4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.
- CENTER TERMINAL LEAD IS OPTIONAL. TERMINAL LEAD IS CONNECTED TO TERMINAL LEAD # 4.
- 2. PINS 1, 2, 5 AND 6 ARE TIED TO THE FLAG.

	MILLIMETERS			
DIM	MIN	MAX		
Α	0.70	0.80		
A1	0.00	0.05		
A3	0.20 REF			
b	0.25	0.35		
b1	0.51	0.61		
D	2.00 BSC			
D2	1.00	1.20		
E	2.00 BSC			
E2	1.10 1.30			
е	0.65 BSC			
K	0.15 REF			
L	0.20	0.30		
L2	0.20	0.30		
J	0.27 REF			
J1	0.65 REF			

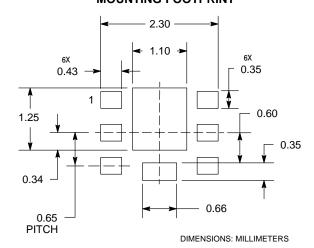
#### **GENERIC MARKING DIAGRAM\***



XX = Specific Device Code = Date Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

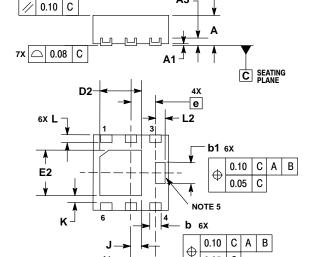
#### **SOLDERMASK DEFINED** MOUNTING FOOTPRINT



DOCUMENT NUMBER:	98AON20860D	Electronic versions are uncontrolled except when accessed directly from the Document Repositor Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.		
DESCRIPTION:	6 PIN WDFN 2X2, 0.65P		PAGE 1 OF 1	

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# В F PIN ONE REFERENCE $\Box$ 0.10 C 0.10



STYLE 1:

- PIN 1. DRAIN
  - DRAIN 2.
  - GATE
  - SOURCE DRAIN
  - 5. 6. DRAIN
- STYLE 2:

**BOTTOM VIEW** 

PIN 1. COLLECTOR

С 0.05

NOTE 3

- COLLECTOR 2.
- 3. BASE
- EMITTER COLLECTOR
- 5.
- COLLECTOR

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