

# Silicon Carbide (SiC) MOSFET - EliteSiC, 32 mohm, 650 V, M3S, D2PAK-7L

## NTBG032N065M3S

### Features

- Typical  $R_{DS(ON)} = 32 \text{ m}\Omega$  @  $V_{GS} = 18 \text{ V}$
- Ultra Low Gate Charge ( $Q_{G(tot)} = 55 \text{ nC}$ )
- High Speed Switching with Low Capacitance ( $C_{oss} = 113 \text{ pF}$ )
- 100% Avalanche Tested
- This Device is Halide Free and RoHS Compliant with Exemption 7a, Pb-Free 2LI (on Second Level Interconnection)

### Applications

- SMPS, Solar Inverters, UPS, Energy Storages, EV charging infrastructure

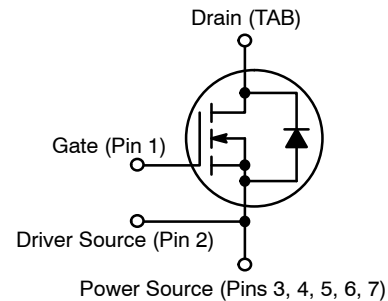
### MAXIMUM RATINGS ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	$V_{DSS}$	650	V
Gate-to-Source Voltage	$V_{GS}$	-8/+22	
Continuous Drain Current	$I_D$	52	A
Power Dissipation	$P_D$	200	W
Continuous Drain Current	$I_D$	32	A
Power Dissipation	$P_D$	100	W
Pulsed Drain Current (Note 1)	$I_{DM}$	156	A
Continuous Source-Drain Current (Body Diode)	$I_S$	30	A
		17	A
Pulsed Source-Drain Current (Body Diode) (Note 1)	$I_{SM}$	127	A
Single Pulse Avalanche Energy (Note 2)	$E_{AS}$	139	mJ
Operating Junction and Storage Temperature	$T_J, T_{stg}$	-55 to 175	$^\circ\text{C}$
Lead Temperature for Soldering Purposes (1/8" from Case for 10 s)	$T_L$	270	$^\circ\text{C}$

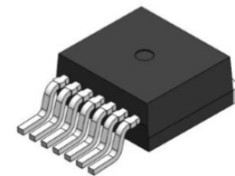
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Single pulse, limited by max junction temperature.
2.  $E_{AS}$  of 139 mJ is based on starting  $T_J = 25^\circ\text{C}$ ,  $L = 1 \text{ mH}$ ,  $I_{AS} = 16.7 \text{ A}$ ,  $V_{DD} = 100 \text{ V}$ ,  $V_{GS} = 18 \text{ V}$ .

$V_{(BR)DSS}$	$R_{DS(ON)} \text{ TYP}$	$I_D \text{ MAX}$
650 V	32 m $\Omega$ @ 18 V	52 A

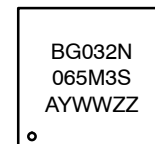


N-CHANNEL MOSFET



D2PAK-7L  
CASE 418BJ

### MARKING DIAGRAM



BG032N065M3S = Specific Device Code  
A = Assembly Location  
Y = Year  
WW = Work Week  
ZZ = Lot Traceability

### ORDERING INFORMATION

Device	Package	Shipping <sup>†</sup>
NTBG032N065M3S	D2PAK-7L	800 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

### THERMAL CHARACTERISTICS

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case (Note 3)	$R_{\theta JC}$	0.75	°C/W
Thermal Resistance, Junction-to-Ambient (Note 3)	$R_{\theta JA}$	40	

3. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.

### RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Value	Unit
Operation Values of Gate-to-Source Voltage	$V_{GSop}$	-5...-3 +18	V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

### ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
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#### OFF CHARACTERISTICS

Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{ V}, I_D = 1\text{ mA}, T_J = 25^\circ\text{C}$	650	-	-	V
Drain-to-Source Breakdown Voltage Temperature Coefficient	$\Delta V_{(BR)DSS} / \Delta T_J$	$I_D = 1\text{ mA}$ , Referenced to $25^\circ\text{C}$	-	90	-	mV/°C
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 650\text{ V}, T_J = 25^\circ\text{C}$	-	-	10	$\mu\text{A}$
		$V_{DS} = 650\text{ V}, T_J = 175^\circ\text{C}$ (Note 5)	-	-	500	$\mu\text{A}$
Gate-to-Source Leakage Current	$I_{GSS}$	$V_{GS} = -8/+22\text{ V}, V_{DS} = 0\text{ V}$	-	-	$\pm 1.0$	$\mu\text{A}$

#### ON CHARACTERISTICS

Drain-to-Source On Resistance	$R_{DS(ON)}$	$V_{GS} = 18\text{ V}, I_D = 15\text{ A}, T_J = 25^\circ\text{C}$	-	32	44	m $\Omega$
		$V_{GS} = 18\text{ V}, I_D = 15\text{ A}, T_J = 175^\circ\text{C}$ (Note 5)	-	49	-	
		$V_{GS} = 15\text{ V}, I_D = 15\text{ A}, T_J = 25^\circ\text{C}$	-	41	-	
		$V_{GS} = 15\text{ V}, I_D = 15\text{ A}, T_J = 175^\circ\text{C}$ (Note 5)	-	52	-	
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D = 7.5\text{ mA}, T_J = 25^\circ\text{C}$	2.0	2.7	4.0	V
Forward Trans-conductance	$g_{FS}$	$V_{DS} = 10\text{ V}, I_D = 15\text{ A}$ (Note 5)	-	9.9	-	S

#### CHARGES, CAPACITANCES & GATE RESISTANCE

Input Capacitance	$C_{ISS}$	$V_{DS} = 400\text{ V}, V_{GS} = 0\text{ V}, f = 1\text{ MHz}$ (Note 5)	-	1409	-	pF
Output Capacitance	$C_{OSS}$		-	113	-	
Reverse Transfer Capacitance	$C_{RSS}$		-	9.0	-	
Total Gate Charge	$Q_{G(TOT)}$	$V_{DD} = 400\text{ V}, I_D = 15\text{ A}, V_{GS} = -3/18\text{ V}$ (Note 5)	-	55	-	nC
Gate-to-Source Charge	$Q_{GS}$		-	15	-	
Gate-to-Drain Charge	$Q_{GD}$		-	14	-	
Gate Resistance	$R_G$	$f = 1\text{ MHz}$	-	5.0	-	$\Omega$

#### SWITCHING CHARACTERISTICS

Turn-On Delay Time	$t_{d(ON)}$	$V_{GS} = -3/18\text{ V}, I_D = 15\text{ A}, V_{DD} = 400\text{ V}, R_G = 4.7\text{ }\Omega, T_J = 25^\circ\text{C}$ (Note 4, 5)	-	8.8	-	ns
Turn-Off Delay Time	$t_{d(OFF)}$		-	31	-	
Rise Time	$t_r$		-	12	-	
Fall Time	$t_f$		-	9	-	
Turn-On Switching Loss	$E_{ON}$		-	33	-	$\mu\text{J}$
Turn-Off Switching Loss	$E_{OFF}$		-	16	-	
Total Switching Loss	$E_{TOT}$		-	49	-	

# NTBG032N065M3S

## ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ unless otherwise specified) (continued)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
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### SWITCHING CHARACTERISTICS

Turn-On Delay Time	$t_{d(ON)}$	$V_{GS} = -3/18\text{ V}$ , $I_D = 15\text{ A}$ , $V_{DD} = 400\text{ V}$ , $R_G = 4.7\ \Omega$ , $T_J = 175^\circ\text{C}$ (Note 4, 5)	–	7.8	–	ns
Turn-Off Delay Time	$t_{d(OFF)}$		–	37	–	
Rise Time	$t_r$		–	12	–	
Fall Time	$t_f$		–	11	–	
Turn-On Switching Loss	$E_{ON}$		–	31	–	$\mu\text{J}$
Turn-Off Switching Loss	$E_{OFF}$		–	25	–	
Total Switching Loss	$E_{TOT}$		–	56	–	

### SOURCE-TO-DRAIN DIODE CHARACTERISTICS

Forward Diode Voltage	$V_{SD}$	$I_{SD} = 15\text{ A}$ , $V_{GS} = -3\text{ V}$ , $T_J = 25^\circ\text{C}$	–	4.5	6.0	V
		$I_{SD} = 15\text{ A}$ , $V_{GS} = -3\text{ V}$ , $T_J = 175^\circ\text{C}$ (Note 5)	–	4.2	–	
Reverse Recovery Time	$t_{RR}$	$V_{GS} = -3\text{ V}$ , $I_S = 15\text{ A}$ , $di/dt = 1000\text{ A}/\mu\text{s}$ , $V_{DS} = 400\text{ V}$ , $T_J = 25^\circ\text{C}$ (Note 5)	–	15.5	–	ns
Charge time	$t_a$		–	8.9	–	
Discharge time	$t_b$		–	6.6	–	
Reverse Recovery Charge	$Q_{RR}$		–	72	–	nC
Reverse Recovery Energy	$E_{REC}$		–	4.6	–	$\mu\text{J}$
Peak Reverse Recovery Current	$I_{RRM}$		–	9.3	–	A

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. EON/EOFF result is with body diode.

5. Defined by design, not subject to production test.

TYPICAL CHARACTERISTICS

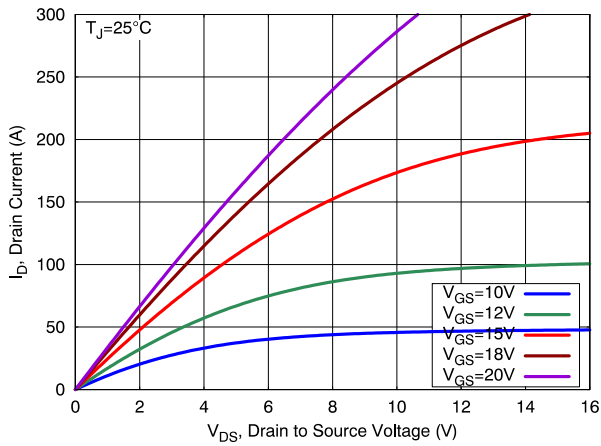


Figure 1. Output Characteristics

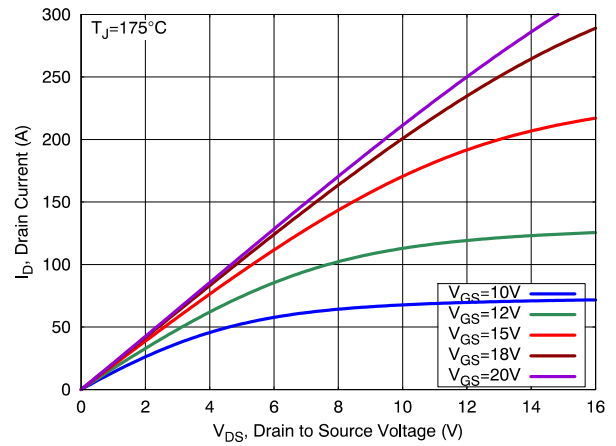


Figure 2. Output Characteristics

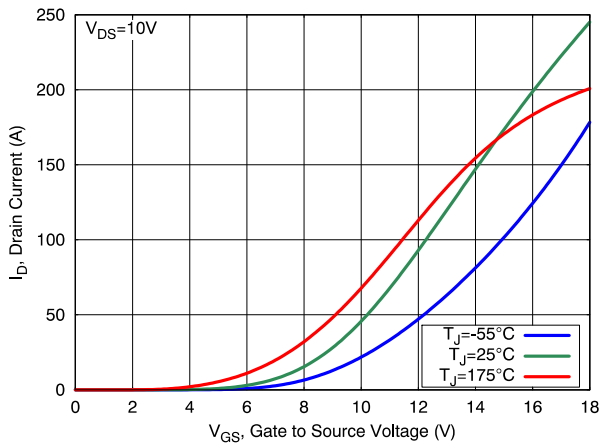


Figure 3. Transfer Characteristics

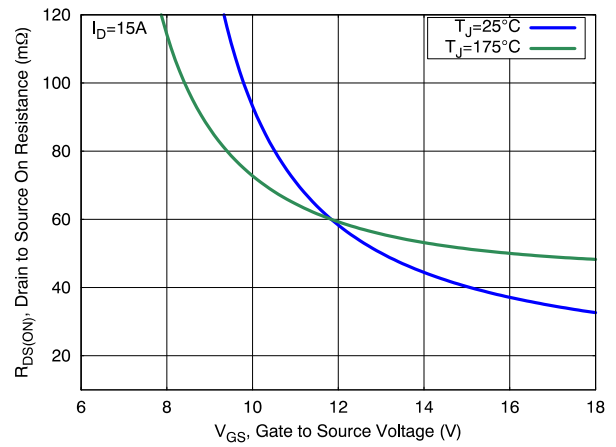


Figure 4. On-Resistance vs. Gate Voltage

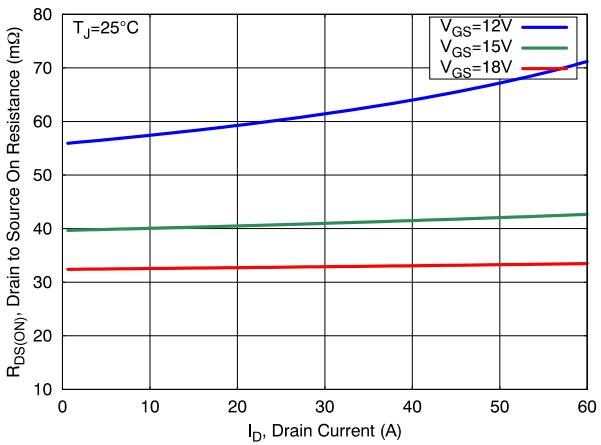


Figure 5. On-Resistance vs. Drain Current

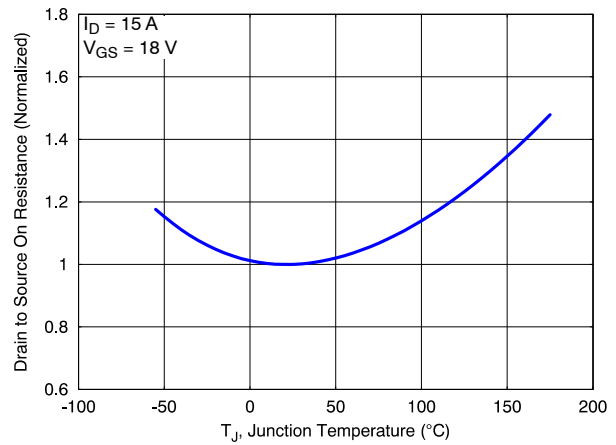


Figure 6. On-Resistance vs. Junction Temperature

TYPICAL CHARACTERISTICS

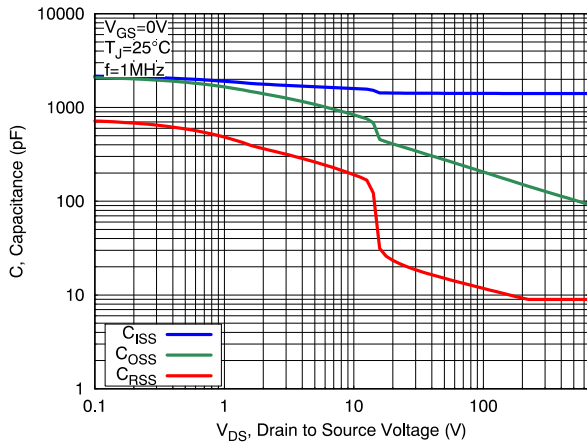


Figure 7. Capacitance Characteristics

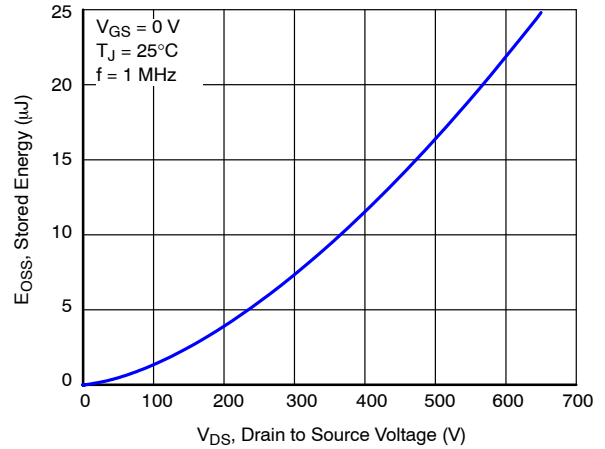


Figure 8. Stored Energy vs. Drain-to-Source Voltage

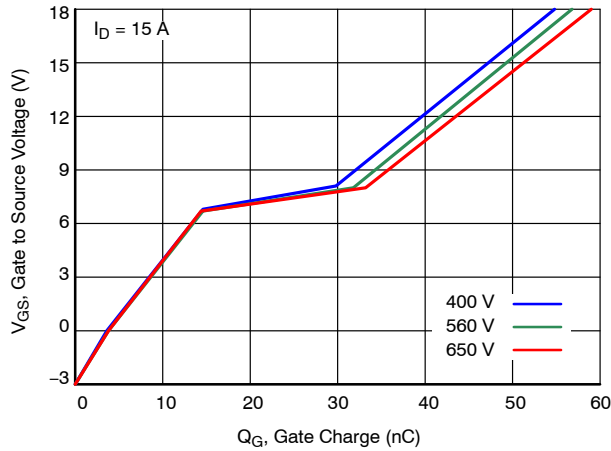


Figure 9. Gate Charge Characteristics

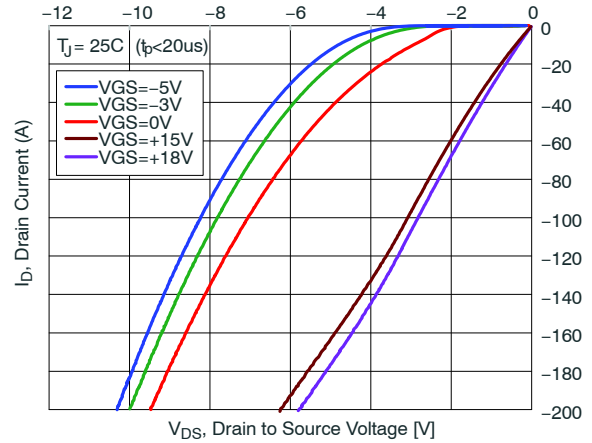


Figure 10. Reverse Conduction Characteristics

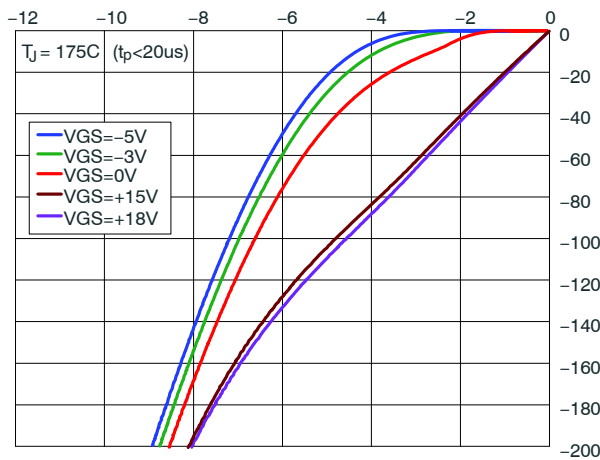


Figure 11. Reverse Conduction Characteristics

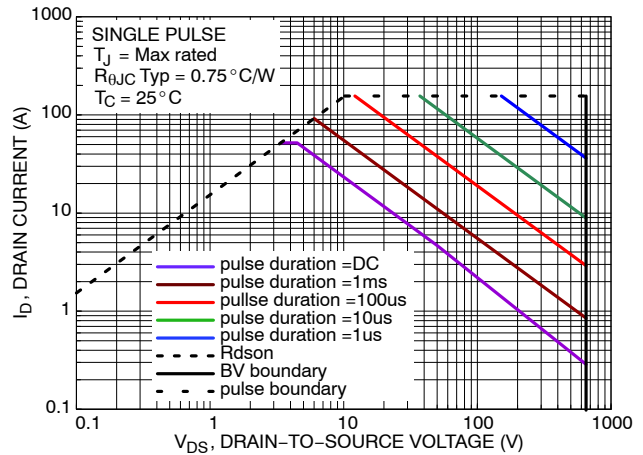


Figure 12. Safe Operating Area

TYPICAL CHARACTERISTICS

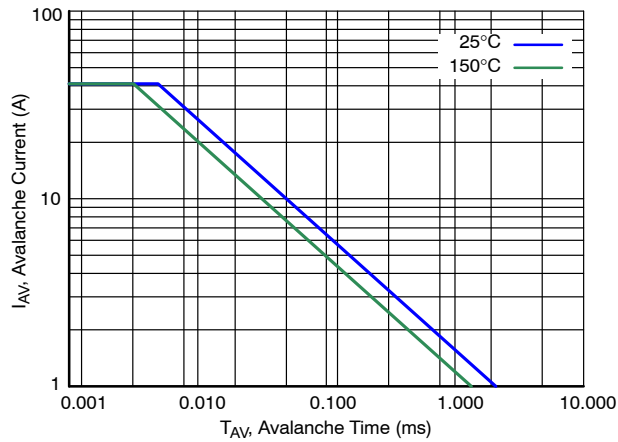


Figure 13. Avalanche Current vs. Pulse Time (UIS)

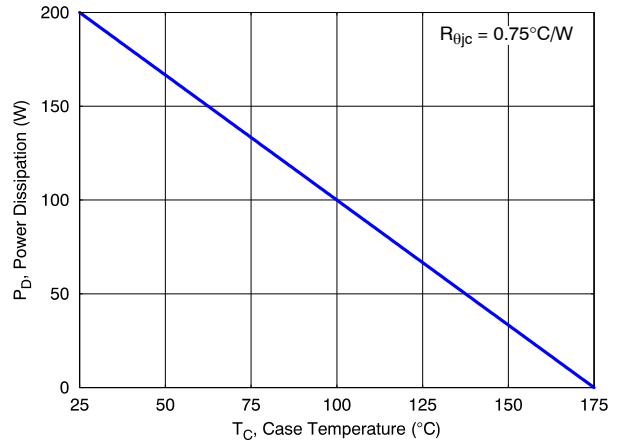


Figure 14. Maximum Power Dissipation vs. Case Temperature

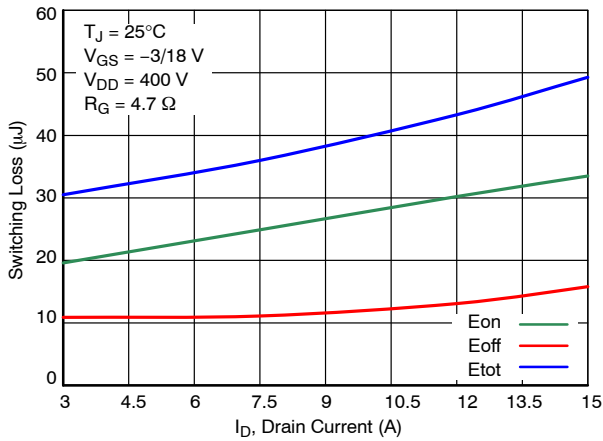


Figure 15. Inductive Switching Loss vs. Drain Current

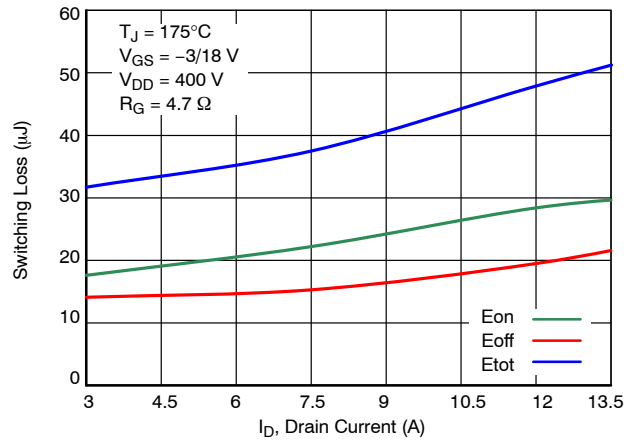


Figure 16. Inductive Switching Loss vs. Drain Current

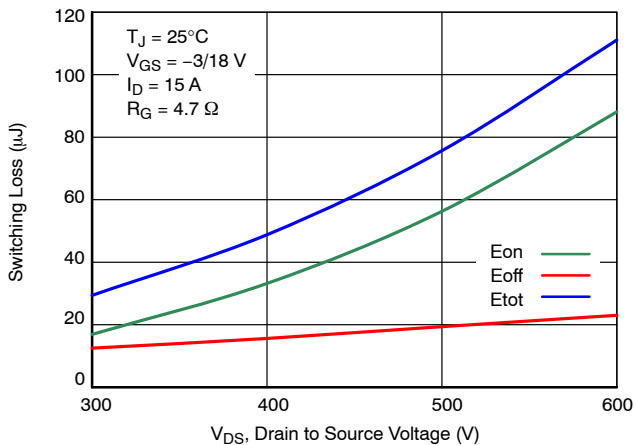


Figure 17. Inductive Switching Loss vs. Drain Voltage

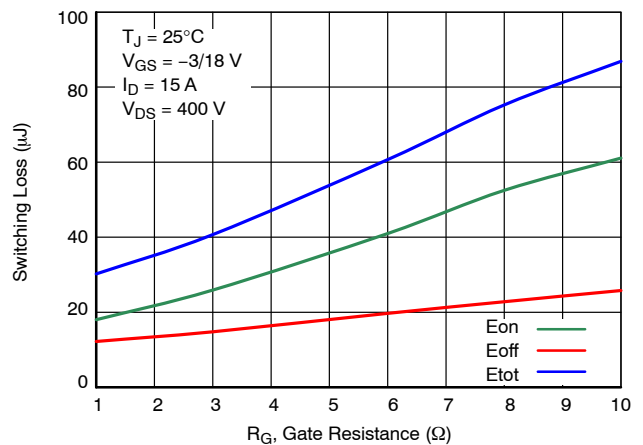


Figure 18. Inductive Switching Loss vs. Gate Resistance

TYPICAL CHARACTERISTICS

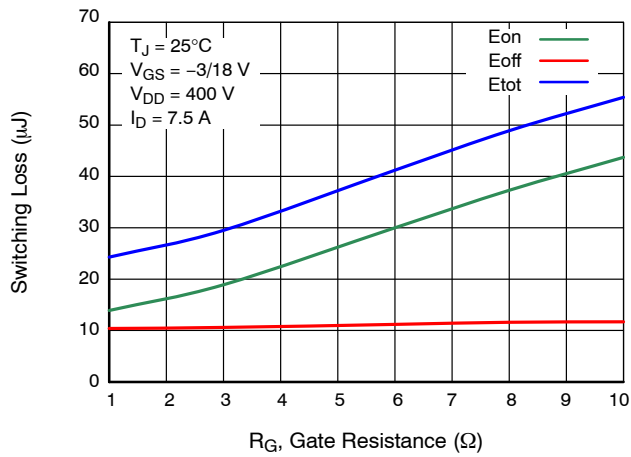


Figure 19. Inductive Switching Loss vs. Gate Resistance

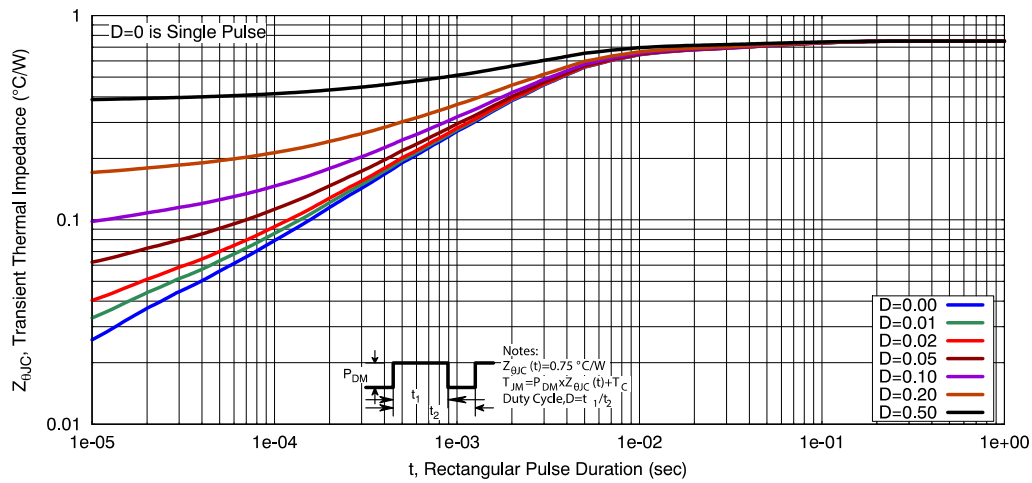
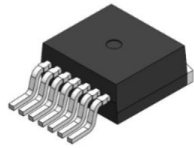
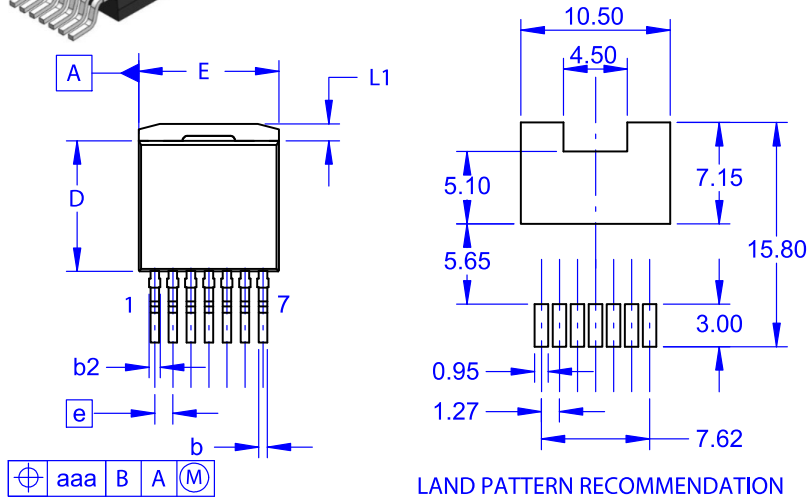


Figure 20. Thermal Response Characteristics

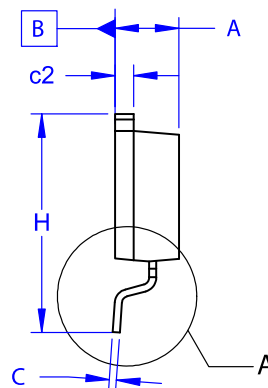
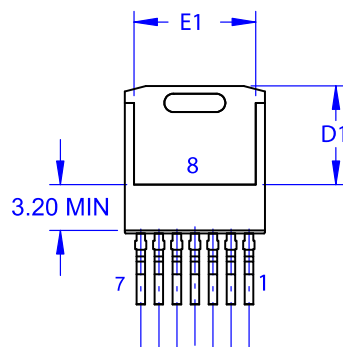
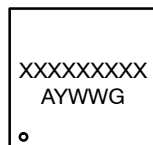

**D<sup>2</sup>PAK7 (TO-263-7L HV)**  
CASE 418BJ  
ISSUE B

DATE 16 AUG 2019


**NOTES:**

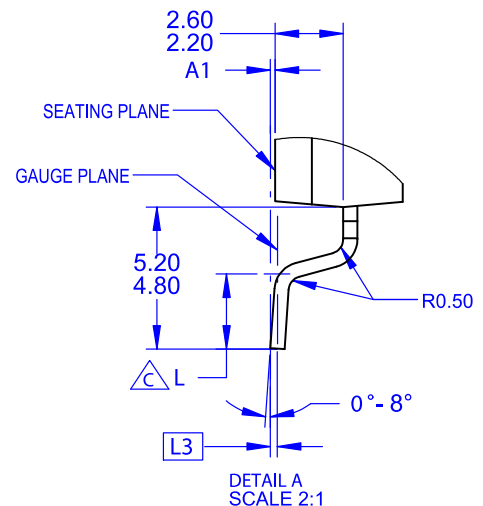
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B. ALL DIMENSIONS ARE IN MILLIMETERS.  
C. OUT OF JEDEC STANDARD VALUE.  
D. DIMENSION AND TOLERANCE AS PER ASME Y14.5-2009.  
E. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.

DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.30	4.50	4.70
A1	0.00	0.10	0.20
b2	0.60	0.70	0.80
b	0.51	0.60	0.70
c	0.40	0.50	0.60
c2	1.20	1.30	1.40
D	9.00	9.20	9.40
D1	6.15	6.80	7.15
E	9.70	9.90	10.20
E1	7.15	7.65	8.15
e	~	1.27	~
H	15.10	15.40	15.70
L	2.44	2.64	2.84
L1	1.00	1.20	1.40
L3	~	0.25	~
aaa	~	~	0.25


**GENERIC MARKING DIAGRAM\***


XXXX = Specific Device Code  
A = Assembly Location  
Y = Year  
WW = Work Week  
G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



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