onsemi

<u>Silicon Carbide (SiC)</u> <u>MOSFET</u> – EliteSiC, 40 mohm, 1200 V, M1, TO-247-4L

NTH4L040N120SC1

Features

- Typ. $R_{DS(on)} = 40 \text{ m}\Omega$
- Ultra Low Gate Charge ($Q_{G(tot)} = 106 \text{ nC}$)
- High Speed Switching with Low Capacitance (Coss = 137 pF)
- 100% Avalanche Tested
- $T_J = 175^{\circ}C$
- This Device is Halide Free and RoHS Compliant with exemption 7a, Pb–Free 2LI (on second level interconnection)

Typical Applications

- UPS
- DC-DC Converter
- Boost Inverter

MAXIMUM RATINGS (T_J = 25° C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage		V _{DSS}	1200	V	
Gate-to-Source Voltage			V _{GS}	-15/+25	V
Recommended Operation Values T _C < 175°C of Gate-to-Source Voltage		T _C < 175°C	V _{GSop}	-5/+20	V
Continuous Drain Current (Note 2)	$\begin{array}{c c} Steady \\ State \end{array} T_C = 25^\circ C \\ \end{array}$		Ι _D	58	A
Power Dissipation (Note 2)			PD	319	W
Continuous Drain Current (Notes 1, 2)	Steady State	T _C = 100°C	Ι _D	41	A
Power Dissipation (Notes 1, 2)			PD	160	W
Pulsed Drain Current (Note 3)	T _A = 25°C		I _{DM}	232	A
Operating Junction and Storage Temperature Range			T _J , T _{stg}	–55 to +175	°C
Source Current (Body Diode)			IS	32	А
Single Pulse Drain-to-Source Avalanche Energy (I _{L(pk)} = 34 A, L = 1 mH) (Note 4)			E _{AS}	578	mJ
Maximum Lead Temperature for Soldering (1/8" from case for 5 s)		ΤL	300	°C	

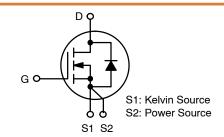
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. JA is constant value to follow guide table of LV/HV discrete final datasheet generation.

 The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
Repetitive rating, limited by max junction temperature.

4. EAS of 578 mJ is based on starting $T_J = 25^{\circ}$ C; L = 1 mH, $I_{AS} = 34$ A, $V_{DD} = 120$ V, $V_{GS} = 20$ V.

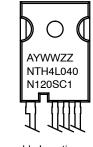
V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
1200 V	56 mΩ @ 20 V	58 A



N-CHANNEL MOSFET



MARKING DIAGRAM



A = Assembly Location

Y = Year

WW = Work Week

ZZ = Lot Traceability

NTH4L040N120SC1 = Specific Device Code

ORDERING INFORMATION

Device	Package	Shipping
NTH4L040N120SC1	TO-247-4LD	30 Units / Tube

Table 1. THERMAL RESISTANCE MAXIMUM RATINGS

Parameter	Symbol	Мах	Unit
Junction-to-Case - Steady State (Note 2)	$R_{\theta JC}$	0.47	°C/W
Junction-to-Ambient - Steady State (Notes 1, 2)	$R_{\theta JA}$	40	

Table 2. ELECTRICAL CHARACTERISTICS (T_J = 25° C unless otherwise specified)

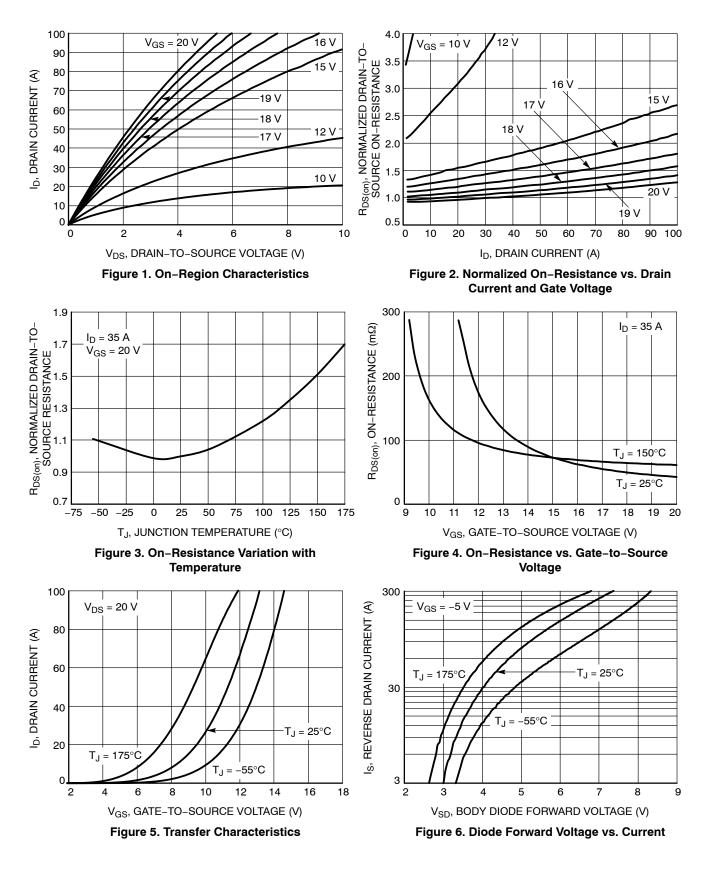
Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS	•						-
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I_D = 1 mA		1200	-	-	V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	$I_D = 1 \text{ mA}$, referenced to 25°C		-	0.45	-	V/∘C
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V,$	$T_J = 25^{\circ}C$	-	-	100	μA
		V _{DS} = 1200 V	$T_J = 175^{\circ}C$	-	-	1	mA
Gate-to-Source Leakage Current	I _{GSS}	$V_{GS} = +25/-15 \text{ V}, \text{ V}_{DS}$	_S = 0 V	-	-	±1	μA
ON CHARACTERISTICS (Note 3)							-
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_{D} = 10 \text{ m/}$	٩	1.8	3	4.3	V
Recommended Gate Voltage	V _{GOP}			-5	-	+20	V
Drain-to-Source On Resistance	R _{DS(on)}	$V_{GS} = 20 \text{ V}, \text{ I}_{D} = 35 \text{ A},$	$T_J = 25^{\circ}C$	-	40	56	mΩ
		$V_{GS} = 20 \text{ V}, \text{ I}_{D} = 35 \text{ A},$	T _J = 175°C	-	70	100	
Forward Transconductance	9fs	$V_{DS} = 20 \text{ V}, \text{ I}_{D} = 35 \text{ A}$		-	20	-	S
CHARGES, CAPACITANCES & GATE RES	ISTANCE						
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1 MHz, V _{DS} = 800 V		-	1762	-	pF
Output Capacitance	C _{OSS}			_	137	-	
Reverse Transfer Capacitance	C _{RSS}			_	11	-	
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = -5/20$ V, $V_{DS} = 600$ V, $I_D = 47$ A		_	106	-	nC
Threshold Gate Charge	Q _{G(TH)}			-	16	-	
Gate-to-Source Charge	Q _{GS}			-	34	-	
Gate-to-Drain Charge	Q _{GD}			-	26	-	
Gate-Resistance	R _G	f = 1 MHz		-	2.4	-	Ω
SWITCHING CHARACTERISTICS, VGS =	10 V						
Turn–On Delay Time	t _{d(ON)}	$V_{GS} = -5/20$ V, $V_{DS} =$	800 V,	-	17	30	ns
Rise Time	t _r	$I_D = 47 \text{ A}, \text{ R}_G = 4.7 \Omega$ Inductive load		-	20	36	
Turn-Off Delay Time	t _{d(OFF)}			-	32	51	
Fall Time	t _f			-	10	20	
Turn–On Switching Loss	E _{ON}			-	411	-	μJ
Turn–Off Switching Loss	E _{OFF}			-	205	-	
Total Switching Loss	E _{tot}			-	616	-	
DRAIN-SOURCE DIODE CHARACTERIST	ICS						-
Continuous Drain-Source Diode Forward Current	I _{SD}	V _{GS} = -5 V, T _J = 25°C		-	-	32	A
Pulsed Drain-Source Diode Forward Current (Note 3)	I _{SDM}			_	_	232	
Forward Diode Voltage	V _{SD}	V _{GS} = -5 V, I _{SD} = 17.5	$5 \text{ A}, \text{ T}_{\text{J}} = 25^{\circ}\text{C}$	-	3.7	-	V
Reverse Recovery Time	t _{RR}	$V_{GS} = -5/20 \text{ V}, I_{SD} = 4$	17 A,	-	24	-	ns
Reverse Recovery Charge	Q _{RR}	dl _S /dt = 1000 A/µs		-	124.8	-	nC

Table 2. ELECTRICAL CHARACTERISTICS (T_J = 25° C unless otherwise specified) (continued)

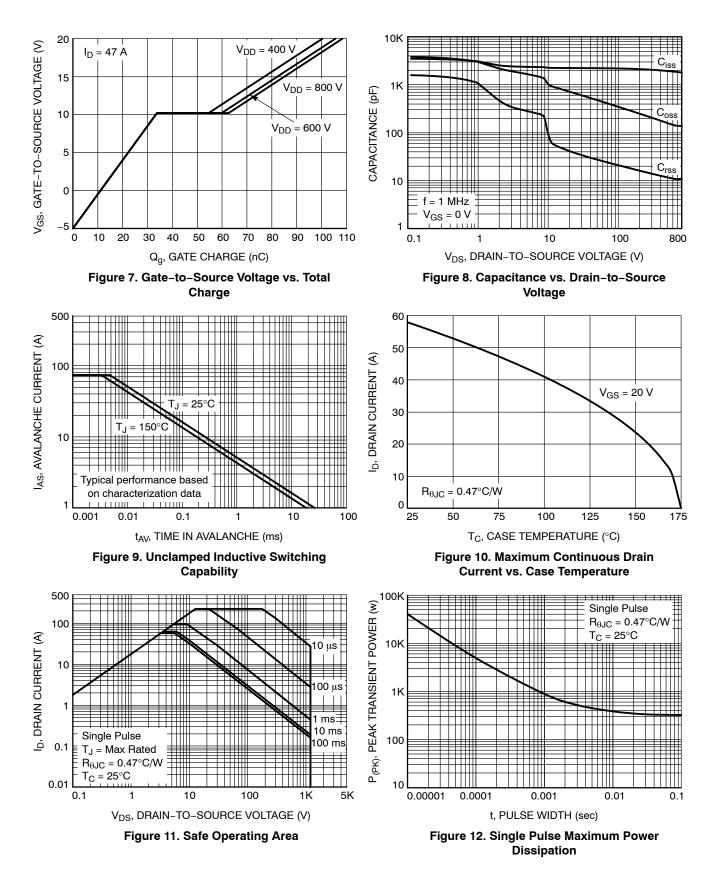
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit	
DRAIN-SOURCE DIODE CHARACTERISTICS							
Reverse Recovery Energy	E _{REC}		-	8.4	-	Lμ	
Peak Reverse Recovery Current	I _{RRM}		-	10.4	-	Α	
Charge Time	Та		-	12.4	-	ns	
Discharge Time	Tb		-	11.6	-	ns	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (CONTINUED)



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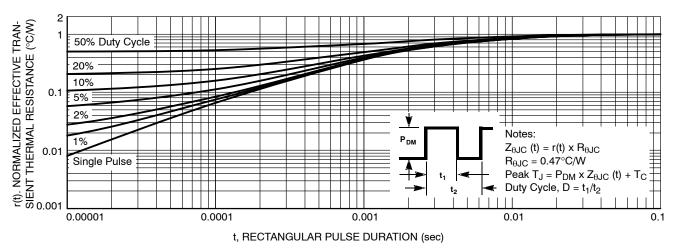


Figure 13. Junction-to-Ambient Thermal Response



TO-247-4LD CASE 340CJ **ISSUE A**

DATE 16 SEP 2019

NOM

5.00

2.40

2.00

1.20

1.40

2.22

0.60

22.54

16.25

1.17

2.54 BSC

5.08 BSC

15.60

13.00

5.00

18.42

2.62

3.60

6.80

6.17

6.17

3.40

6.60

5.97

5.97

р p1

Q

S

MAX

5.20

2.70

2.20

1.33

1.60

2.42

0.70

22.74

16.50

1.37

15.80

13.20

5.20

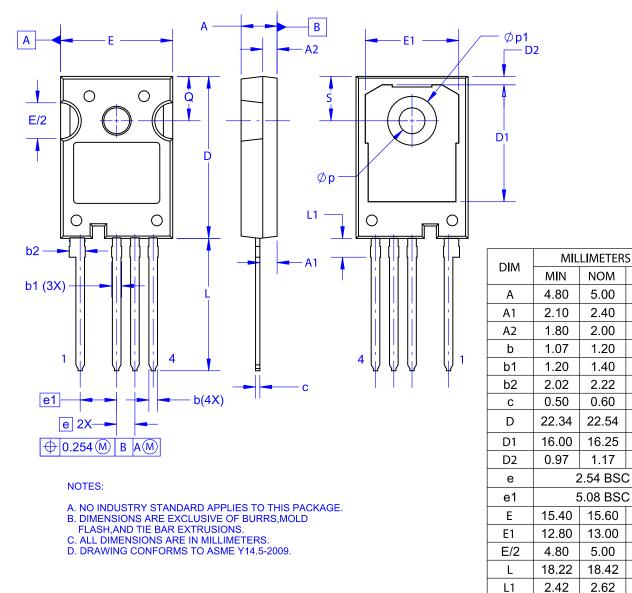
18.62

2.82

3.80

7.00 6.37

6.37



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