

# Silicon Carbide (SiC) **MOSFET** - EliteSiC, 65 mohm, 1200 V, M3S, TO-247-4L NTH4L070N120M3S

#### **Features**

- Typ.  $R_{DS(on)} = 65 \text{ m}\Omega @ V_{GS} = 18 \text{ V}$
- Ultra Low Gate Charge  $(Q_{G(tot)} = 57 \text{ nC})$
- High Speed Switching with Low Capacitance ( $C_{oss} = 57 \text{ pF}$ )
- 100% Avalanche Tested
- This Device is Halide Free and RoHS Compliant with exemption 7a, Pb-Free 2LI (on second level interconnection)

## **Typical Applications**

- Solar Inverters
- Electric Vehicle Charging Stations
- UPS (Uninterruptible Power Supplies)
- Energy Storage Systems
- SMPS (Switch Mode Power Supplies)

# MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise noted)

| Parameter  |                              |                                   | Symbol          | Value   | Unit |
|--|------------------------------|-----------------------------------|-----------------|---------|------|
| Drain-to-Source Voltage  |                              |                                   | $V_{DSS}$       | 1200    | ٧    |
| Gate-to-Source Voltage   |                              |                                   | $V_{GS}$        | -10/+22 | ٧    |
| Continuous Drain<br>Current (Notes 1, 3)                                     | Steady T <sub>C</sub> =25°C  |                                   | I <sub>D</sub>  | 34      | Α    |
| Power Dissipation (Note 1)   |                              |                                   | $P_{D}$         | 160     | V    |
| Continuous Drain<br>Current (Notes 1, 3)                                     | Steady T <sub>C</sub> =100°C |                                   | I <sub>D</sub>  | 24      | Α    |
| Power Dissipation (Note 1)   |                              |                                   | P <sub>D</sub>  | 80      | W    |
| Pulsed Drain Current<br>(Note 2)   | T <sub>C</sub> = 25°C        |                                   | I <sub>DM</sub> | 98      | Α    |
| Operating Junction and Storage Temperature Range                             |                              | T <sub>J</sub> , T <sub>stg</sub> | -55 to<br>+175  | °C      |      |
| Source Current (Body Diode)<br>T <sub>C</sub> = 25°C, V <sub>GS</sub> = -3 V |                              |                                   | I <sub>S</sub>  | 31      | Α    |
| Single Pulse Drain-to-Source Avalanche Energy (Note 4)                       |                              | E <sub>AS</sub>                   | 91              | mJ      |      |
| Maximum Lead Temperature for Soldering (1/25" from case for 10 s)            |                              |                                   | TL              | 270     | °C   |

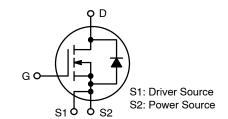
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.

1

- 2. Repetitive rating, limited by max junction temperature.
- 3. The maximum current rating is based on typical  $R_{DS(on)}$  performance. 4. EAS of 91 mJ is based on starting  $T_J = 25^{\circ}C$ ; L = 1 mH,  $I_{AS} = 13.5$  A,  $V_{DD} = 100 \text{ V}, V_{GS} = 18 \text{ V}.$

| V <sub>(BR)DSS</sub> | R <sub>DS(ON)</sub> MAX | I <sub>D</sub> MAX |
|----------------------|-------------------------|--------------------|
| 1200 V               | 87 mΩ @ 18 V            | 34 A               |

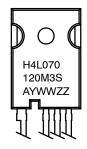


**N-CHANNEL MOSFET** 



CASE 340CJ

#### **MARKING DIAGRAM**



H4L070120M3S = Specific Device Code

= Assembly Location

= Year WW = Work Week = Lot Traceability

#### **ORDERING INFORMATION**

| Device          | Package   | Shipping           |
|-----------------|-----------|--------------------|
| NTH4L070N120M3S | TO-247-4L | 30 Units /<br>Tube |

#### **Table 1. THERMAL CHARACTERISTICS**

| Parameter                                   |                 | Max  | Unit |
|---|-----------------|------|------|
| Junction-to-Case - Steady State (Note 1)    |                 | 0.94 | °C/W |
| Junction-to-Ambient - Steady State (Note 1) | $R_{\theta JA}$ | 40   |      |

## **Table 2. RECOMMENDED OPERATING CONDITIONS**

| Parameter                                  | Symbol     | Value       | Unit |
|--|------------|-------------|------|
| Operation Values of Gate-to-Source Voltage | $V_{GSop}$ | −5−3<br>+18 | V    |

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

Table 3. ELECTRICAL CHARACTERISTICS (T<sub>1</sub> = 25°C unless otherwise specified)

| Parameter  | Symbol                               | Test Condi  | tion                     | Min  | Тур  | Max | Unit |
|--|--------------------------------------|---|--------------------------|------|------|-----|------|
| OFF-STATE CHARACTERISTICS                                    | •                                    |   |                          |      |      |     |      |
| Drain-to-Source Breakdown Voltage                            | V <sub>(BR)DSS</sub>                 | V <sub>GS</sub> = 0 V, I <sub>D</sub> = 1 mA                                      |                          | 1200 | -    | -   | V    |
| Drain-to-Source Breakdown Voltage<br>Temperature Coefficient | V <sub>(BR)DSS</sub> /T <sub>J</sub> | I <sub>D</sub> = 1 mA, reference<br>(Note 6)                                      |                          | =    | 0.3  | -   | V/°C |
| Zero Gate Voltage Drain Current                              | I <sub>DSS</sub>                     | V <sub>GS</sub> = 0 V,<br>V <sub>DS</sub> = 1200 V                                | T <sub>J</sub> = 25°C    | -    | -    | 100 | μΑ   |
| Gate-to-Source Leakage Current                               | I <sub>GSS</sub>                     | $V_{GS} = +22/-10 \text{ V},$   | V <sub>DS</sub> = 0 V    | -    | -    | ±1  | μΑ   |
| ON-STATE CHARACTERISTICS (Note 2                             | 2)                                   |   |                          |      |      |     |      |
| Gate Threshold Voltage                                       | V <sub>GS(TH)</sub>                  | $V_{GS} = V_{DS}, I_D$  | = 7 mA                   | 2.04 | 2.9  | 4.4 | V    |
| Drain-to-Source On Resistance                                | R <sub>DS(on)</sub>                  | V <sub>GS</sub> = 18 V, I <sub>D</sub> = 15                                       | A, T <sub>J</sub> = 25°C | -    | 65   | 87  | mΩ   |
|  |                                      | V <sub>GS</sub> = 18 V, I <sub>D</sub> = 15 A, T <sub>J</sub> = 175°C<br>(Note 6) |                          | -    | 136  | -   |      |
| Forward Transconductance                                     | 9 <sub>FS</sub>                      | V <sub>DS</sub> = 10 V, I <sub>D</sub> = 15 A (Note 6)                            |                          | -    | 12   | -   | S    |
| CHARGES, CAPACITANCES & GATE R                               | ESISTANCE                            |   |                          |      |      |     |      |
| Input Capacitance  | C <sub>ISS</sub>                     | V <sub>GS</sub> = 0 V, f = 1 MHz, V <sub>DS</sub> = 800 V                         |                          | -    | 1230 | -   | pF   |
| Output Capacitance   | C <sub>OSS</sub>                     |   |                          | -    | 57   | -   | 1    |
| Reverse Transfer Capacitance                                 | C <sub>RSS</sub>                     |   |                          | -    | 5    | -   | 1    |
| Total Gate Charge  | Q <sub>G(TOT)</sub>                  | $V_{GS} = -3/18 \text{ V}, V_{DS} = 800 \text{ V},$                               |                          | -    | 57   | -   | nC   |
| Threshold Gate Charge  | Q <sub>G(TH)</sub>                   | I <sub>D</sub> = 15 A   | 4                        | -    | 3.2  | -   | 1    |
| Gate-to-Source Charge  | Q <sub>GS</sub>                      |   |                          | -    | 9.6  | -   | 1    |
| Gate-to-Drain Charge   | $Q_{GD}$                             |   |                          | -    | 17   | -   | 1    |
| Gate-Resistance  | R <sub>G</sub>                       | f = 1 MH  | Z                        | -    | 4.3  | -   | Ω    |
| SWITCHING CHARACTERISTICS                                    |                                      |   |                          |      |      |     |      |
| Turn-On Delay Time   | t <sub>d(ON)</sub>                   | $V_{GS} = -3/18 \text{ V}, V_{D}$   | os = 800 V,              | -    | 9.2  | =   | ns   |
| Rise Time  | t <sub>r</sub>                       | $I_D = 15 \text{ A, R}_G = 4.7 \Omega$<br>Inductive load (Notes 5, 6)             |                          | -    | 11   | -   |      |
| Turn-Off Delay Time  | t <sub>d(OFF)</sub>                  |   |                          | -    | 29   | -   |      |
| Fall Time  | t <sub>f</sub>                       |   |                          | -    | 8.8  | -   |      |
| Turn-On Switching Loss                                       | E <sub>ON</sub>                      |   |                          | -    | 124  | -   | μJ   |
| Turn-Off Switching Loss                                      | E <sub>OFF</sub>                     |   |                          | -    | 36   | -   |      |
| Total Switching Loss   | E <sub>tot</sub>                     | 1   |                          | _    | 160  | -   |      |

Table 3. ELECTRICAL CHARACTERISTICS ( $T_J = 25^{\circ}C$  unless otherwise specified) (continued)

| Parameter   | Symbol           | Test Condition   | Min | Тур  | Max | Unit |
|---|------------------|--|-----|------|-----|------|
| SOURCE-DRAIN DIODE CHARACTERIST                       | ics              |  | •   |      |     |      |
| Continuous Source-Drain Diode Forward Current         | I <sub>SD</sub>  | $V_{GS} = -3 \text{ V}, T_{C} = 25^{\circ}\text{C (Note 6)}$             | -   | -    | 31  | А    |
| Pulsed Source-Drain Diode Forward<br>Current (Note 2) | I <sub>SDM</sub> | -  | -   | -    | 98  |      |
| Forward Diode Voltage                                 | $V_{SD}$         | $V_{GS} = -3 \text{ V}, I_{SD} = 15 \text{ A}, T_J = 25^{\circ}\text{C}$ | -   | 4.7  | -   | V    |
| Reverse Recovery Time                                 | t <sub>RR</sub>  | $V_{GS} = -3/18 \text{ V}, I_{SD} = 15 \text{ A},$                       | -   | 14.4 | -   | ns   |
| Reverse Recovery Charge                               | Q <sub>RR</sub>  | dl <sub>S</sub> /dt = 1000 A/μs, V <sub>DS</sub> = 800 V<br>(Note 6)     | _   | 60   | -   | nC   |
| Reverse Recovery Energy                               | E <sub>REC</sub> | 1  | _   | 4.8  | -   | μJ   |
| Peak Reverse Recovery Current                         | I <sub>RRM</sub> | 1  | _   | 8.4  | -   | Α    |
| Charge Time   | T <sub>A</sub>   | 1  | _   | 7.9  | _   | ns   |
| Discharge Time  | T <sub>B</sub>   | 1  | _   | 6.5  | -   | ns   |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

5. E<sub>ON</sub>/E<sub>OFF</sub> result is with body diode.

6. Defined by design, not subject to production test.

#### **TYPICAL CHARACTERISTICS**

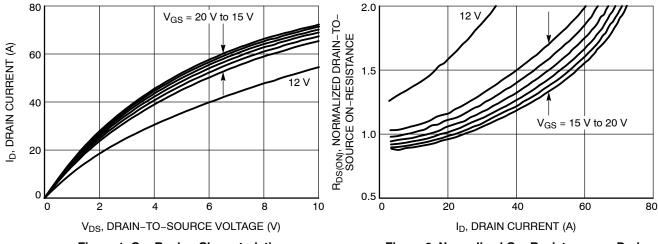


Figure 1. On-Region Characteristics

Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

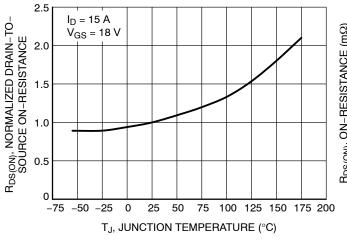


Figure 3. On–Resistance Variation with Temperature

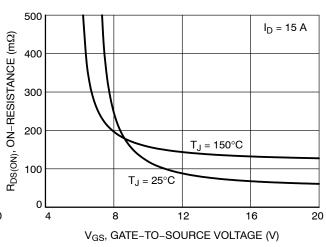


Figure 4. On-Resistance vs. Gate-to-Source Voltage

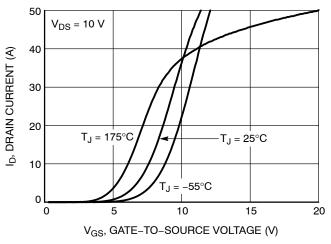


Figure 5. Transfer Characteristics

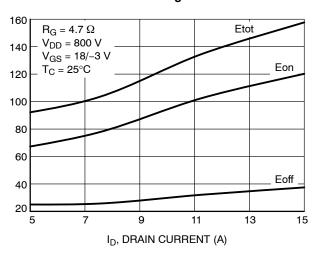


Figure 6. Switching Loss vs. Drain Current

SWITCHING LOSS (µJ)

#### TYPICAL CHARACTERISTICS

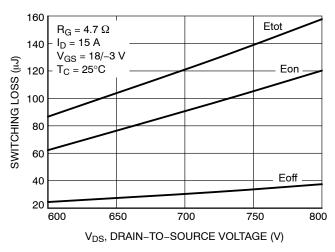


Figure 7. Switching Loss vs. Drain-to-Source Voltage

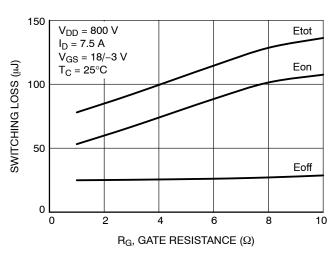


Figure 8. Switching Loss vs. Gate Resistance

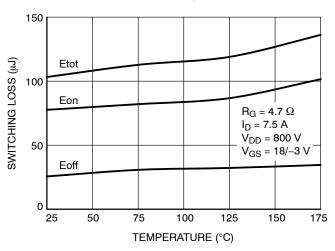


Figure 9. Switching Loss vs. Temperature

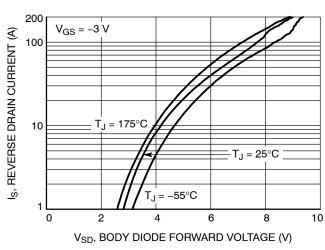


Figure 10. Reverse Drain Current vs. Body Diode Forward Voltage

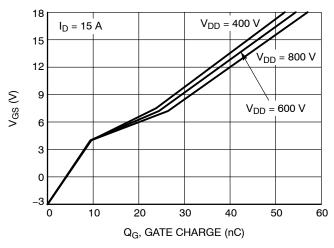


Figure 11. Gate-to-Source Voltage vs. Total Charge

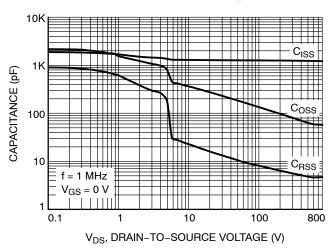


Figure 12. Capacitance vs. Drain-to-Source Voltage

## **TYPICAL CHARACTERISTICS**

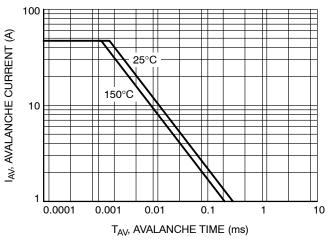


Figure 13. Unclamped Inductive Switching Capability

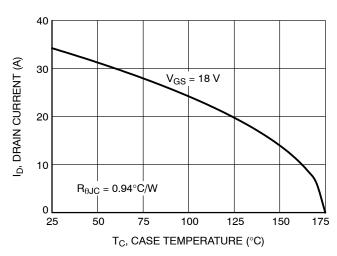


Figure 14. Maximum Continuous Drain Current vs. Case Temperature

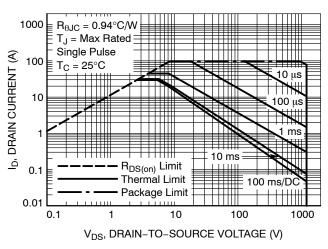


Figure 15. Safe Operating Area

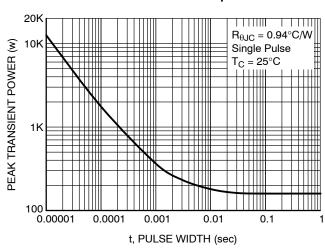


Figure 16. Single Pulse Maximum Power Dissipation

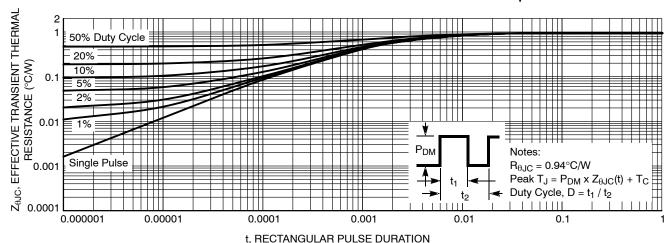


Figure 17. Junction-to-Case Transient Thermal Response

 $\emptyset$ p1

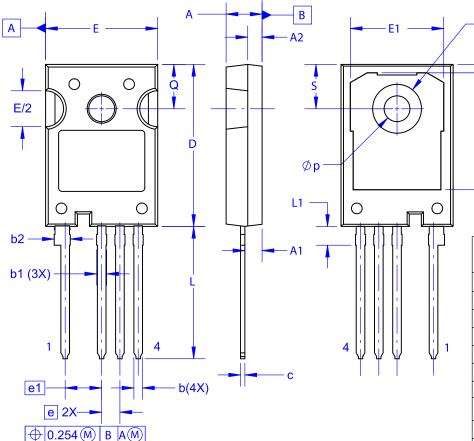
D1

D2



## TO-247-4LD CASE 340CJ **ISSUE A**

**DATE 16 SEP 2019** 



# NOTES:

- A. NO INDUSTRY STANDARD APPLIES TO THIS PACKAGE.
  B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD
  FLASH, AND TIE BAR EXTRUSIONS.
  C. ALL DIMENSIONS ARE IN MILLIMETERS.
  D. DRAWING CONFORMS TO ASME Y14.5-2009.

| DIM | MILLIMETERS |          |       |  |  |
|-----|-------------|----------|-------|--|--|
| DIM | MIN         | NOM      | MAX   |  |  |
| Α   | 4.80        | 5.00     | 5.20  |  |  |
| A1  | 2.10        | 2.40     | 2.70  |  |  |
| A2  | 1.80        | 2.00     | 2.20  |  |  |
| b   | 1.07        | 1.20     | 1.33  |  |  |
| b1  | 1.20        | 1.40     | 1.60  |  |  |
| b2  | 2.02        | 2.22     | 2.42  |  |  |
| С   | 0.50        | 0.60     | 0.70  |  |  |
| D   | 22.34       | 22.54    | 22.74 |  |  |
| D1  | 16.00       | 16.25    | 16.50 |  |  |
| D2  | 0.97        | 1.17     | 1.37  |  |  |
| е   | 2           | 2.54 BSC |       |  |  |
| e1  | 5           | 5.08 BSC |       |  |  |
| E   | 15.40       | 15.60    | 15.80 |  |  |
| E1  | 12.80       | 13.00    | 13.20 |  |  |
| E/2 | 4.80        | 5.00     | 5.20  |  |  |
| L   | 18.22       | 18.42    | 18.62 |  |  |
| L1  | 2.42        | 2.62     | 2.82  |  |  |
| р   | 3.40        | 3.60     | 3.80  |  |  |
| p1  | 6.60        | 6.80     | 7.00  |  |  |
| Q   | 5.97        | 6.17     | 6.37  |  |  |
| S   | 5.97        | 6.17     | 6.37  |  |  |

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