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MOSFET - Power, Single N-Channel, SUPERFET[®] V, FAST, TO247-3L

600 V, 185 mΩ, 15 A

NTHL185N60S5H

Description

The SUPERFET V MOSFET FAST series helps maximize system efficiency by the extremely low switching losses in hard switching application. Features

• $650 \text{ V} @ \text{T}_{\text{J}} = 150^{\circ}\text{C}$

- Typ. $R_{DS(on)} = 148 \text{ m}\Omega$
- 100% Avalanche Tested
- Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- Telecom / Server Power Supplies
- EV Charger / UPS / Solar / Industrial Power Supplies

ABSOLUTE MAXIMUM RATINGS (T_J = 25°C, Unless otherwise noted)

Parameter	Symbol	Value	Unit	
Drain-to-Source Voltage	V _{DSS}	600	V	
Gate-to-Source Voltage	DC	V _{GSS}	±30	V
	AC (f > 1 Hz)		±30	
Continuous Drain Current	$T_{C} = 25^{\circ}C$	Ι _D	15	А
	$T_{C} = 100^{\circ}C$		9	
Power Dissipation	$T_{C} = 25^{\circ}C$	PD	116	W
Pulsed Drain Current (Note 1)	Pulsed Drain Current (Note 1) $T_{C} = 25^{\circ}C$			
Pulsed Source Current (Body Diode) (Note 1)	I _{SM}	53	A	
Operating Junction and Storage Range	T _J , T _{STG}	–55 to +150	°C	
Source Current (Body Diode)	I _S	15	А	
Single Pulse Avalanche Energy	E _{AS}	124	mJ	
Avalanche Current	I _{AS}	3.6	А	
Repetitive Avalanche Energy (N	E _{AR}	1.16	mJ	
MOSFET dv/dt	dv/dt	120	V/ns	
Peak Diode Recovery dv/dt (No		20		
Lead Temperature for Soldering (1/8" from case for 10 seconds)	ΤL	260	°C	

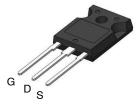
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive rating: pulse-width limited by maximum junction temperature.

2. $I_{SD} \le 7.5$ A, di/dt ≤ 200 A/µs, $V_{DD} \le 400$ V, starting $T_J = 25^{\circ}C$.

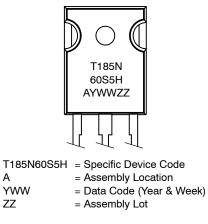
V _{DSS}	R _{DS(ON)} MAX	I _D MAX	
600 V	185 m Ω @ 10 V	15 A	

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TO-247 Long Leads CASE 340CX

MARKING DIAGRAM



ORDERING INFORMATION

Device	Package	Shipping
NTHL185N60S5H	TO-247	30 Units / Tube

THERMAL CHARACTERISTICS

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case, Max.	$R_{ ext{ heta}JC}$	1.08	°C/W
Thermal Resistance, Junction-to-Ambient, Max.	$R_{ hetaJA}$	40	

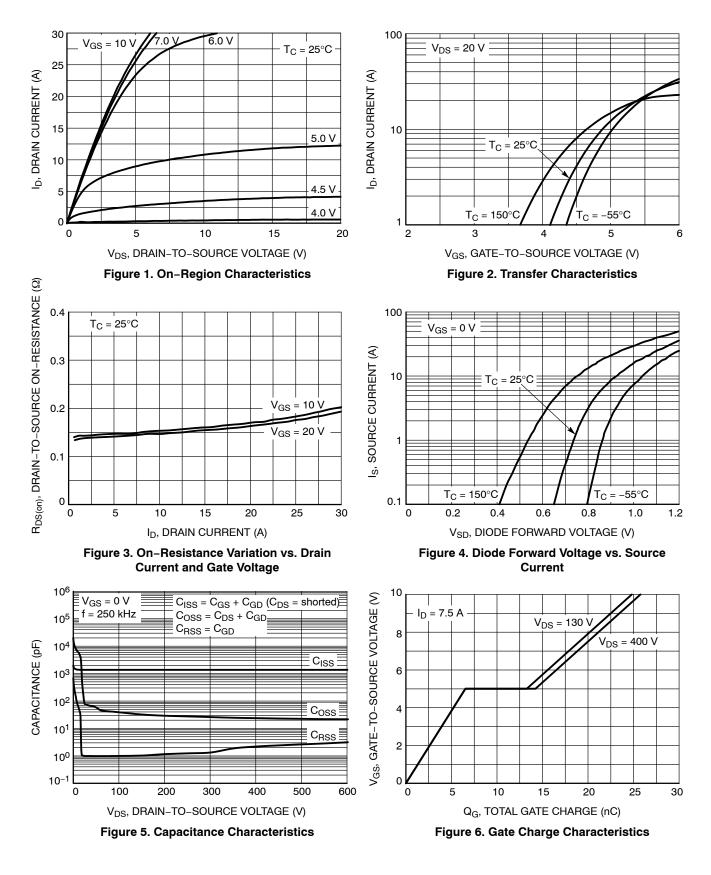
ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
OFF CHARACTERISTICS		•				
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I _D = 1 mA, T _J = 25°C	600	_	-	V
Drain-to-Source Breakdown Voltage Temperature Coefficient	$\Delta V_{(BR)DSS}/ \Delta T_{J}$	I_D = 10 mA, Referenced to 25°C	-	630	-	mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V_{GS} = 0 V, V_{DS} = 600 V, T_{J} = 25°C	-	-	1	μA
Gate-to-Source Leakage Current	I _{GSS}	V_{GS} = ±30 V, V_{DS} = 0 V	-	-	±100	nA
ON CHARACTERISTICS			-			
Drain-to-Source On Resistance	R _{DS(on)}	V_{GS} = 10 V, I_{D} = 7.5 A, T_{J} = 25 $^{\circ}C$	-	148	185	mΩ
Gate Threshold Voltage	V _{GS(th)}	V_{GS} = V_{DS} , I_D = 1.4 mA, T_J = 25°C	2.7	-	4.3	V
Forward Trans-conductance	9fs	$V_{DS} = 20 \text{ V}, \text{ I}_{D} = 7.5 \text{ A}$	_	18	-	S
CHARGES, CAPACITANCES & GATE	RESISTANCE		-			
Input Capacitance	C _{ISS}	V_{DS} = 400 V, V_{GS} = 0 V, f = 250 kHz	-	1350	-	pF
Output Capacitance	C _{OSS}		_	25	-	
Time Related Output Capacitance	C _{OSS(tr.)}	$I_{D} = Constant, V_{DS} = 0 V to 400 V, \\ V_{GS} = 0 V$	-	372	-	
Energy Related Output Capacitance	C _{OSS(er.)}	V_{DS} = 0 V to 400 V, V_{GS} = 0 V	-	42	-	
Total Gate Charge	Q _{G(tot)}	V_{DD} = 400 V, I_{D} = 7.5 A, V_{GS} = 10 V	-	25	-	nC
Gate-to-Source Charge	Q _{GS}		_	7	-	1
Gate-to-Drain Charge	Q _{GD}		_	8	-	
Gate Resistance	R _G	f = 1 MHz	-	0.9	-	Ω
SWITCHING CHARACTERISTICS		•				
Turn-On Delay Time	t _{d(on)}	$V_{GS} = 0/10 \text{ V}, V_{DD} = 400 \text{ V},$	-	18	-	ns
Rise Time	t _r	l _D = 7.5 A, R _G = 10 Ω	_	9	-	
Turn-Off Delay Time	t _{d(off)}	1	_	53	-	1
Fall Time	t _f	1	-	4	-	1
SOURCE-TO-DRAIN DIODE CHARAC	TERISTICS	•	-	-	-	-
Forward Diode Voltage	V _{SD}	V _{GS} = 0 V, I _{SD} = 7.5 A, T _J = 25°C	_	-	1.2	V

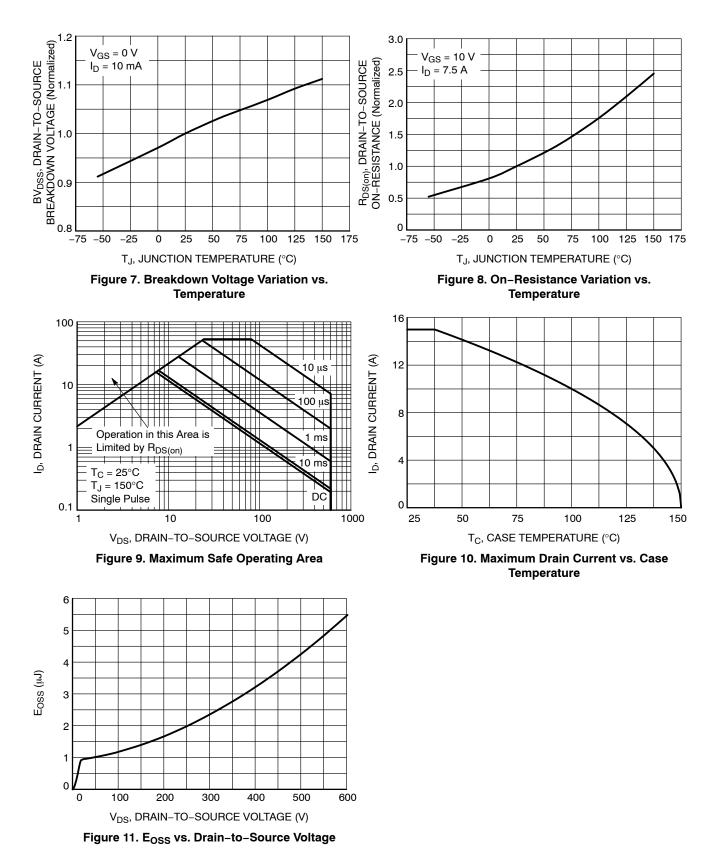
Forward Diode Voltage	V _{SD}	V_{GS} = 0 V, I_{SD} = 7.5 A, T_{J} = 25°C	-	-	1.2	V
Reverse Recovery Time	t _{RR}	$V_{GS} = 0 V, I_{SD} = 7.5 A,$	-	251	-	ns
Reverse Recovery Charge	Q _{RR}	dl/dt = 100 A/μs, V _{DD} = 400 V	-	3028	-	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS



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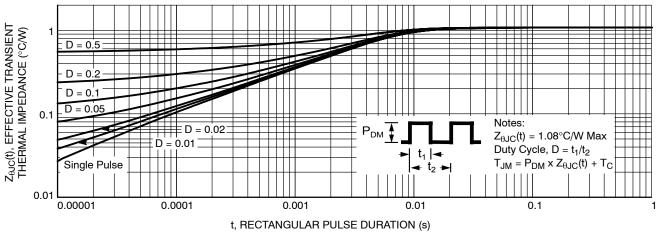
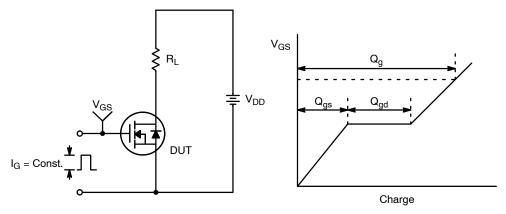


Figure 12. Transient Thermal Impedance





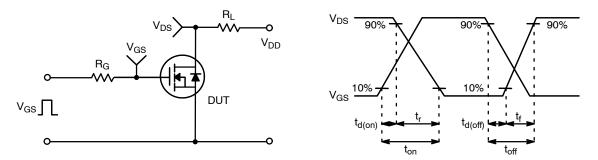


Figure 14. Resistive Switching Test Circuit & Waveforms

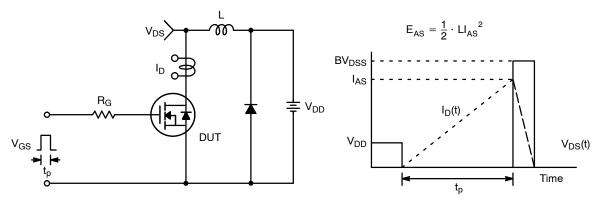


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

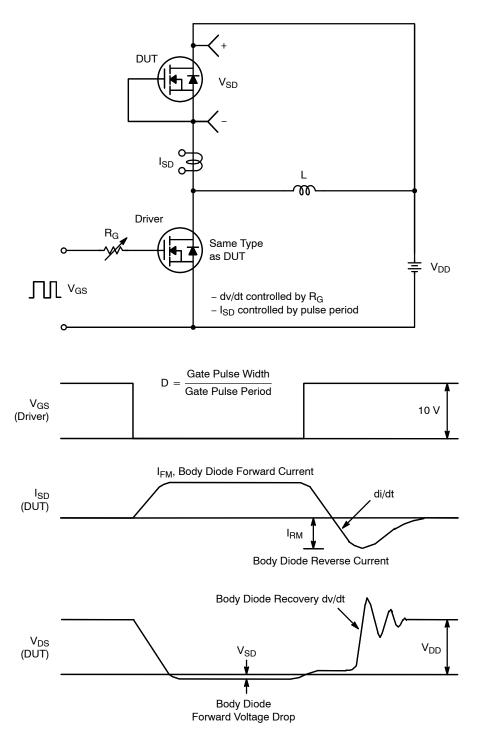
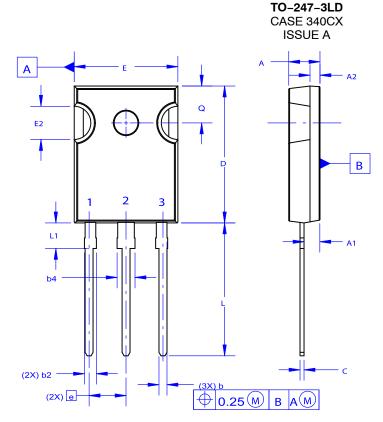


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

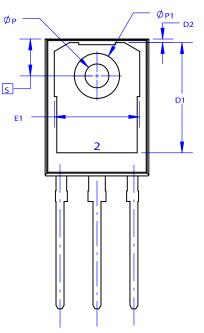
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PACKAGE DIMENSIONS



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
 D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.



	MILLIMETERS					
DIM	MIN	NOM	MAX			
Α	4.58	4.70	4.82			
A1	2.20	2.40	2.60			
A2	1.40	1.50	1.60			
D	20.32	20.57	20.82			
Е	15.37	15.62	15.87			
E2	4.96	5.08	5.20			
е	~	5.56	~			
L	19.75	20.00	20.25			
L1	3.69	3.81	3.93			
ØР	3.51	3.58	3.65			
Q	5.34	5.46	5.58			
S	5.34	5.46	5.58			
b	1.17	1.26	1.35			
b2	1.53	1.65	1.77			
b4	2.42	2.54	2.66			
С	0.51	0.61	0.71			
D1	13.08	~	~			
D2	0.51	0.93	1.35			
E1	12.81	~	~			
Ø P 1	6.60	6.80	7.00			

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