## **ON Semiconductor**

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## Power MOSFET -30 V, 13 mΩ, -60 A, P-Channel

The NVATS4A103PZ is a power MOSFET designed for compact size and high efficiency which can achieve high thermal performance. AEC-Q101 qualified MOSFET and PPAP capable suitable for automotive



- Low On-Resistance
- High Current Capability
- 100% Avalanche Tested
- AEC-Q101 qualified and PPAP capable
- ATPAK package is pin-compatible with DPAK (TO-252)
- Pb-Free, Halogen Free and RoHS compliance

#### **Typical Applications**

- Reverse Battery Protection
- Load Switch
- Automotive Front Lighting
- Automotive Body Controllers

#### **SPECIFICATIONS**

#### ABSOLUTE MAXIMUM RATING at Ta = 25°C (Note 1)

Parameter	Symbol	Value	Unit
Drain to Source Voltage	VDSS	-30	V
Gate to Source Voltage	VGSS	±20	V
Drain Current (DC)	ID	-60	Α
Drain Current (Pulse) PW ≤ 10 μs, duty cycle ≤ 1%	IDP	-180	Α
Power Dissipation Tc = 25°C	PD	60	W
Operating Junction and Storage Temperature	Tj, Tstg	–55 to +175	°C
Avalanche Energy (Single Pulse) (Note 2)	EAS	57	mJ
Avalanche Current (Note 3)	lav	-28	Α

Note 1: Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 2 :  $V_{DD}$  = -10 V, L = 100  $\mu H$ ,  $I_{AV}$  = -28 A
- 3 : L ≤ 100 μH, Single pulse

#### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Value	Unit
Junction to Case Steady State (Tc = 25°C)	$R_{\theta}$ JC	2.5	°C/W
Junction to Ambient (Note 4)	$R_{\theta}$ JA	80.1	°C/W

Note 4: Surface mounted on FR4 board using a 130 mm<sup>2</sup>, 1 oz. Cu pad.

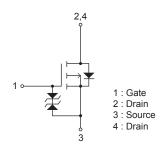


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VDSS	R <sub>DS</sub> (on) Max	ID Max
-30 V	13 mΩ @ –10 V	004
	20.5 mΩ @ -4.5 V	–60A

## ELECTRICAL CONNECTION P-Channel





#### **MARKING**



#### **ORDERING INFORMATION**

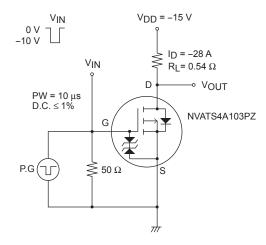
See detailed ordering and shipping information on page 6 of this data sheet.

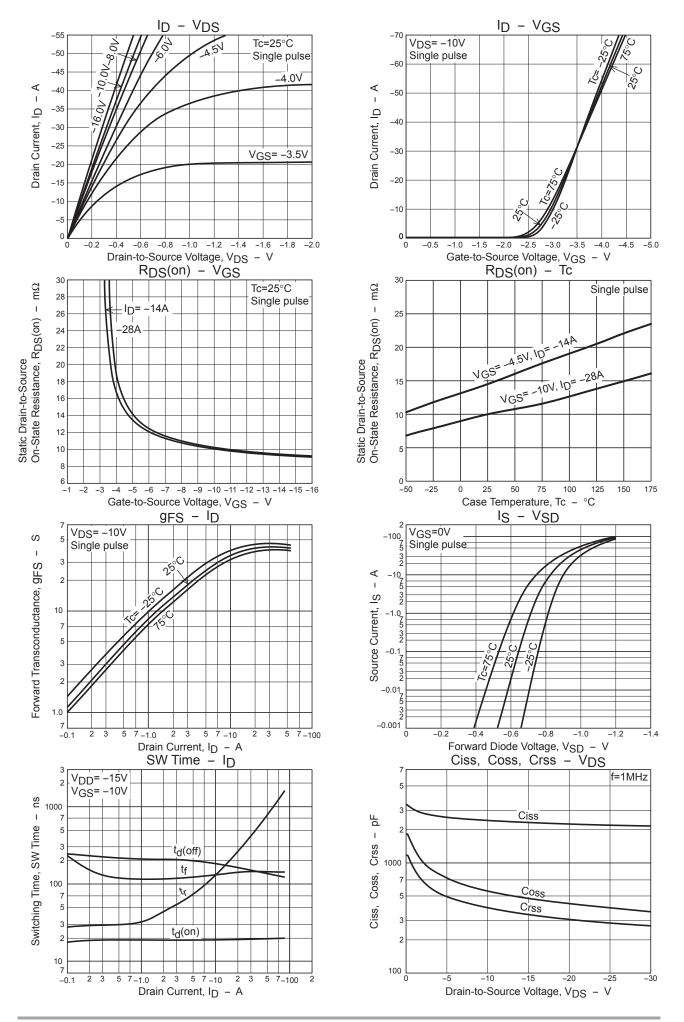
**ELECTRICAL CHARACTERISTICS** at Ta = 25°C (Note 5)

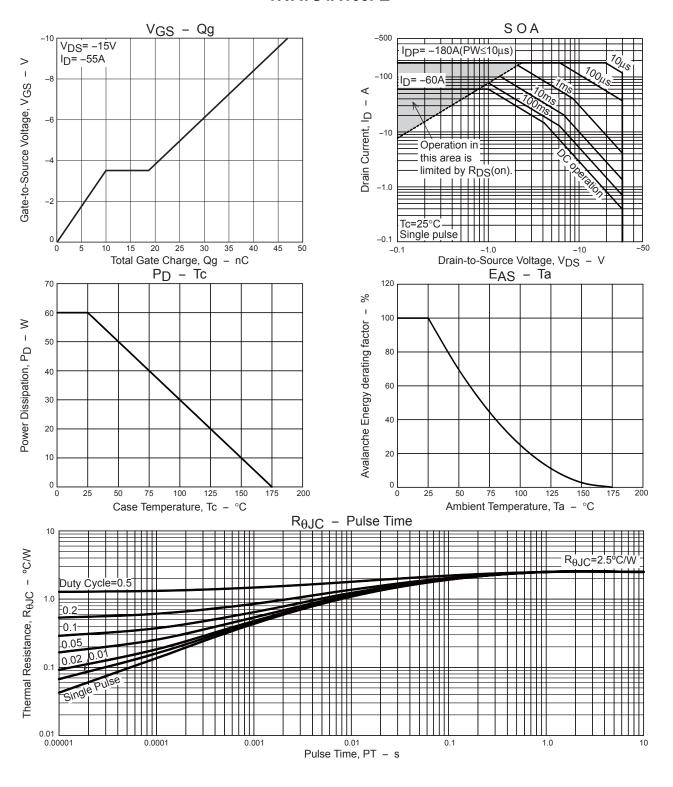
Daramatas	O wash at	O and the ma	Value			1.124
Parameter	Symbol	Conditions	min	typ	max	Unit
Drain to Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> = -1 mA, V <sub>GS</sub> = 0 V	-30			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0 V			-1	μΑ
Gate to Source Leakage Current	IGSS	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$			±10	μΑ
Gate Threshold Voltage	VGS(th)	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$	-1.2		-2.6	V
Forward Transconductance	gFS .	$V_{DS} = -10 \text{ V}, I_{D} = -28 \text{ A}$		45		S
Static Drain to Source On-State	R <sub>DS</sub> (on)1	I <sub>D</sub> = -28 A, V <sub>GS</sub> = -10 V		10	13	mΩ
Resistance	R <sub>DS</sub> (on)2	I <sub>D</sub> = -14 A, V <sub>GS</sub> = -4.5 V		14.5	20.5	mΩ
Input Capacitance	Ciss			2,430		pF
Output Capacitance	Coss	$V_{DS} = -10 \text{ V, f} = 1 \text{ MHz}$		555		pF
Reverse Transfer Capacitance	Crss			395		pF
Turn-ON Delay Time	t <sub>d</sub> (on)			19		ns
Rise Time	tr	See Fig. 1		400		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	See Fig.1		150		ns
Fall Time	tf			145		ns
Total Gate Charge	Qg			47		nC
Gate to Source Charge	Qgs	$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -55 \text{ A}$		10		nC
Gate to Drain "Miller" Charge	Qgd			8.7		nC
Forward Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = -55 A, V <sub>GS</sub> = 0 V		-1.03	-1.5	V

Note 5 : Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

#### Fig.1 Switching Time Test Circuit





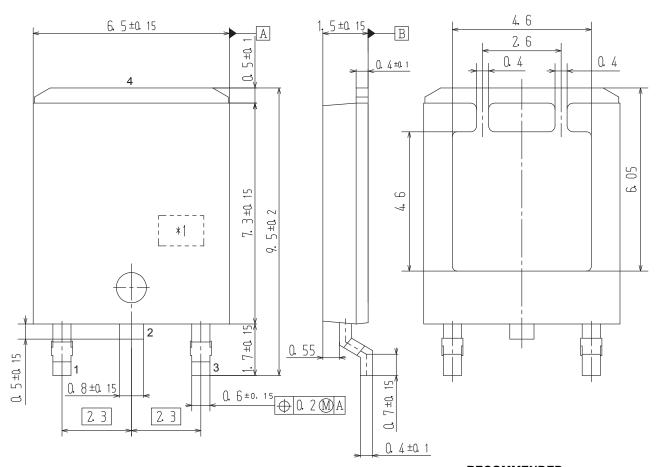


#### **PACKAGE DIMENSIONS**

unit: mm

### **DPAK (Single Gauge) / ATPAK**

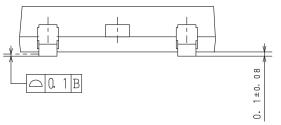
CASE 369AM ISSUE O



1 : Gate 2 : Drain

3 : Source

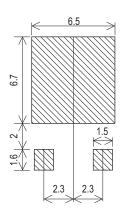
4 : Drain



Pin2 is idle pin with electrical designation only carried

\*1:Lot indication

# RECOMMENDED SOLDERING FOOTPRINT



#### ORDERING INFORMATION

Device	Marking	Package	Shipping (Qty / Packing)
NVATS4A103PZT4G	ATP103	DPAK(Single Gauge) / ATPAK (Pb-Free / Halogen Free)	3,000 / Tape & Reel

<sup>†</sup> For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub\_link/Collateral/BRD8011-D.PDF

Note on usage: Since the NVATS4A103PZ is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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