

Silicon Carbide (SiC) **MOSFET** - 31 mohm, 650 V, M2, D2PAK-71 **NVBG045N065SC1**

Features

- Typ. $R_{DS(on)} = 31 \text{ m}\Omega$ @ $V_{GS} = 18 \text{ V}$ Typ. $R_{DS(on)} = 45 \text{ m}\Omega$ @ $V_{GS} = 15 \text{ V}$
- Ultra Low Gate Charge (Q_{G(tot)} = 105 nC)
- Low Effective Output Capacitance (Coss = 168 pF)
- 100% Avalanche Tested
- AEC-Q101 Qualified and PPAP Capable
- This Device is Halide Free and RoHS Compliant with exemption 7a, Pb-Free 2LI (on second level interconnection)

Typical Applications

- Automotive On Board Charger
- Automotive DC-DC Converter for EV/HEV

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

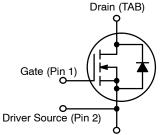
Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V_{DSS}	650	V
Gate-to-Source Voltage	Gate-to-Source Voltage			-8/+22	V
	Recommended Operation Values of Gate – Source Voltage		V_{GSop}	-5/+18	V
Continuous Drain Current (Note 2)	Steady State	T _C = 25°C	I _D	62	Α
Power Dissipation (Note 2)			P _D	242	W
Continuous Drain Current (Notes 1, 2)	Steady State	T _C = 100°C	I _D	44	Α
Power Dissipation (Notes 1, 2)			P _D	121	W
Pulsed Drain Current (Note 3) T _C = 25°C			I _{DM}	184	Α
Single Pulse Surge Drain Current Capability	Drain Current $R_G = 4.7 \Omega$		I _{DSC}	315	Α
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55 to +175	°C
Source Current (Body Diode)			I _S	56	Α
Single Pulse Drain-to-Source Avalanche Energy (I _L = 12 A _{pk} , L = 1 mH) (Note 4)			E _{AS}	72	mJ
Maximum Lead Temperature for Soldering, 1/8" from Case for 10 Seconds			TL	245	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Surface mounted on a FR-4 board using1 in² pad of 2 oz copper.

- 2. The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 3. Repetitive rating, limited by max junction temperature.

V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
650 V	50 mΩ @ 18 V	62 A



Power Source (Pins 3, 4, 5, 6, 7)

N-CHANNEL MOSFET



D2PAK-7L CASE 418BJ

MARKING DIAGRAM

BG045N 065SC1 **AYWWZZ**

BG045N065SC1 = Specific Device Code

= Assembly Location Υ = Year

WW = Work Week = Lot Traceability

ORDERING INFORMATION

Device	Package	Shipping [†]
NVBG045N065SC1	D2PAK-7L	800 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

4. E_{AS} of 72 mJ is based on starting T_J = 25°C; L = 1 mH, I_{AS} = 12 A, V_{DD} = 50 V, V_{GS} = 18 V.

THERMAL CHARACTERISTICS

Parameter	Symbol	Max	Unit
Thermal Resistance Junction-to-Case (Note 2)	$R_{ heta JC}$	0.62	°C/W
Thermal Resistance Junction-to-Ambient (Notes 1, 2)	$R_{ heta JA}$	40	°C/W

ELECTRICAL CHARACTERISTICS (T₁ = 25°C unless otherwise stated)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS	•				•	1	
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 1 mA		650			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	I _D = 20 mA, refer to 25°C			0.13		V/°C
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V, T _J = 25°C				10	μΑ
		$V_{DS} = 650 \text{ V}$ T_J	= 175°C			1	mA
Gate-to-Source Leakage Current	I _{GSS}	$V_{GS} = +18/-5 \text{ V}, V_{DS}$	₃ = 0 V			250	nA
ON CHARACTERISTICS	•						
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}$, $I_D = 8$ m	ıA	1.8	2.8	4.3	V
Recommended Gate Voltage	V _{GOP}			-5		+18	V
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 15 V, I _D = 25 A	A, T _J = 25°C		45		mΩ
		V _{GS} = 18 V, I _D = 25 A	A, T _J = 25°C		31	50	
		V _{GS} = 18 V, I _D = 25 A	A, T _J = 175°C		40		
Forward Transconductance	9FS	V _{DS} = 10 V, I _D = 25 A			16		S
CHARGES, CAPACITANCES & GATE RE	SISTANCE				1	I	
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1 MHz, V _{DS} = 325 V			1890		pF
Output Capacitance	C _{OSS}				168		
Reverse Transfer Capacitance	C _{RSS}				15		
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = -5/18 \text{ V}, V_{DS} = 520 \text{ V},$ $I_{D} = 25 \text{ A}$			105		nC
Gate-to-Source Charge	Q _{GS}				27		
Gate-to-Drain Charge	Q_{GD}				30		
Gate-Resistance	R_{G}	f = 1 MHz			3.1		Ω
SWITCHING CHARACTERISTICS							
Turn–On Delay Time	t _{d(ON)}	$V_{GS} = -5/18 \text{ V, } V_{DS} = 400 \text{ V,}$ $I_D = 25 \text{ A, } R_G = 2.2 \Omega,$ Inductive Load			13		ns
Rise Time	t _r				14		
Turn-Off Delay Time	t _{d(OFF)}				26		1
Fall Time	t _f				7		
Turn-On Switching Loss	E _{ON}				47		μJ
Turn-Off Switching Loss	E _{OFF}				33		1
Total Switching Loss	E _{TOT}				80		
DRAIN-SOURCE DIODE CHARACTERIS		L			1	1	
Continuous Drain-Source Diode Forward Current	I _{SD}	$V_{GS} = -5 \text{ V}, T_{J} = 25^{\circ}\text{C}$				56	Α
Pulsed Drain-Source Diode Forward Current (Note 3)	I _{SDM}	$V_{GS} = -5 \text{ V}, T_{J} = 25^{\circ}\text{C}$				184	Α
Forward Diode Voltage	V _{SD}	V _{GS} = -5 V, I _{SD} = 25 A, T _J = 25°C			4.4		V

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise stated)(continued)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit		
DRAIN-SOURCE DIODE CHARACTERISTICS								
Reverse Recovery Time	t _{RR}	$V_{GS} = -5/18 \text{ V, } I_{SD} = 25 \text{ A,}$ $dI_{S}/dt = 1000 \text{ A/}\mu\text{s}$		20		ns		
Reverse Recovery Charge	Q _{RR}			100		nC		
Reverse Recovery Energy	E _{REC}			3.8		μJ		
Peak Reverse Recovery Current	I _{RRM}			10		Α		
Charge Time	Ta]		11		ns		
Discharge Time	Tb]		8.7		ns		

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

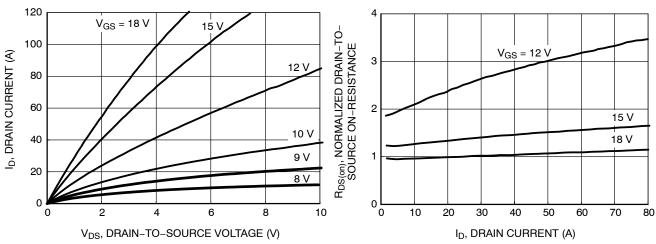


Figure 1. On-Region Characteristics

Figure 2. Normalized On-Resistance vs. Drain **Current and Gate Voltage**

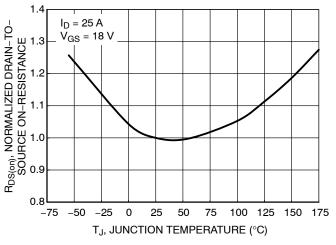


Figure 3. On-Resistance Variation with **Temperature**

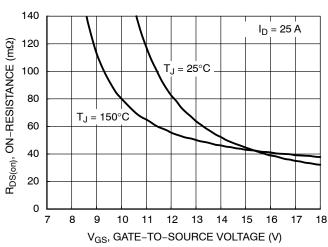


Figure 4. On-Resistance vs. Gate-to-Source Voltage

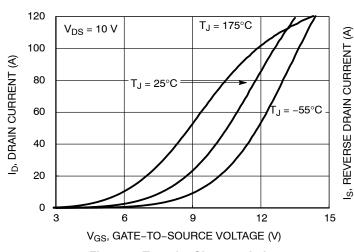


Figure 5. Transfer Characteristics

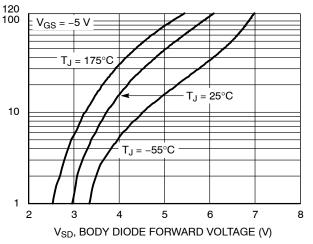


Figure 6. Diode Forward Voltage vs. Current

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TYPICAL CHARACTERISTICS (continued)

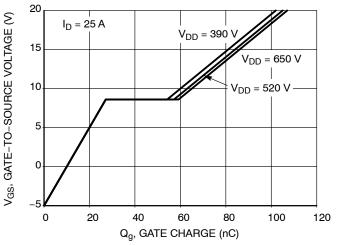


Figure 7. Gate-to-Source Voltage vs. Total Charge

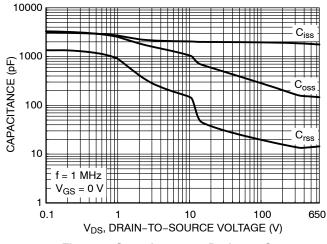


Figure 8. Capacitance vs. Drain-to-Source Voltage

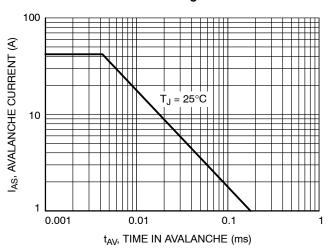


Figure 9. Unclamped Inductive Switching Capability

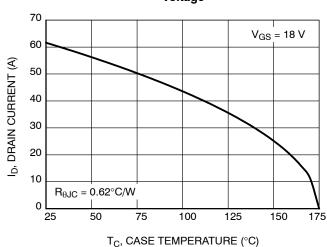


Figure 10. Maximum Continuous Drain Current vs. Case Temperature

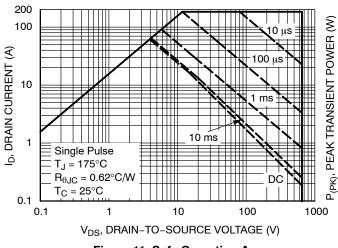


Figure 11. Safe Operating Area

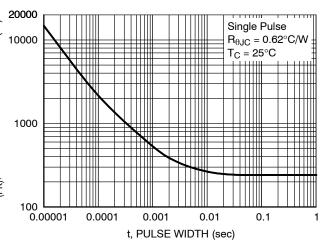


Figure 12. Single Pulse Maximum Power Dissipation

TYPICAL CHARACTERISTICS (continued)

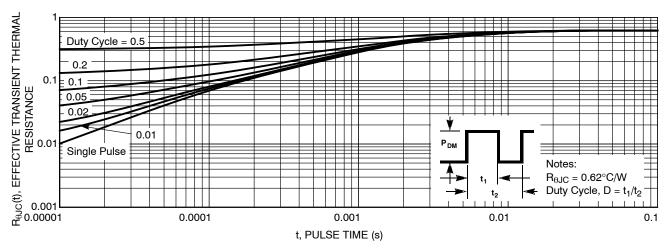
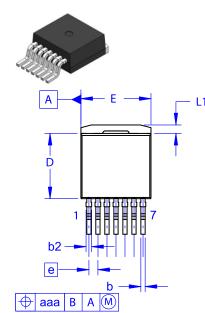


Figure 13. Junction-to-Case Transient Thermal Response



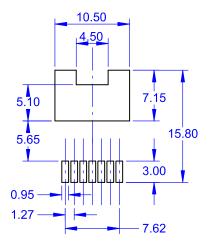


E1

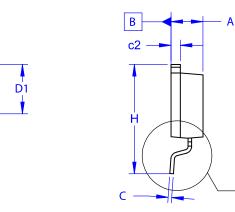
8

3.20 MIN

D²PAK7 (TO-263-7L HV) CASE 418BJ ISSUE B



LAND PATTERN RECOMMENDATION



DATE 16 AUG 2019

NOTES:

A. PACKAGE CONFORMS TO JEDEC TO-263 VARIATION CB EXCEPT WHERE NOTED. B. ALL DIMENSIONS ARE IN MILLIMETERS.

OUT OF JEDEC STANDARD VALUE.
D. DIMENSION AND TOLERANCE AS PER ASME
Y14.5-2009.

E. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.

54	MILLIMETERS				
DIM	MIN	NOM	MAX		
Α	4.30	4.50	4.70		
A1	0.00	0.10	0.20		
b2	0.60	0.70	0.80		
b	0.51	0.60	0.70		
С	0.40	0.50	0.60		
c2	1.20	1.30	1.40		
D	9.00	9.20	9.40		
D1	6.15	6.80	7.15		
Е	9.70	9.90	10.20		
E1	7.15	7.65	8.15		
е	~	1.27	~		
Н	15.10	15.40	15.70		
L	2.44	2.64	2.84		
L1	1.00	1.20	1.40		
L3	~	0.25	~		
aaa	~	~	0.25		

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code

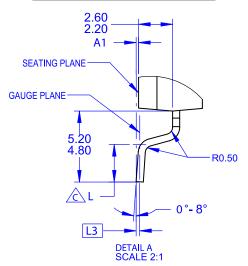
A = Assembly Location

Y = Year

WW = Work Week

G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



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