onsemi

IGBT Die PCFG75T65LQF

Using novel field stop IGBT technology, **onsemi**'s new series of field stop 4th generation IGBTs offer the optimum performance for solar inverter and UPS applications where low conduction and switching losses are essential.

Features

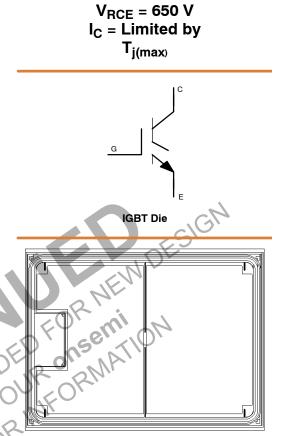
- Maximum Junction Temperature: $T_J = 175^{\circ}C$
- Positive Temperature Co-efficient for Easy Parallel Operating
- High Current Capability
- Low Saturation Voltage: $V_{CE(sat)} = 1.1 \text{ V} (Typ.) @ I_C = 75 \text{ A}$
- High Input Impedance
- Fast Switching
- Tighten Parameter Distribution

Typical Applications

- Solar Inverters
- UPS Systems

MECHANICAL DATA

Parameter	Mils	μm	
Die Size	251.97 × 185.04	6400 x 4700	
Gate Pad Size	112.36×157.37	2854 x 3997.1	
Emitter Pad Size	31.378 × 56.17	797 x 1426.8	
Die Thickness	2.48	63	
Scribe Width	80		
Top Metal	5 um/	AlSiGu	
Back Metal	1.05 µm Al/NiV/Ag		
Topside Passivation	Silicon Nitride		
Wafer Diameter	200 mm		
Max Possible Die Per Wafer	RV 80)7	
Recommended Storage Environment	In original container, in dry nitrogen, < 3 months at ambient temperature of 23°C		



DIE Outline

ORDERING INFORMATION

Device	Inking? Shipping Metho	
PCFG75T65LQF	No	Sawn Wafer on Tape

MAXIMUM RATINGS

Parameter	Symbol	Value	Unit	
Collector to Emitter Voltage, T _J = 25°C	V _{CES}	650	V	
Gate to Emitter Voltage	V _{GES}	±20	V	
Collector Current $@T_C = 25^{\circ}C$	۱ _C	(Note 1)	А	
Pulsed Collector Current	I _{CM}	300	А	
Operating Junction Temperature	TJ	-40 to +175	°C	
Storage Temperature Range	T _{STG}	-17 to +25	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Depending on the thermal properties of assembly.

2. Not subject to production test - verified by design/characterization.

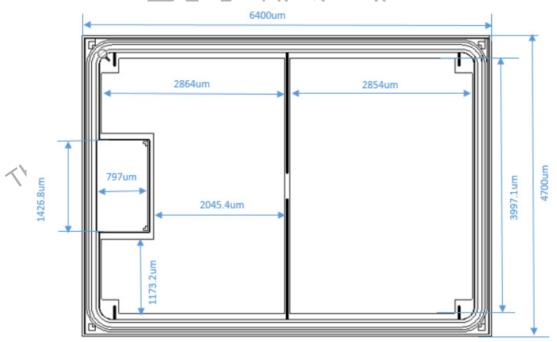
PCFG75T65LQF

ELECTRICAL CHARACTERISTICS (T_J = 25° C unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	-	-	-			
Collector-Emitter Breakdown Voltage	$V_{GE} = 0 V, I_{C} = 1 mA$	BV _{CES}	650			V
Temperature Coefficient of Breakdown Voltage	$I_{\rm C}$ = 1 mA, reference to 25°C	$\Delta BV_{CES}/\Delta T_{J}$		0.6		V/°C
Collector-Emitter Cutoff Current	V_{GE} = 0 V, V_{CE} = V_{CES}	I _{DSS}			250	μA
Gate Leakage Current	V_{CE} = 0 V, V_{GE} = V_{GES}	I _{GSS}			±400	nA
ON CHARACTERISTICS						
G-E Threshold Voltage	$V_{GE} = V_{CE}, I_C = 60 \text{ mA}$	V _{GE(th)}	2.6	4.4	6.4	V
Collector-Emitter Saturation Voltage	I _C = 60 A, V _{GE} = 15 V	V _{CE(sat)}		1.1	1.5	V
	I_{C} = 60 A, V_{GE} = 15 V, T_{C} = 175°C			1.13		V
DYNAMIC CHARACTERISTICS						
Input Capacitance	V_{GE} = 0 V, V_{CE} = 30 V, f = 1 MHz	C _{ies}		16400	~	pF
Output Capacitance]	Coes		85	~(G/	
Reverse Transfer Capacitance	1	C _{res}		74	<i>)</i>	
GATE CHARGE CHARACTERISTICS	-			100		
Total Gate Charge	V_{CE} = 400 V, I _C = 60 A, V _{GE} = 15 V	Qg	(E)	830		nC
Gate to Emitter Charge	1	Q _{ge}	44	80		
Gate to Collector Charge		Qgc		243		

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Switching characteristics and thermal properties depend strongly on module design and mounting technology. For ordering, technique and other information on **onsemi** automotive bare die products, please contact <u>automotivebaredie@onsemi.com</u>.



(all dimensions in μm) Figure 1. Die Layout

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