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Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild_questions@onsemi.com.

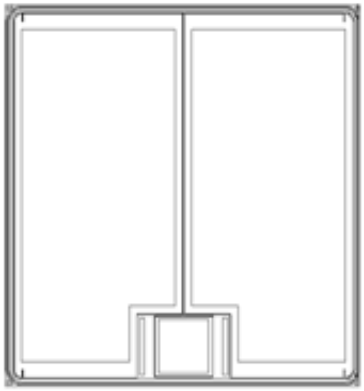
May 2016

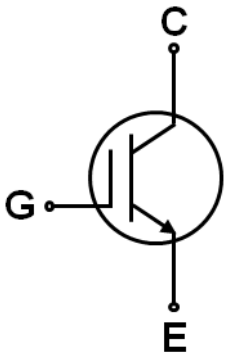
PCGA200T65NF8

650 V, 200 A Field Stop Trench IGBT

Features

- AEC-Q101 Qualified
- Max Junction Temperature 175°C
- Positive Temperature Co-efficient
- Ease of Paralleling
- Short Circuit Rated
- Very Low Saturation Voltage: $V_{CE(SAT)}$ = 1.53V (Typ.) @ I_C = 200A
- Optimized for Motor Control Applications





Applications

- Automotive Traction Modules
- General Power Modules

Ordering Information

P/N	PCGA200T65NF8	
Packing	Wafer (Sawn-On-Foil)	
	mils	μm
Die Size	394 X 394	10,000 X 10,000
Emitter Attach Area	2 x (169 x 340)	2 x (4,300 x 8,640)
Gate pad Attach Area	55 x 55	1,400 x 1,400
Die thickness	3	78
Top Metal	Al (0.5% Cu, 0.8% Si)	
Back Metal	Al/VNi/Ag	
Topside Passivation	Silicon Nitride Plus Polymide	
Wafer diameter	200mm	
Max Possible Die Per Wafer	234	

Absolute Maximum Ratings (T_{VJ}= 25°C unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{CES}	Collector to Emitter Voltage	650	V
V_{GES}	Gate-to-Emmiter Voltage	±20	V
I_C	Collector Current, limited by T_{VJ} max	(Note 1)	A
I_{CM}	Pulsed Collector Current, VGE=15V, limited by T_{VJ} max	600	A
S_{CWT}	Short Circuit Withstand Time, VGE= 15V, VCE≤400V, T_{VJ}≤150ºC	5	μs
T_{VJ}	Operating Junction Temperature	-40 to +175	°C
Tstg	Storage Temperature Range	+17 to +25	°C

Notes:
1: Depends on the thermal properties of assembly

Electrical Characteristics of the IGBT (T_{VJ} = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
Static Characteristics (Tested on wafers)						
B_{VCES}	Collector to Emitter Breakdown Voltage	V_{GE} = 0V, I_C = 1mA	650	-	-	V
$V_{CE(SAT)}$	Collector to Emitter Saturation Voltage	I_C = 100A, V_{GE} = 15V	-	1.25	1.75	V
$V_{GE(th)}$	G-E Threshold Voltage	V_{GE} = V_{CE}, I_C = 200mA	4.5	5.5	6.5	V
I_{CES}	Collector Cut-Off Current	V_{CE} = V_{CES}, V_{GE} = 0V	-	-	40	μA
I_{GES}	G-E Leakage Current	V_{GE} = V_{GES}, V_{CE} = 0V	-	-	±400	nA

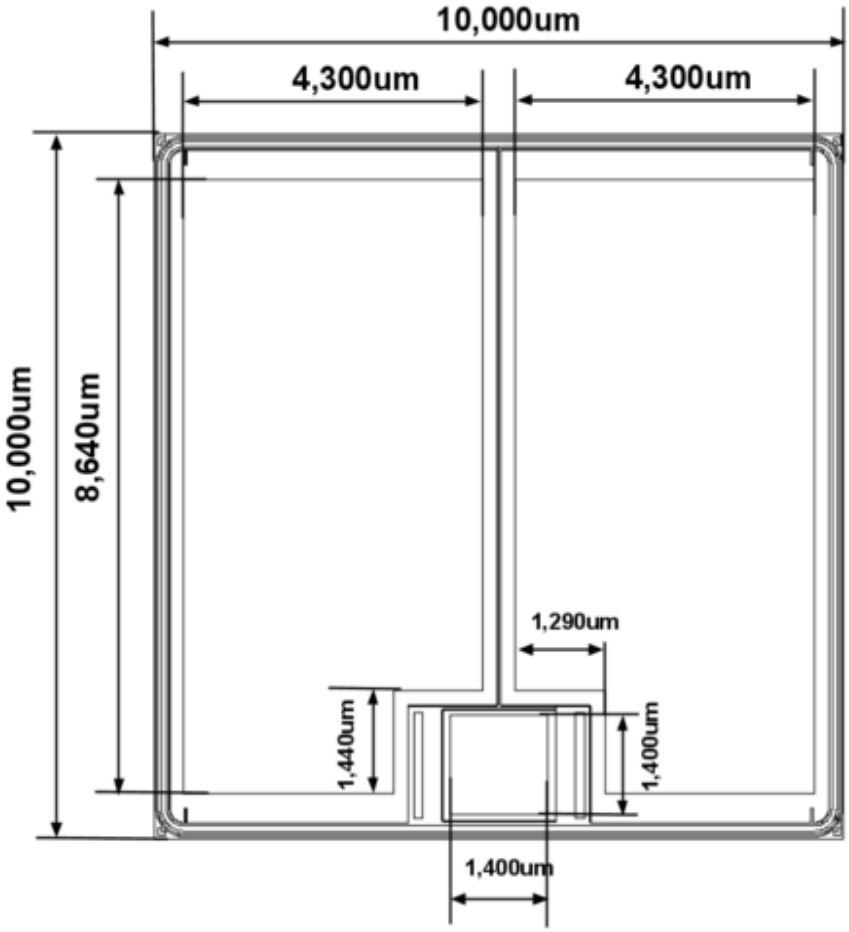
Electrical Characteristics (Not subject to production test, verified by design /characterization)

$V_{CE(SAT)}$	Collector to Emitter Saturation Voltage	I_C = 200A, V_{GE} = 15V, T_{VJ} = 25°C	-	1.53	1.9	V
		I_C = 200A, V_{GE} = 15V, T_{VJ} = 175°C	-	2.04	-	V
C_{IES}	Input Capacitance	V_{DS} = 30V, V_{GS} = 0V, f = 1MHz	-	9600	-	pF
C_{OES}	Output Capacitance		-	445	-	pF
C_{RES}	Reverse Transfer Capacitance		-	78	-	pF
R_G	Internal Gate Resistance	f = 1MHz	-	2.0	-	Ω
$Q_{G(ToT)}$	Total Gate Charge	V_{CE} = 400V, I_C = 200A, V_{GE} = 15V	-	229	-	nC
Q_{GE}	Gate-to-Emitter Charge		-	66	-	nC
Q_{GC}	Gate-to-Collector Charge		-	64	-	nC
$t_{d(on)}$	Turn-On Delay Time	V_{CE} = 400V, I_C = 200A, R_G = 5Ω, V_{GE} = +15V, Inductive Load T_{VJ} = 25°C	-	67	-	ns
t_r	Rise Time		-	233	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	118	-	ns
t_f	Fall Time		-	177	-	ns
$t_{d(on)}$	Turn-On Delay Time	V_{CE} = 400V, I_C = 200A, R_G = 5Ω, V_{GE} = +15V, Inductive Load T_{VJ} = 175°C	-	64	-	ns
t_r	Rise Time		-	236	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	124	-	ns
t_f	Fall Time		-	208	-	ns

For ordering, technique and other information on Fairchild automotive bare die products, please contact automotivedie@fairchildsemi.com

May 2016

Physical Dimensions Dimensionis in micrometer unless otherwise noted



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Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

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