RB520S30T1G, RB520S30T5G

Schottky Barrier Diode

These Schottky barrier diodes are designed for high-speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand-held and portable applications where space is limited.

Features

- Extremely Fast Switching Speed
- Extremely Low Forward Voltage 0.6 V (max) @ $I_F = 200 \text{ mA}$
- Low Reverse Current
- ESD Rating: Class 3B per Human Body Model Class C per Machine Model
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|--------------------|----------------|-------|------|
| Reverse Voltage | V _R | 30 | Vdc |
| Forward Current DC | ١ _F | 200 | mA |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Max | Unit |
|---|-----------------------------------|-------------|-------|
| Total Device Dissipation FR–5 Board, (Note 1) $T_A = 25^{\circ}C$ | PD | 200 | mW |
| Derate above 25°C | | 1.57 | mW/°C |
| Thermal Resistance, Junction-to-Ambient | R_{\thetaJA} | 635 | °C/W |
| Junction and Storage Temperature Range | T _J , T _{stg} | -55 to +150 | °C |
| Non–Repetitive Peak Forward Current, $t_p < 10$ msec | I _{FSM} | 600 | mA |
| Repetitive Peak Forward Current Pulse Wave = 1 sec, Duty Cycle = 66% | I _{FRM} | 300 | mA |
| Thermal Resistance, Junction-to-Ambient | $R_{\theta JA}$ | 635 | °C/W |

1. FR–5 Minimum Pad.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

| Characteristic | Symbol | Min | Тур | Max | Unit |
|--|----------------|-----|-----|------|------|
| Reverse Leakage (V _R = 10 V) | I _R | - | Ι | 1.0 | μA |
| Forward Voltage (I _F = 200 mA) | V _F | _ | - | 0.60 | Vdc |



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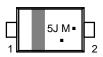
www.onsemi.com

30 VOLT SCHOTTKY BARRIER DIODE





MARKING DIAGRAM



5J = Device Code

M = Date Code*

= Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation position may vary depending upon manufacturing location.

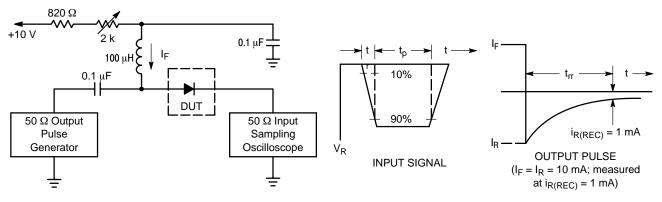
ORDERING INFORMATION

| Device | Package | Shipping [†] |
|-------------|----------------------|--------------------------------|
| RB520S30T1G | SOD-523 (Pb-Free) | 4 mm Pitch 3000/Tape & Reel |
| RB520S30T5G | SOD-523 (Pb-Free) | 2 mm Pitch 8000/Tape & Reel |

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

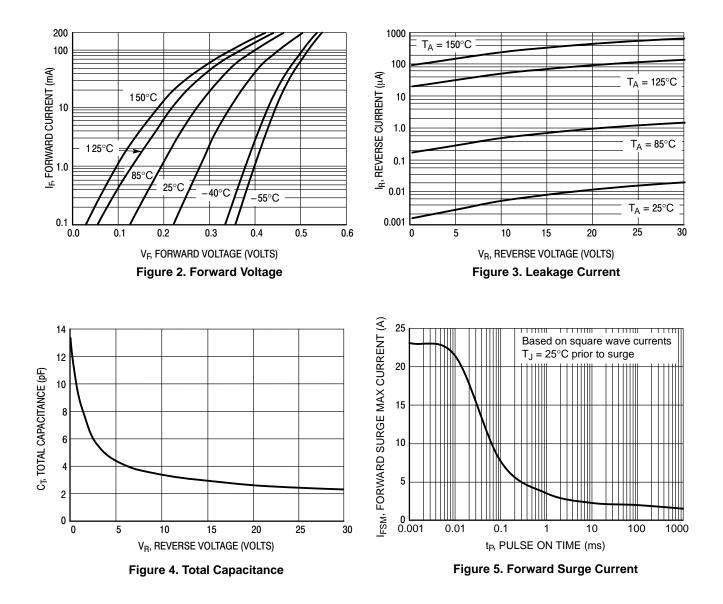
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RB520S30T1G, RB520S30T5G



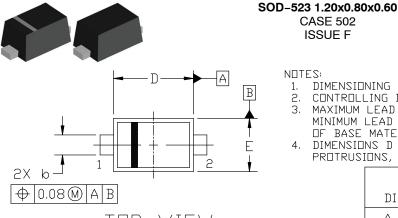
Notes: 1. A 2.0 k Ω variable resistor adjusted for a Forward Current (I_F) of 10 mA. 2. Input pulse is adjusted so I_{R(peak)} is equal to 10 mA. 3. t_p » t_{rr}



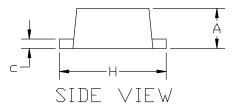


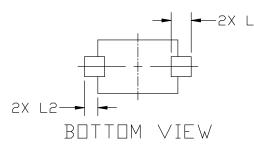
MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS

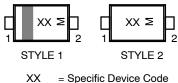








GENERIC **MARKING DIAGRAM***



Μ Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

| STYLE 1: | STYLE 2: | |
|--------------------------------|----------|-------------|
| PIN 1. CATHODE (POLARITY BAND) | | NO POLARITY |
| 2. ANODE | | |

DATE 08 FEB 2024

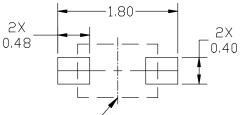
DURSEM

NDTES:

CASE 502 ISSUE F

- 1.
- 2.
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018. CONTROLLING DIMENSION: MILLIMETERS. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH, MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS З. OF BASE MATERIAL.
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. 4.

| | MILLIMETERS | | | |
|-----|-------------|------|------|--|
| DIM | MIN. | NDM. | MAX. | |
| A | 0.50 | 0.60 | 0.70 | |
| b | 0.25 | 0.30 | 0.35 | |
| С | 0.07 | 0.14 | 0.20 | |
| D | 1.10 | 1.20 | 1.30 | |
| E | 0.70 | 0.80 | 0.90 | |
| Н | 1.50 | 1.60 | 1.70 | |
| L | 0.30 REF | | | |
| L2 | 0.15 | 0.20 | 0.25 | |





RECOMMENDED MOUNTING FOOTPRINT

*For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference manual, SOLDERRM/D.

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|------------------|------------------------|---|-------------|--|
| DESCRIPTION: | SOD-523 1.20x0.80x0.60 | | PAGE 1 OF 1 | |

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