



## Bill of Materials for the NCP702SN18T1GEVB Evaluation Board

| Designator | Quantity | Description                      | Value         | Tolerance | Footprint | Manufacturer     | Manufacturer Part Number | Substitution Allowed | Lead Free |
|------------|----------|----------------------------------|---------------|-----------|-----------|------------------|--------------------------|----------------------|-----------|
| CIN        | 1        | Ceramic Capacitor, X7R           | 1 $\mu$ F/50V | $\pm$ 10% | 1206      | MULTICOMP        | MCCA000571               | Yes                  | Yes       |
| COUT       | 1        | Ceramic Capacitor, X7R           | 1 $\mu$ F/50V | $\pm$ 10% | 1206      | MULTICOMP        | MCCA000571               | Yes                  | Yes       |
| U1         | 1        | Ultra-Low Noise LDO, 200mA, 1.8V | N/A           | N/A       | TSOP-5    | ON Semiconductor | NCP702SN18T1G            | No                   | Yes       |
| JP1, JP2   | 2        | Jumper, RM 2.54 mm               | N/A           | N/A       | 2.54mm    | Harwin           | M7686-05                 | Yes                  | Yes       |
| N/A        | 6        | Pin 1.3mm                        | N/A           | N/A       | 1.3mm     | Various          | Various                  | Yes                  | Yes       |