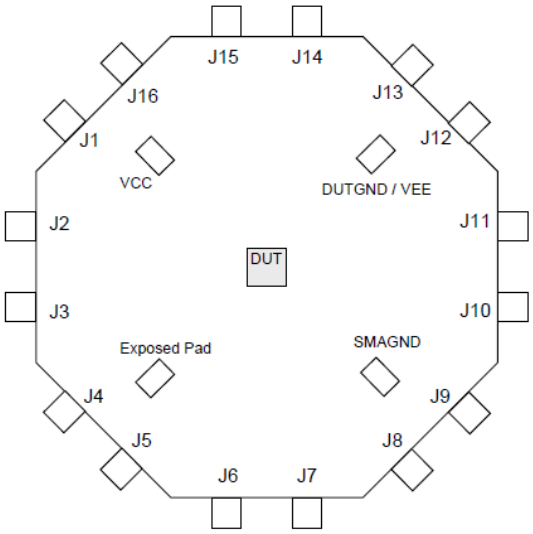
NB6L72MMNGEVB Test Procedure



	J1	J2	J3	J4	J5	J6	J7	J8	J9	J10	J11	J12	J13	J14	J15	J16
Pin#	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Pin Name	SEL0	D0	D0b	VTD0	VTD1	D1	D1b	SEL1	GND	Q1	Q1b	VCC	VDD	Q0b	Q0	GND

Board Check

Ground the VEE/DUT GND, SMA and EP (Surface Mount Test Points)
Apply 3.3 V on VCC (Surface Mount Test Point)

- Measure Current on VCC supply, should be roughly 80 mA to 90 mA
- Measure SEL0 (Pin1) and SEL1 (Pin8), should be roughly 3.0 V
- Measure Resistance across VTD1 (Pin5) to D1 (Pin6), should be roughly 50 Ohms
- Measure Resistance across VTD1 (Pin5) to D1b (Pin7), should be roughly 50 Ohms
- Measure Resistance across VTD0 (Pin4) to D0 (Pin2), should be roughly 50 Ohms
- Measure Resistance across VTD0 (Pin4) to D0b (Pin3), should be roughly 50 Ohms

Functional Check (outputs Toggle)

Ground D0b (Pin3), D1 (Pin6), SEL0 (Pin1) and SEL1 (Pin8)
Apply 3.3 V on D0 (Pin2) and D1b (Pin7)

- Measure Voltage on Q0 (Pin15), Q1 (Pin11), should be roughly 3.3 V
- Measure Voltage on Q0b (Pin14), Q1b (Pin10), should be roughly 2.4 V
- Check Current on VCC supply, should be roughly 150 mA

Ground D0b (Pin3), D1 (Pin6)
Apply 3.3 V on D0 (Pin2), D1b (Pin7), SEL0 (Pin1) and SEL1 (Pin8)

- Measure Voltage on Q0 (Pin15), Q1 (Pin11), should be roughly 2.4 V
- Measure Voltage on Q0b (Pin14), Q1b (Pin10), should be roughly 3.3 V
- Check Current on VCC supply, should be roughly 150 mA