

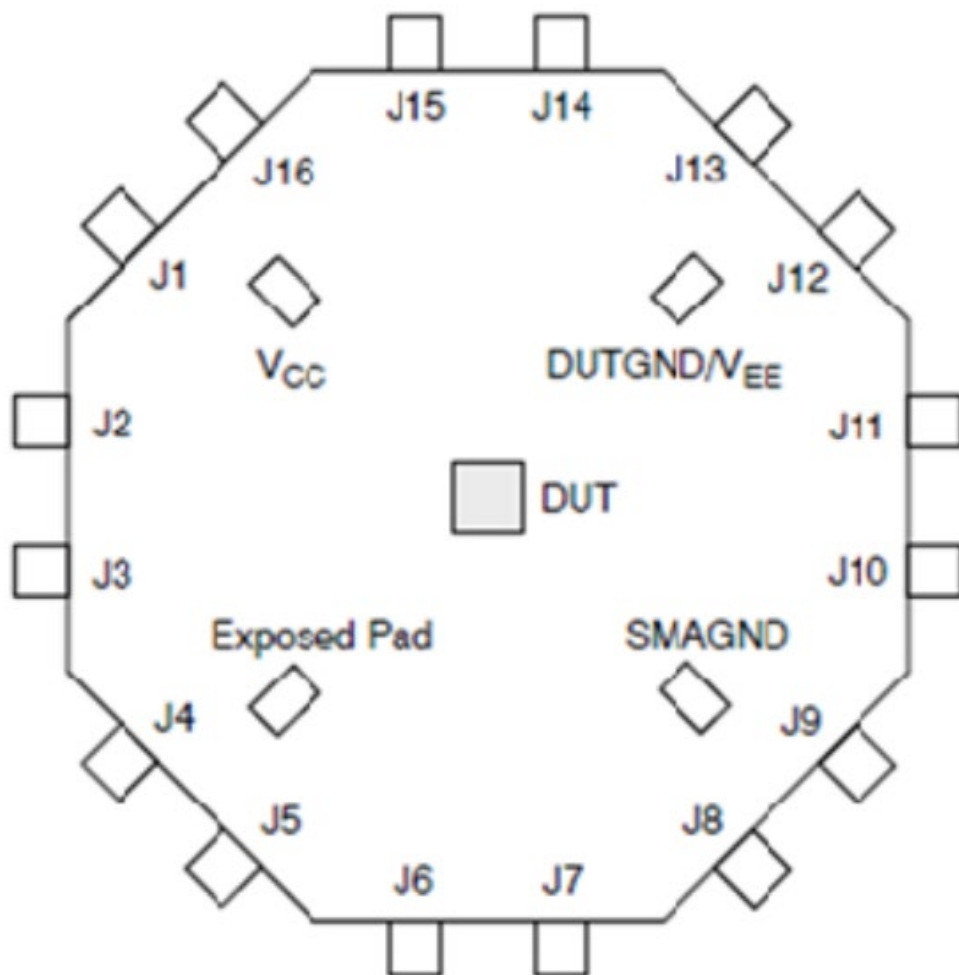
NB7L72MMNGEVB – Basic Build Verification Test Procedure

Purpose: This procedure verifies correct assembly of the NB7L72MMNGEVB evaluation board by confirming:

- Proper power delivery and current draw
- Correct resistances / voltages on pins ensuring all pads are correctly soldered with good contact.

1. Required Equipment

- Bench power supply
- DMM (voltage and resistance checks)



2. **Power-Up Verification**

- Connect VEE/DUT_GND and ExPAD anvil to 0V
- Connect VCC and SMA_GND anvil to 3.3V
- Measure VCC current – should be roughly 135mA
- Measure voltage on IN0 (J1), IN0b (J2), IN1b (J3), IN1 (J4), VT1 (J5) and VT0 (J16) – should be roughly 1.65V
- Measure voltage on Q1 (J9), Q1b (J10), Q0b (J11) and Q0 (J12) – should be roughly 2.8V
- Measure voltage on SEL1 (J6) and SEL0 (J15) – should be 3.3V

3. **Resistance Verification – Verify resistance between each of the 2 points below is between 40 and 60 Ohms.**

- Measure resistance between VT1 (J5) and IN1 (J4)
- Measure resistance between VT1 (J5) and IN1b (J3)
- Measure resistance between VT0 (J16) and IN0 (J1)
- Measure resistance between VT0 (J16) and IN0b (J2)