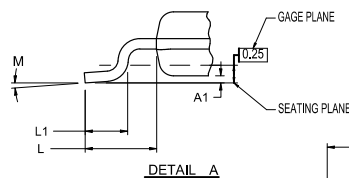
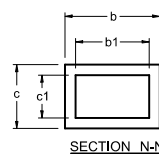
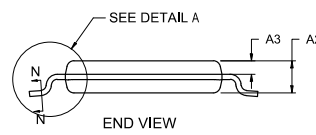
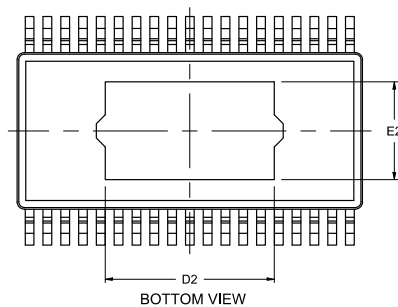
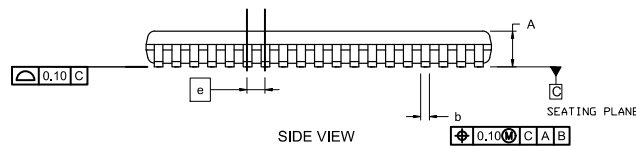
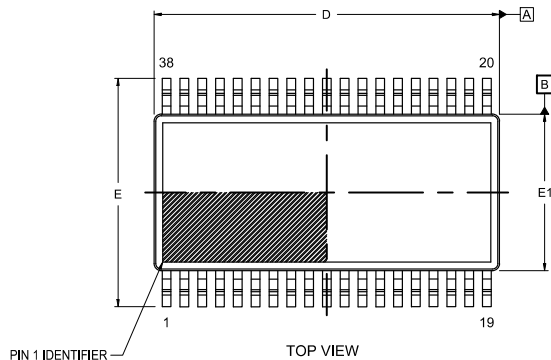
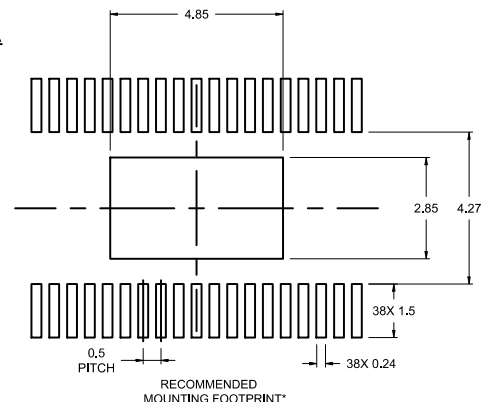


TSSOP38 EP 9.7x4.4
CASE 137AB
ISSUE O

DATE 05 JUN 2019



DETAIL A



* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD PROTRUSION.
4. MAXIMUM MOLD PROTRUSION 0.15mm PER SIDE.
5. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION.
ALLOWABLE DAMBAR PROTRUSION SHALL NOT BE IN 0.13 TOTAL
IN EXCESS OF b DIMENSION AT MAXIMUM MATERIAL CONDITION.

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	---	---	1.10
A1	0.05	0.10	0.15
A2	0.85	0.90	0.95
A3	0.34	---	0.43
b	0.17	---	0.27
b1	0.20 REF		
c	0.09	---	0.20
c1	0.127 REF		
D	9.60	9.70	9.80
D2	4.496	4.750	4.852
E	6.25	6.40	6.55
E1	4.30	4.40	4.50
E2	2.489	2.743	2.845
e	0.50 BSC		
L	1.00 REF		
L1	0.50	---	0.70
M	0°	---	8°

**GENERIC
MARKING DIAGRAM***



XXXX = Specific Device Code
A = Assembly Location
L = Wafer Lot
Y = Year
W = Work Week

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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