

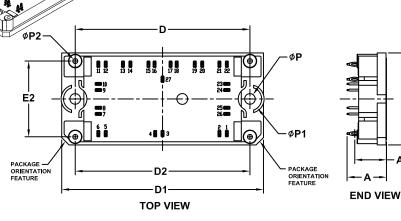
NOTE 4

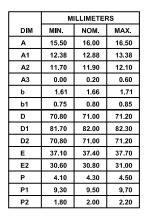
PIN POSITION

13.00

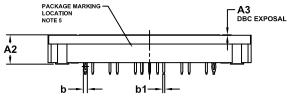
28.20

**DATE 20 DEC 2022** 

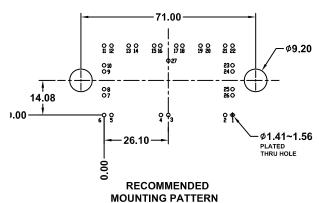




PIN POSITION



## SIDE VIEW



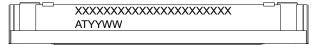
\* For additional Information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009
- 2. CONTROLLING DIMENSION: MILLIMETERS
- 3. DIMENSIONS b AND b1 APPLY TO THE PLATED TERMINALS AND ARE MEASURED AT DIMENSION A1
- 4 PIN POSITION TO FRANCE IS + 0 4mm
- 5. PACKAGE MARKING IS LOCATED AS SHOWN ON THE SIDE OPPOSITE THE PACKAGE ORIENTATION FEATURES

P <b>I</b> N	Х	Υ	P <b>i</b> N	Х	Υ
1	52.20	0.00	15	20.35	28.20
2	49.20	0.00	16	22.85	28.20
3	26.10	0.00	17	29.35	28.20
4	23.10	0.00	18	31.85	28.20
5	3.00	0.00	19	39.20	28.20
6	0.00	0.00	20	42.20	28.20
7	0.00	8.00	21	49.20	28.20
8	0.00	10.50	22	52.20	28.20
9	0.00	17.70	23	52.20	20.20
10	0.00	20.20	24	52.20	17.70
11	0.00	28.20	25	52.20	10.50
12	3.00	28.20	26	52.20	8.00
13	10.00	28.20	27	26.10	22.10

## GENERIC MARKING DIAGRAM\*



XXXXX = Specific Device Code

AT = Assembly & Test Site Code

YYWW = Year and Work Week Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

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DESCRIPTION:	PIM27, 71X37.4 (PRESSFIT	F PIN)	PAGE 1 OF 1	

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