

PIM22 33.80x42.50x10.00 CASE 180HL **ISSUE O**

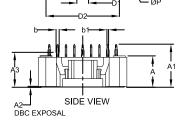
ACKAGE MARKING LOCATION

DATE 29 AUG 2023

NOTES:

- 1. CONTROLLING DIMENSION: MILLIMETERS
- 2. PIN POSITION TOLERANCE IS ± 0.4mm
- 3. PRESS FIT PIN

	MILLIMETERS					
D I M	MIN.	NOM.	MAX.			
Α	11.65	12.00	12.35			
A1	16.00	16.50	17.00			
A2	0.00	0.35	0.60			
A3	12.85	13.35	13.85			
b	1.15	1.20	1.25			
b1	0.59	0.64	0.69			
D	33.50	33.80	34.10			
D1	4.40	4.50	4.60			
D2	27.95	28.10	28.25			
Е	47.70	48.00	48.30			
E1	42.35	42.50	42.65			
E2	52.90	53.00	53.10			
E3	62.30	62.80	63.30			
E4	4.90	5.00	5.10			
Р	2.20	2.30	2.40			



8

0.00

PIN POSITION TABLE:

END VIEW

Pin	Х	Υ	Function	Pin	Х	Υ	Function
1	0	0	AC2	12	28.8	12.8	DC+
2	0	3.2	AC2	13	25.6	12.8	DC+
3	0	6.4	S3	14	22.4	12.8	DC+
4	0	9.6	G3	15	32	22.4	DC-1
5	9.6	0	TH1	16	32	25.6	DC-1
6	12.8	0	TH2	17	28.8	22.4	G2
7	28.8	0	S4	18	28.8	25.6	S2
8	28.8	3.2	G4	19	0	25.6	AC1
9	32	0	DC-2	20	0	22.4	AC1
10	32	3.2	DC-2	21	0	19.2	S1
11	32	12.8	DC+	22	0	16	G1

Ø0.94~1.09 PLATED THRU HOLE RECOMMENDED

MOUNTING PATTERN

* For additional Information on our Pb—Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

GENERIC MARKING DIAGRAM*

XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
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XXXXX = Specific Device Code

= Assembly & Test Site Code YYWW = Year and Work Week Code *This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ", may or may not be present. Some products may not follow the Generic Marking.

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