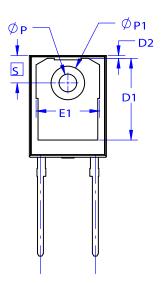


DATE 09 JUL 2020



## NOTES:

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSION AND TOLERANCE AS PER ASME Y14.5-2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

DIM	MILLIMITERS			
DIM	MIN	NOM	MAX	
Α	4.58	4.70	4.82	
A1	2.20	2.40	2.60	
A2	1.40	1.50	1.60	
b	1.17	1.26	1.35	
b2	1.60	1.72	1.84	
С	0.51	0.61	0.71	
D	20.32	20.57	20.82	
D1	13.08	~	~	
D2	0.51	0.93	1.35	
Е	15.37	15.62	15.87	
E1	12.81	~	~	
E2	4.96	5.08	5.20	
е	~	11.12	~	
L	19.75	20.00	20.25	
L1	3.69	3.81	3.93	
ØР	3.51	3.58	3.65	
Ø <b>P</b> 1	6.60	6.80	7.00	
Q	5.34	5.46	5.58	
S	5.34	5.46	5.58	

## GENERIC MARKING DIAGRAM\*



XXXXX = Specific Device Code

A = Assembly Location Y = Year

WW = Year Work Week

ZZ = Assembly Lot Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb–Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

DOCUMENT NUMBER:	98AON15748H	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.		
DESCRIPTION:	TO-247-2LD		PAGE 1 OF 1	

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.