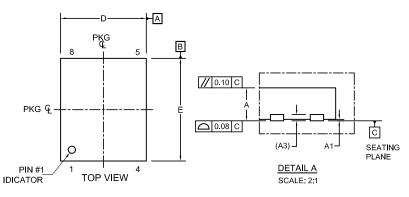


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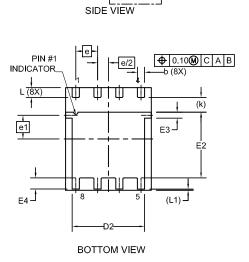
**DATE 25 JUN 2021** 

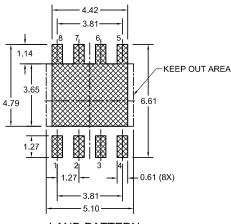


SEE DETAIL A

## NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- 3. COPLANARITY APPLIES TO THE EXPOSED PADS AS WELL AS THE TERMINALS.
- 4. DIMENSIONS D1 AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.
- SEATING PLANE IS DEFINED BY THE TERMINALS. "A1" IS DEFINED AS THE DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT ON THE PACKAGE BODY.
- 6. IT IS RECOMMENDED TO HAVE NO TRACES OR VIAS WITHIN THE KEEP OUT AREA.





## LAND PATTERN RECOMMENDATION

\*FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

DIM	MILLIMETERS			
DIIVI	MIN.	NOM.	MAX.	
Α	0.90	1.00	1.10	
A1	0.00	-	0.05	
А3	0.20 REF			
b	0.37	0.42	0.47	
D	4.90	5.00	5.10	
D2	4.13	4.23	4.33	
E	5.90	6.00	6.10	
E2	3.74	3.84	3.94	
E3	0.25	0.35	0.45	
E4	0.60	0.70	0.80	
е	1,27 BSC			
e/2	0.635 BSC			
e1	1.31 BSC			
k	0.86 REF			
L	0.47	0.57	0.67	
L1	0.08REF			

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