

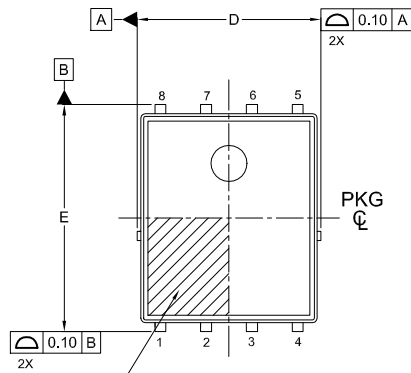
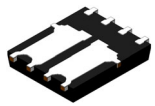
MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

ON Semiconductor®

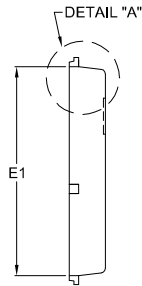


PQFN8 5X6, 1.27P CASE 483BL ISSUE A

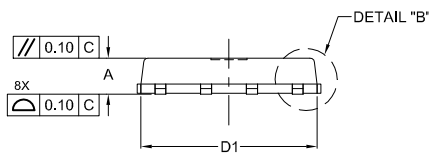
DATE 23 APR 2021



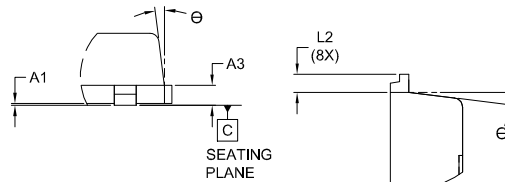
TOP VIEW



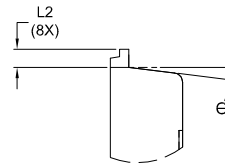
SIDE VIEW



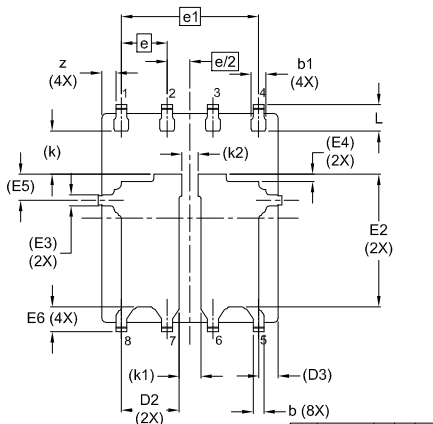
FRONT VIEW



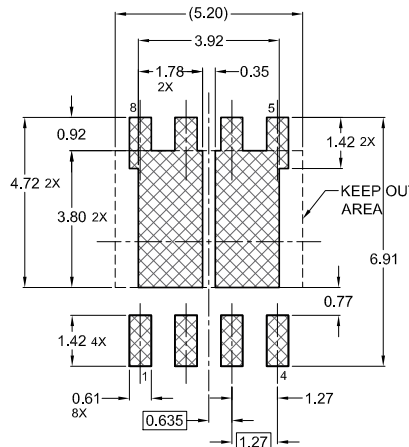
DETAIL "B"



DETAIL "A"



BOTTOM VIEW



LAND PATTERN
RECOMMENDATION

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
2. CONTROLLING DIMENSION: MILLIMETERS
3. COPLANARITY APPLIES TO THE EXPOSED PADS AS WELL AS THE TERMINALS.
4. DIMENSIONS D1 AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.
5. SEATING PLANE IS DEFINED BY THE TERMINALS. "A1" IS DEFINED AS THE DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT ON THE PACKAGE BODY.
6. IT IS RECOMMENDED TO HAVE NO TRACES OR VIAS WITHIN THE KEEP OUT AREA.

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
A1	0.00	-	0.05
A3	0.23	0.28	0.33
b	0.26	0.31	0.36
b1	0.36	0.41	0.46
D	5.00	5.10	5.20
D1	4.80	4.90	5.00
D2	1.51	1.61	1.71
D3	0.54 REF		
E	6.20	6.30	6.40
E1	5.70	5.80	5.90
E2	3.58	3.68	3.78
E3	0.30 REF		
E4	0.10	0.20	0.30
E5	0.72 REF		
E6	0.59	0.69	0.79
e	1.27 BSC		
e1	3.81 BSC		
e/2	0.635 BSC		
k	1.19 REF		
k1	0.60 REF		
k2	0.45 REF		
L	0.64	0.74	0.84
L2	0.15	0.25	0.35
z	0.39 REF		
θ	0°	-	7°

*FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

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