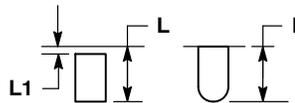
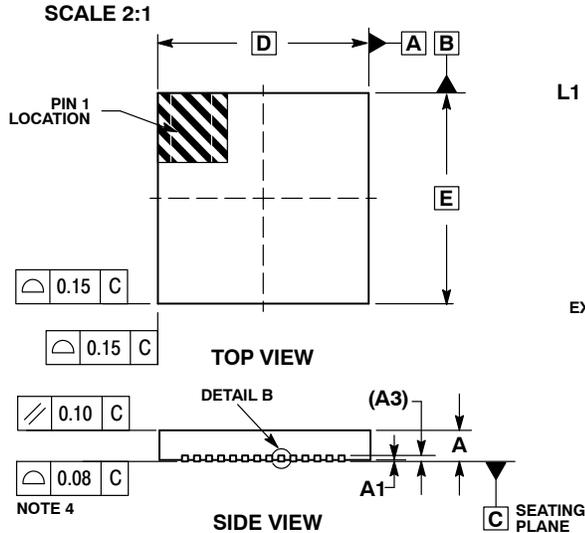


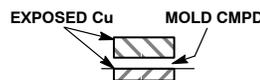
1 56
SCALE 2:1

QFN56 7x7, 0.4P
CASE 485BT
ISSUE A

DATE 02 DEC 2014



DETAIL A
ALTERNATE TERMINAL
CONSTRUCTIONS

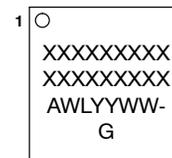


DETAIL B
ALTERNATE
CONSTRUCTIONS

- NOTES:
- DIMENSIONS AND TOLERANCING PER ASME Y14.5M, 1994.
 - CONTROLLING DIMENSION: MILLIMETERS.
 - DIMENSION b APPLIES TO THE PLATED TERMINAL AND IS MEASURED ABETWEEN 0.15 AND 0.25 MM FROM TERMINAL TIP.
 - COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.
 - FOR DEVICE OPN CONTAINING W OPTION, DETAILS A AND B, ALTERNATE CONSTRUCTION PERTAINING TO THE L1 DIMENSION, ARE NOT APPLICABLE.

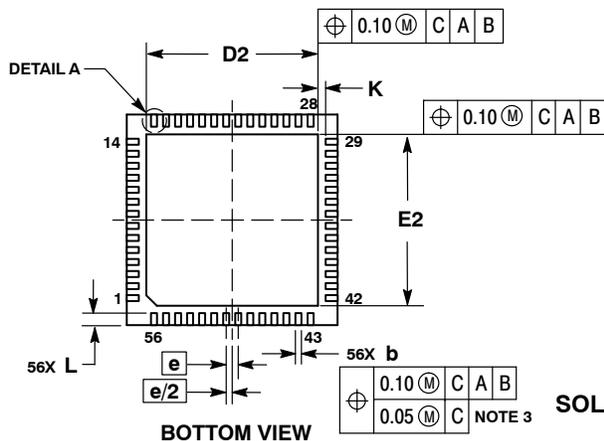
| MILLIMETERS | | |
|-------------|------|------|
| DIM | MIN | MAX |
| A | 0.80 | 1.00 |
| A1 | 0.00 | 0.05 |
| A3 | 0.20 | REF |
| b | 0.15 | 0.25 |
| D | 7.00 | BSC |
| D2 | 5.60 | 5.80 |
| E | 7.00 | BSC |
| E2 | 5.60 | 5.80 |
| e | 0.40 | BSC |
| K | 0.25 | REF |
| L | 0.30 | 0.50 |
| L1 | 0.05 | 0.15 |

GENERIC
MARKING DIAGRAM*

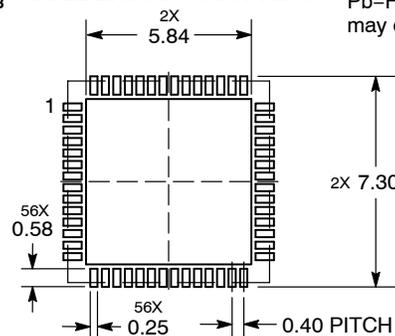


- A = Assembly Location
- WL = Wafer Lot
- YY = Year
- WW = Work Week
- G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present.



RECOMMENDED
SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

| | | |
|------------------|------------------|--|
| DOCUMENT NUMBER: | 98AON56933E | Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. |
| DESCRIPTION: | QFN56 7X7, 0.40P | PAGE 1 OF 1 |

onsemi and Onsemi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.