



### QFN24, 4x4, 0.5P CASE 485DJ **ISSUE O**

**DATE 28 OCT 2013** 

- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ASME
- THE PROPERTY OF THE PROPERTY O
- FROM THE TERMINAL TIP.

  4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

	MILLIMETERS	
DIM	MIN	MAX
Α	0.80	0.90
A1	0.00	0.05
A3	0.20 REF	
b	0.20	0.30
D	4.00 BSC	
D2	2.40	2.60
E	4.00 BSC	
E2	2.40	2.60
е	0.50 BSC	
L	0.30	0.50
11		0.15

## **GENERIC** MARKING DIAGRAM\*



XXXXXX= Specific Device Code

Α = Assembly Location

= Wafer Lot L Υ = Year W = Work Week = Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present.

#### D **A3** EXPOSED Cu MOLD CMPD В PIN ONE REFERENCE **DETAIL B** ALTERNATE CONSTRUCTIONS 2X \arr 0.10 C 2X | $\bigcirc$ | 0.10 | C | **TOP VIEW** (A3) DETAIL B 0.10 C **DETAIL A** $\triangle$ 0.08 C ALTERNATE CONSTRUCTIONS NOTE 4 SEATING PLANE С SIDE VIEW

24X L

E2

0.10

0.05 С NOTE 3

CAB

# **RECOMMENDED SOLDERING FOOTPRINT\***

<-D2→

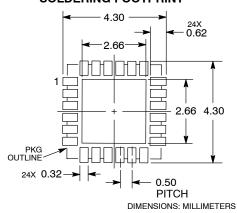
<u>andhan</u>

**BOTTOM VIEW** 

е

e/2

DETAIL A



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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