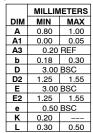




QFN16 3x3, 0.5P CASE 485FM **ISSUE A**

DATE 30 JAN 2018

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS.
- DIMENSION & APPLIES TO PLATED
 TERMINAL AND IS MEASURED BETWEEN
- 0.25 AND 0.30 MM FROM TERMINAL.
 COPLANARITY APPLIES TO THE EXPOSED
- PAD AS WELL AS THE TERMINALS.
 OUTLINE MEETS JEDEC DIMENSIONS PER MO-220, VARIATION VEED-6.



GENERIC MARKING DIAGRAM*



= Assembly Location

= Wafer Lot

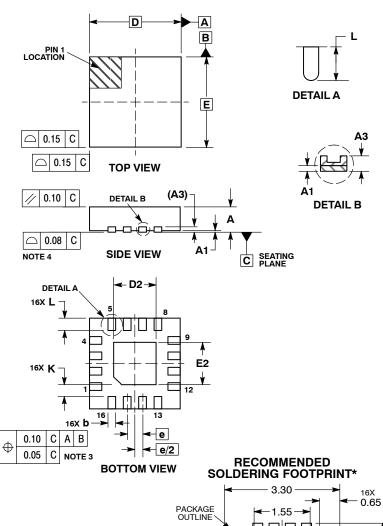
= Year

W = Work Week

= Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present. Some products may not follow the Generic Marking.



16X 0.30

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

0.50 **PITCH DIMENSIONS: MILLIMETERS**

.55 3.30

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