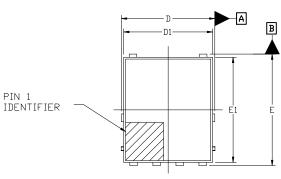


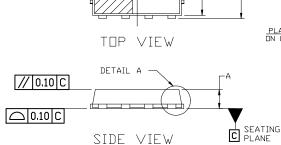
DFNW5 5x6, FULL-CUT SO8FL WF

CASE 507BD ISSUE O

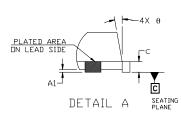
DATE 13 APR 2021

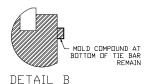
MILL IMETEDS





SIDE VIEW

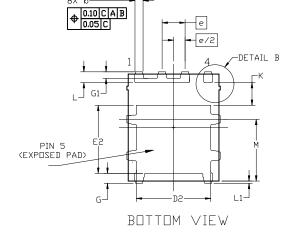




NOTES

- TES:
 DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
 CONTROLLING DIMENSION: MILLIMETERS
 DIMENSIONS D1 AND E1 DO NOT INCLUDE MOLD FLASH,
 PROTRUSIONS, OR GATE BURRS.
 THIS PACKAGE CONTAINS WETTABLE FLANK DESIGN
 FEATURES TO AID IN FILLET FORMATION ON THE LEADS
 DURING MOUNTING.

	MILLIME LEK2			
DIM	MIN.	N□M.	MAX.	
А	0.90	1.00	1.10	
A1	0.00	1	0.05	
b	0.33	0.41	0.51	
C	0.23	0.28	0.33	
D	5.00	5.15	5.30	
D1	4.80	5.00	5.20	
D2	3.90	4.10	4.30	
E	6.00	6.15	6.30	
E1	5.70	5.90	6.10	
E2	3.55	3.75	3.95	
е		1.27 BSC		
G	0.50	0.55	0.70	
G1	0.26	0.36	0.46	
k	1.10	1.25	1.40	
L	0.50	0.60	0.70	
L1	0.150 REF			
М	3.00	3.40	3.80	
θ	0°		12°	



GENERIC MARKING DIAGRAM*



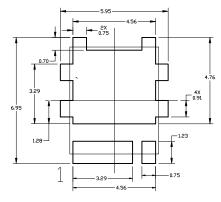
XXXX = Specific Device Code

Α = Assembly Location

Υ = Year

W = Work Week

ΖZ = Assembly Lot *This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.



RECOMMENDED MOUNTING FOOTPRINT

For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mouring Techniques Reference Manual, SDLDERRM/D.

DOCH	MENIT	NUMB	ED.
DUCU	IVICIN I	NUMB	ER:

8X b

98AON31027H

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DESCRIPTION:

DFNW5 5x6, FULL-CUT SO8FL WF

PAGE 1 OF 1

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