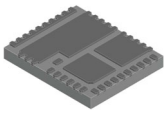

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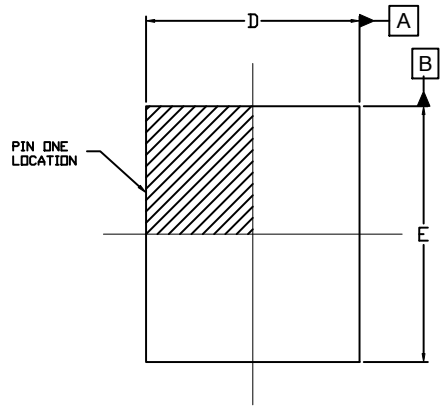
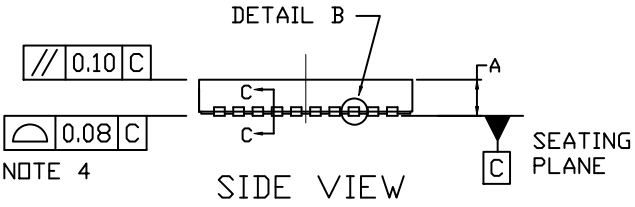
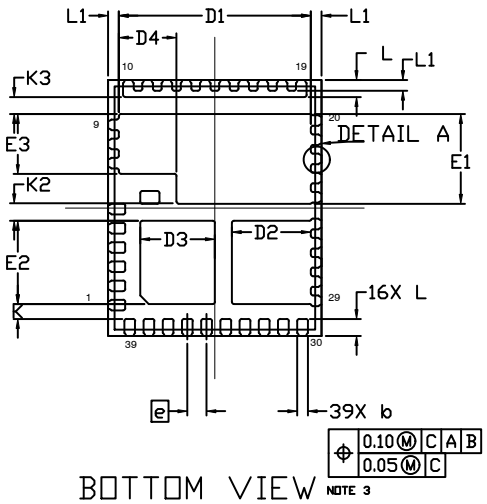


TOP VIEW



NOTE 4

SIDE VIEW



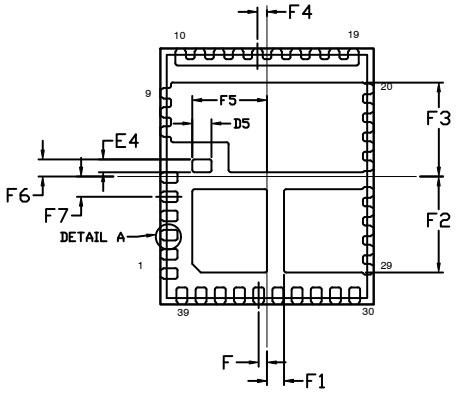
BOTTOM VIEW

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 MM FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

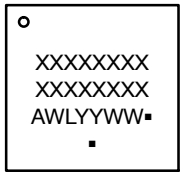
DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.70	0.75	0.80
A1	---	---	0.05
A3	0.20 REF		
A4	0.10	---	---
b	0.20	0.25	0.30
D	4.90	5.00	5.10
D1	4.40	4.50	4.60
D2	1.75	1.85	1.95
D3	1.65	1.75	1.85
D4	1.25	1.35	1.45
D5	0.35	0.45	0.55
E	5.90	6.00	6.10
E1	2.00	2.10	2.20
E2	1.85	1.95	2.05
E3	1.30	1.40	1.50
E4	0.20	0.30	0.40

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
e	0.45 BSC		
K	0.35	---	---
K2	0.40 REF		
K3	0.40 REF		
L	0.35	0.40	0.45
L3	---	---	0.10
K	0.35	---	---
F	0.195 REF		
F1	0.40 REF		
F2	2.25 REF		
F3	2.20 REF		
F4	0.23 REF		
F5	1.75 REF		
F6	0.40 REF		
F7	0.475 REF		



BOTTOM VIEW
(ADDITIONAL DETAIL)

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code
A = Assembly Location
WL = Wafer Lot
YY = Year
WW = Work Week
▪ = Pb−Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb−Free indicator, “G” or microdot “▪”, may or may not be present. Some products may not follow the Generic Marking.

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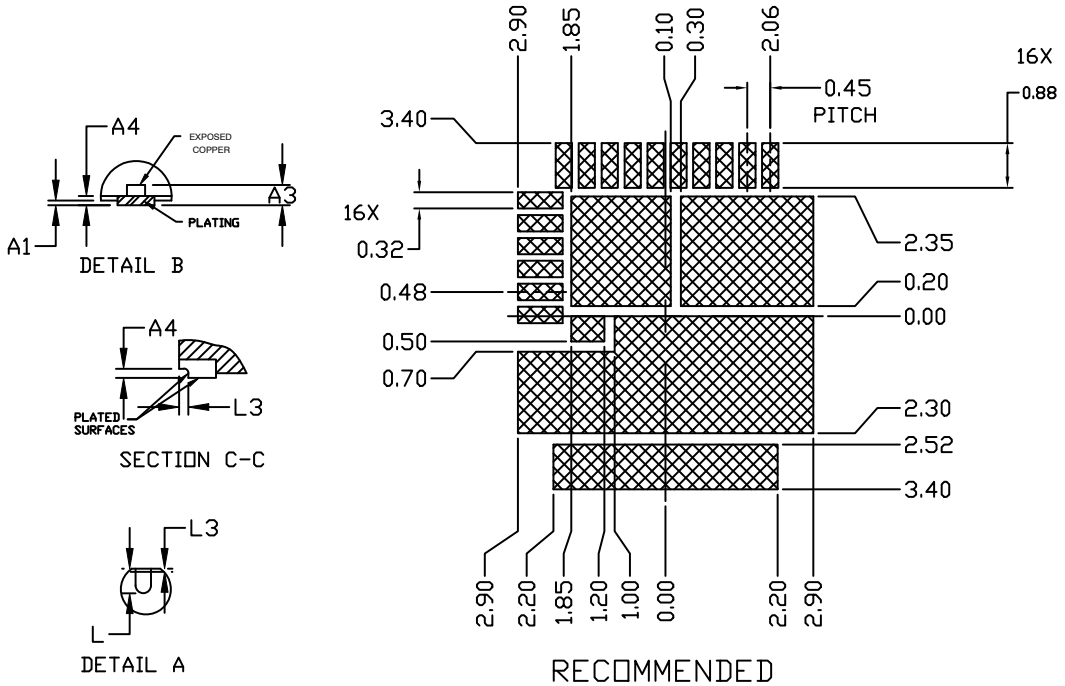

* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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