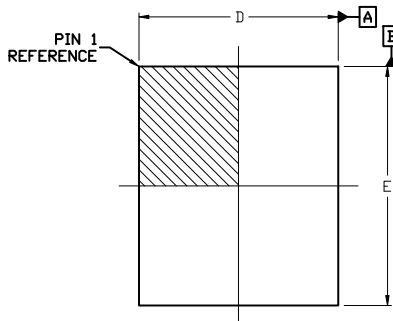
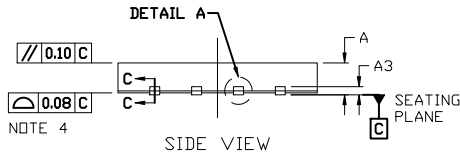


WQFNW16 5x6, 1.05P
CASE 512AN
ISSUE O

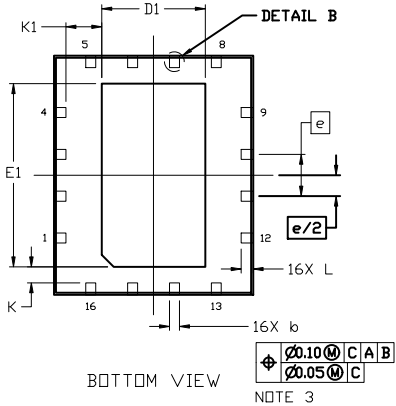
DATE 30 JUL 2021



TOP VIEW

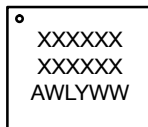


SIDE VIEW



BOTTOM VIEW

GENERIC MARKING DIAGRAM*

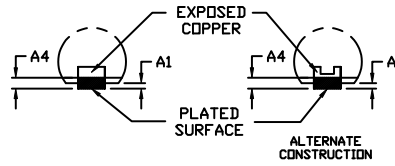


XXXX = Specific Device Code
 A = Assembly Location
 WL = Wafer Lot
 Y = Year
 WW = Work Week

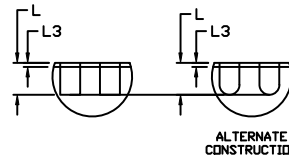
*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

NOTES:

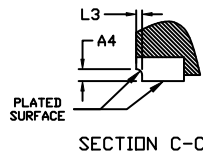
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5, 2009.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.25MM FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.
5. DIMENSIONS DO NOT INCLUDE BURRS. MOLD FLASH OR BURRS DO NOT EXCEED 0.10MM.
6. THIS DEVICE CONTAINS WETTABLE FLANK DESIGN FEATURE TO AID IN FILLET FORMATION ON THE LEADS DURING MOUNTING.



DETAIL A

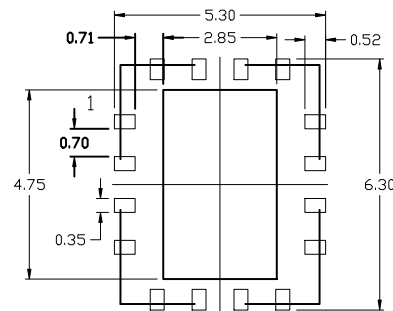


DETAIL B



SECTION C-C

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.70	0.75	0.80
A1	0.00	---	0.05
A3	0.20 REF		
A4	0.10	---	---
b	0.20	0.25	0.30
D	4.90	5.00	5.10
D1	2.60	2.70	2.80
E	5.90	6.00	6.10
E1	4.50	4.60	4.70
e	1.05 BSC		
K	0.40 REF		
K1	0.85 REF		
L	0.25	0.30	0.35
L3	---	---	0.09



RECOMMENDED MOUNTING PATTERN

* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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DESCRIPTION:	WQFNW16 5x6, 1.05P	PAGE 1 OF 1

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