

製品概要

MC100EP11: Clock / Data Fanout Buffer, 1:2 Differential, ECL, 3.3 V / 5.0 V

技術情報は、データシートをご参照ください。

The MC100EP11 is a differential 1 to 2 fanout buffer. The device is pin and functionally equivalent to the LVEL11 device. With AC performance much faster than the LVEL11 device, the EP11 is ideal for applications requiring the fastest AC performance available.

特長

- 220 ps Typical Propagation Delay
- Maximum Frequency > 3 GHz Typical
- PECL Mode Operating Range: VCC= 3.0 V to 5.5 V with VEE= 0 V
- NECL Mode Operating Range: VCC= 0 V with VEE= -3.0 V to -5.5 V
- Open Input Default State
- Safety Clamp on Inputs
- Q Outputs Will Default LOW with Inputs Open or at VEE

電氣的仕様

製品	Pricing (\$/Unit)	Compliance	Status	Type	Channels	Input / Output Ratio	Input Level	Output Level	V _{CC} Typ (V)	t _{jitter} MS Typ (ps)	t _{skew(o-)} Max (ps)	t _{pd} Typ (ns)	t _R & t _F Max (ps)	f _{max} Clock Typ (MHz)	f _{max} Data Typ (Mbps)	Package Type
MC100EP11DG		Pb-free Halide free	Active	Buffer	1	1:2	CM L ECL	ECL	5 3.3	0.2	15	0.22	170	3000		SOIC-8
MC100EP11DR2G		Pb-free Halide free	Active	Buffer	1	1:2	CM L ECL	ECL	5 3.3	0.2	15	0.22	170	3000		SOIC-8
MC100EP11DTG		Pb-free Halide free	Active	Buffer	1	1:2	CM L ECL	ECL	3.3 5	0.2	15	0.22	170	3000		TSSOP-8
MC100EP11DTR2G		Pb-free Halide free	Active	Buffer	1	1:2	CM L ECL	ECL	3.3 5	0.2	15	0.22	170	3000		TSSOP-8
MC100EP11MNR4G		Pb-free Halide free	Active	Buffer	1	1:2	ECL CM L	ECL	3.3 5	0.2	15	0.22	170	3000		DFN-8

詳細は、弊社 www.onsemi.jp の営業または販売代理店にお問い合わせください。

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