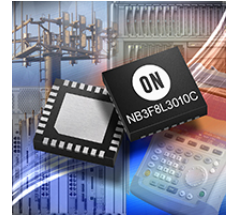


製品概要

NB3F8L3010C: Clock / Data Fanout Buffer, 3:1:10 Differential, LVCMOS, 3.3 V / 2.5 V / 1.8 V / 1.5 V

技術情報は、データシートをご参照ください。



The NB3F8L3010C is a 3:1:10 Clock or Data fanout buffer operating on a 3.3 V or 2.5 V Core VDD and a flexible 3.3 V or 2.5 V or 1.8 V or 1.5 V VDDO supply which must be equal or less than VDD.

特長

- Ten CMOS / LVTTTL Outputs up to 200 MHz
- Differential Inputs Accept LVPECL, LVDS, HCSL, or SSTL
- Crystal Oscillator Interface
- Crystal Input Frequency Range: 10 MHz to 40 MHz
- Output Skew: 10 ps Typical
- Additive RMS Phase Jitter @ 125 MHz, (12 kHz - 20 MHz): 0.03 ps(Typical)
- Synchronous Output Enable
- Output Defined Level When Input is Floating
- Pure 3.3 V or 2.5 V Operating Mode, 3.3 V Core with 2.5 V/1.8 V/1.5 V Output Supply; or 2.5 V Core with 1.8 V/1.5 V Output Supply.

アプリケーション

- Wireless and Wired Infrastructure
- Networking and Data Communications
- High-End Computing
- Clock Distribution

最終製品

- Ethernet Switches / Routers
- Servers
- Test and Measurement
- ATE

電気的仕様

製品	Pricing (\$/Unit)	Compliance	Status	Type	Chans	Input / Output Ratio	Input Level	Output Level	V _{CC} Typ (V)	t _{jitter} RMS Typ (ps)	t _{skew(o)} Max (ps)	t _{pd} Typ (ns)	t _R & t _F Max (ps)	f _{max} Clock Typ (MHz)	f _{max} Data Typ (Mbps)	Package Type
NB3F8L3010CMNG		Pb-free Halide free	Active	Buffer	1	3:1:10	LVC MOS	LVC MOS	3.3	0.13	50		500	100		QFN-32
									2.5	0.08						
							LVT TL			0.14						
							LVD S			0.27						
							HC SL			0.18						
							Crystal			0.19						
							SST L									
							LVP ECL									
NB3F8L3010CMNR4G		Pb-free Halide free	Active	Buffer	1	3:1:10	LVP ECL	LVC MOS	3.3	0.13	50		500	100		QFN-32
									2.5	0.18						
							LVD S			0.08						
							LVC MOS			0.14						
							LVT TL			0.19						
							HC SL			0.27						
							SST L									
							Crystal									
NB3F8L3010CMNTWG		Pb-free Halide free	Active													QFN-32

詳細は、弊社 www.onsemi.jp の営業または販売代理店にお問い合わせください。

