

## 製品概要

### MC10E212: 5.0 V ECL 3-Bit Scannable Registered Address Driver

技術情報は、データシートをご参照ください。

The MC10E/100E212 is a scannable registered ECL driver typically used as a fan-out memory address driver for ECL cache driving. In a VLSI array based CPU design, use of the E212 allows the user to conserve array output cell functionality and also output pins. The input shift register is designed with control logic which greatly facilitates its use in boundary scan applications. The 100 Series contains temperature compensation.

### 特長

- Scannable Version E112 Driver
- 1025ps Max. CLK to Output
- Dual Differential Outputs
- Master Reset
- PECL Mode Operating Range: VCC = 4.2 V to 5.7 V with VEE = 0 V
- NECL Mode Operating Range: VCC = 0 V with VEE = -4.2 V to -5.7 V
- Internal Input Pulldown Resistors
- ESD Protection: > 1 KV HBM, > 75 V MM
- Meets or Exceeds JEDEC Spec EIA/JESD78 IC Latchup Test
- Moisture Sensitivity Level 1 For Additional Information, see Application Note AND8003/D

For more features, see the data sheet

詳細は、弊社 [www.onsemi.jp](http://www.onsemi.jp) の営業または販売代理店にお問い合わせください。

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