

## 製品概要

## MC100LVEP05: ECL 2-Input Differential AND/NAND Gate

技術情報は、データシートをご参照ください。

The MC100LVEP05 is a 2-input differential AND/NAND gate. The MC100LVEP05 is the low voltage version of the MC100EP05 and is functionally equivalent to the EL05 and LVEL05 devices. With AC performance much faster than the LVEL05 device, the EP05 is ideal for low voltage applications requiring the fastest AC performance available. The 100 Series contains temperature compensation.

## 特長

- Low Output Jitter
- 3 GHz Max Operating Frequency

## 利点

- Improved System Performance

## アプリケーション

- Any application where a high-speed (and low-speed) AND/NAND function is needed.

## 電氣的仕様

製品	Pricing (\$/Unit)	Compliance	Status	Type	Channels	Input Level	Output Level	V <sub>CC</sub> Typ (V)	f <sub>Toggle</sub> Max (MHz)	t <sub>pd</sub> Typ (ns)	t <sub>Jitter</sub> Typ (ps)	t <sub>R</sub> & t <sub>F</sub> Max (ps)	Package Type
MC100LVEP05DTG		Pb-free Halide free	Active	AND/NAND	1	ECL LVDS CML	ECL	3.3 2.5	3000	0.22	0.2	170	TSSOP-8
MC100LVEP05DTR2G		Pb-free Halide free	Active	AND/NAND	1	CML ECL LVDS	ECL	3.3 2.5	3000	0.22	0.2	170	TSSOP-8
MC100LVEP05MNTXG		Pb-free Halide free	Active	AND/NAND	1	ECL LVDS CML	ECL	2.5 3.3	3000	0.22	0.2	170	DFN-8

詳細は、弊社 [www.onsemi.jp](http://www.onsemi.jp) の営業または販売代理店にお問い合わせください。

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