

製品概要

MC100ELT22: Translator, Dual TTL to Differential PECL

技術情報は、データシートをご参照ください。

The MC10ELT/100ELT22 is a dual TTL to differential PECL translator. Because PECL (Positive ECL) levels are used only +5 V and ground are required. The small outline 8-lead package and the low skew, dual gate design of the ELT22 makes it ideal for applications which require the translation of a clock and a data signal.

特長

- 1.2ns Typical Propagation Delay
 - < 300ps Typical Output to Output Skew
 - PNP TTL Inputs for Minimal Loading
 - Flow Through Pinouts
 - ESD Protection: > 2 KV HBM, > 200 V MM
 - Operating Range: VCC= 4.75 V to 5.25 V with GND= 0 V
 - No Internal Input Pulldown Resistors
 - Meets or Exceeds JEDEC Spec EIA/JESD78 IC Latchup Test
 - Moisture Sensitivity Level 1 For Additional Information, see Application Note AND8003/D
 - Flammability Rating: UL-94 code V-0 @ 1/8", Oxygen Index 28 to 34
- For more features, see the data sheet

電氣的仕様

製品	Pricing (\$/Unit)	Compliance	Status	Channels	Input Level	Output Level	V _{CC} Typ (V)	f _{Max} Typ (MHz)	t _{pd} Typ (ns)	t _r & t _f Max (ps)	Package Type
MC100ELT22DG		Pb-free Halide free	Active	2	TTL	ECL	5	500	1.2	1600	SOIC-8
MC100ELT22DR2G		Pb-free Halide free	Active	2	TTL	ECL	5	500	1.2	1600	SOIC-8
MC100ELT22DTG		Pb-free Halide free	Active	2	TTL	ECL	5	500	1.2	1600	TSSOP-8
MC100ELT22DTR2G		Pb-free Halide free	Active	2	TTL	ECL	5	500	1.2	1600	TSSOP-8

詳細は、弊社 www.onsemi.jp の営業または販売代理店にお問い合わせください。

11/11/2019 作成