

製品概要

MC10ELT21: Translator, Differential PECL to TTL

技術情報は、データシートをご参照ください。

The MC10ELT/100ELT21 is a differential PECL to TTL translator. Because PECL (Positive ECL) levels are used, only +5 V and ground are required. The small outline 8-lead package and the single gate of the ELT21 makes it ideal for those applications where space, performance and low power are at a premium. The VBB pin, an internally generated voltage supply, is available to this device only. For single-ended input conditions, the unused differential input is connected to VBB as a switching reference voltage. VBB may also rebias AC coupled inputs. When used, decouple VBB and VCC via a 0.01 F capacitor and limit current sourcing or sinking to 0.5 mA. When not used, VBB should be left open. The 100 Series contains temperature compensation.

特長

- 3.5ns Typical Propagation Delay
- 24mA TTL Output
- Flow Through Pinouts
- ESD Protection: > 2 KV HBM
- Operating Range: VCC= 4.75 V to 5.25 V with GND= 0 V
- Q Output Will Default LOW with Inputs Left Open or < 1.3 V
- Internal Input Pulldown Resistors
- Meets or Exceeds JEDEC Spec EIA/JESD78 IC Latchup Test
- Moisture Sensitivity Level 1 For Additional Information, see Application Note AND8003/D
- Flammability Rating: UL-94 code V-0 @ 1/8", Oxygen Index 28 to 34

For more features, see the data sheet

電氣的仕様

製品	Pricing (\$/Unit)	Compliance	Status	Channels	Input Level	Output Level	V _{CC} Typ (V)	f _{Max} Typ (MHz)	t _{pd} Typ (ns)	t _R & t _F Max (ps)	Package Type
MC10ELT21DG		Pb-free	Active	1	ECL	TTL	5	100	3.5	750	SOIC-8
		Halide free									
MC10ELT21DR2G		Pb-free	Active	1	ECL	TTL	5	100	3.5	750	SOIC-8
		Halide free									

詳細は、弊社 www.onsemi.jp の営業または販売代理店にお問い合わせください。

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