

製品概要

FS6370: EE Programmable 3-PLL Clock

技術情報は、データシートをご参照ください。

The FS6370 is a CMOS clock generator IC designed to minimize cost and component count in a variety of electronic systems. Three EEPROM-programmable phase-locked loops (PLLs) driving four programmable muxes and post dividers provide a high degree of flexibility. An internal EEPROM permits just-in-time factory programming of devices for end user requirements.

特長

- Just-in-time customization of clock frequencies via internal non-volatile 128-bit serial EEPROM
- I2C-bus serial interface
- Three on-chip PLLs with programmable reference and feedback dividers
- Four independently programmable muxes and post dividers
- Programmable power-down of all PLLs and output clock drivers
- Tristate outputs for board testing
- One PLL and two mux/post-divider combinations can be modified via SEL_CD input
- 5 V to 3.3 V operation
- Accepts 5 MHz to 27 MHz crystal resonators

アプリケーション

- Industrial

電氣的仕様

製品	Pricing (\$/Unit)	Compliance	Status	Input Level	Output Level	V _s Typ (V)	f _{in} Typ (MHz)	f _{out} Typ (MHz)	t _{jitter} (Cy-Cy) Typ (ps)	t _{jitter} (Period) Typ (ps)	t _{jitter} (Φ) Typ (ps)	t _R & t _F Typ (ps)	t _R & t _F Max (ps)	T _A Min (°C)	T _A Max (°C)	Package Type
FS6370-01G-XTD		Pb-free Halide free	Active	TTL CMOS	TTL	3.3 5	5-27	0.8-150	165	390		1800	2100	0	70	SOIC-16

詳細は、弊社 www.onsemi.jp の営業または販売代理店にお問い合わせください。

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