

# EZAIRO 7111





## DSP

Ezairo® 7111DSPITC Ezairo 7111 Ezairo 7100DSP: 375 MIPSEA2M: 2 Mb EEPROM : EzairoEDK Ezairo

- Ezairo Pre Suite Firmware Bundle: Provides up to 8 channels of Wide Dynamic Range Compression (WDRC), 16 kHz sampling rate, and adaptive audio algorithms including a new feedback canceller.
- Miniature, User-Friendly Package: Features large pads for easy hand soldering.
- Ultra-Low Power Consumption: 1090 µA while running Ezairo Pre Suite firmware bundle @ 10.24 MHz with all algorithms active.
- Flexible Programmability: Open-programmable DSP-based system can be customized to the specific signal-processing needs of any manufacturer. Modify or add new algorithms or features without making changes to the chip.
- Quad-Core Architecture: Industry's most integrated, flexible, and power efficient single-chip solution. Features a CFX DSP, HEAR Configurable Accelerator, Arm® Cortex®-M3 processor.
- Ultra-High Audio Fidelity: Provides 85 dB system dynamic range with up to 110 input signal dynamic range to preserve a more detailed sound.
- Configurable System Clock Speeds: Twelve clocking speeds to maximize computing performance versus power consumption.
- Low Audio Delay: Programmable Filter Engine supports 0.044 ms audio delay.

• Audio Processing

• Hearing Aids

	Pricing (\$/Unit)	Compliance	Status	WDRC Channels	Graphic EQ Bands	Program Modes	Advanced Algorithms	Acoustic Indicators	Other Features	Wireless Standards	Package Type
E7111-0-102A19-AG		 	Active	8	33	8				null	SIP-19
E7111-0-102A19-BPG		 	Active	8	33	8					SIP-19