

製品概要

MUN5232DW1: Dual NPN Bipolar Digital Transistor (BRT)

技術情報は、データシートをご参照ください。

This series of digital transistors is designed to replace a single device and its external resistor bias network. The Bias Resistor Transistor (BRT) contains a single transistor with a monolithic bias network consisting of two resistors; a series base resistor and a base-emitter resistor. The BRT eliminates these individual components by integrating them into a single device. The use of a BRT can reduce both system cost and board space.

特長

- Simplifies Circuit Design
- Reduces Board Space
- Reduces Component Count
- S and NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

電氣的仕様

製品	Pricing (\$/Unit)	Compliance	Status	Polarity	I _c Continuous (A)	V _{BRICE0} Min (V)	h _{FE} Min	R1 (kΩ)	R2 (kΩ)	R1/R2 Typ	V _{CE(off)} Max (V)	V _{CE(on)} Min (V)	Package Type
MUN5232DW1T1G	0.0273	Pb-free Halide free	Active	Dual NPN	0.1	50	15	4.7	4.7	1	0.5	2.5	SC-88-6 / SC-70-6 / SOT-363-6
SMUN5232DW1T1G	0.0713	AEC Qualified PPAP Capable Pb-free Halide free	Active	Dual NPN	0.1	50	15	4.7	4.7	1	0.5	2.5	SC-88-6 / SC-70-6 / SOT-363-6

詳細は、弊社 www.onsemi.jp の営業または販売代理店にお問い合わせください。

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