

# NLAS323

## SPST



NLAS323 SPST () 4066 2 / Ultra-Small 8 0.6 CMOS RON

- On Resistance is 20  $\Omega$  Typical at 5.0 V
- Matching is < 1  $\Omega$  Between Sections
- 2 - 6 V Operating Range
- Ultra Low < 5 pC Charge Injection
- Ultra Low Leakage < 1 nA at 5.0 V, 25° C
- Wide Bandwidth > 200 MHz, -3 dB
- CMOS/TTL Compatible
- 2000 V ESD (HBM)
- Ron Flatness +/- 6  $\Omega$  at 5.0 V
- US8 Package

For more features, see the data sheet

|            | Pricing (\$/Unit) | Compliance | Status | Channels | Number of Switches | Configuration | I <sub>cc</sub> Max ( $\mu$ A) | r <sub>on</sub> Max ( $\Omega$ ) | V <sub>CC</sub> Min (V) | V <sub>CC</sub> Max (V) | Package Type |
|------------|-------------------|------------|--------|----------|--------------------|---------------|--------------------------------|----------------------------------|-------------------------|-------------------------|--------------|
| NLAS323USG | 0.3072            |            | Active | 2        | 2                  | SPST          | 2                              | 30                               | 2                       | 5.5                     | US8          |