

製品概要

NLAS4717: Analog Switch, High Bandwidth, Dual SPDT

技術情報は、データシートをご参照ください。

The NLAS4717 is an advanced CMOS analog switch fabricated in Sub-micron silicon gate CMOS technology. The device is a dual Independent Single Pole Double Throw (SPDT) switch featuring two Low RON of 4.5 at 3.0V. The part also features guaranteed Break Before Make switching, assuring the switches never short the driver. The NLAS4717 is available in a 2.0 x 1.5 mm bumped die array, with a 4 x 3 arrangement of solder bumps. The pitch of the solder bumps is 0.5 mm for easy handling.

特長

- Low RDS(on); 4.5Ω at 3.0V
- Matching Between the Switches ±0.5Ω
- Wide Low Voltage Range: 1.8V to 5.5V
- High Bandwidth > 40 MHz
- 1.65V to 5.5V Operating Range
- Low Threshold Voltages on Pins 4 and 8 (CTRL pins)
- Ultra-Low Charge Injection ≤ 6.0pC
- Low Stand-By Current-ICC = 1.0nA (max) @ TA = 25°C
- OVT on Pins 4 and 8 (CTRL pins)

アプリケーション

- Cell Phones
- PDAs
- MP3s
- Digital Still Cameras

電氣的仕様

製品	Pricing (\$/Unit)	Compliance	Status	Channels	Number of Switches	Configuration	I _{cc} Max (μA)	r _{on} Max (Ω)	V _{CC} Min (V)	V _{CC} Max (V)	Package Type
NLAS4717FCT1G	0.4	Pb-free Halide free	Active	2	2	SPDT	1	4.5	1.65	5.5	Flip-Chip-10
NLAS4717MR2G	0.5333	Pb-free Halide free	Active	2	2	SPDT	1	4.5	1.65	5.5	Micro10

詳細は、弊社 www.onsemi.jp の営業または販売代理店にお問い合わせください。

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