

## 製品概要

### AR0134CS: CMOS イメージセンサ、デジタル、グローバルシャッタ、1.2 MP、1/3インチ

技術情報は、データシートをご参照ください。

ピクセル性能の向上にフォーカスするオン・セミコンダクターの技術が、このセンサの優れた画質を優れた性能で支えています。このセンサはオン・セミコンダクター最小で、1/3 インチ光学フォーマットの高精細度 (HD) デバイスへの高速画像取り込みに向けて高性能グローバルシャッタ・テクノロジーを統合したものです。卓越した低照度性能による 3.75 ミクロンのグローバルシャッタ・ピクセルにより、ローリングシャッタ・ピクセルにつきもののアーティファクトを発生させることなく、動きを捉えることができます。

## 特長

- ON Semiconductor's 3rd Generation Global Shutter Technology
- Superior low-light performance
- HD video (720p60)
- Video/Single Frame mode
- Flexible row-skip modes
- On-chip AE and statistics engine
- Parallel and serial output
- Support for external LED or flash
- Auto black level calibration
- Context switching

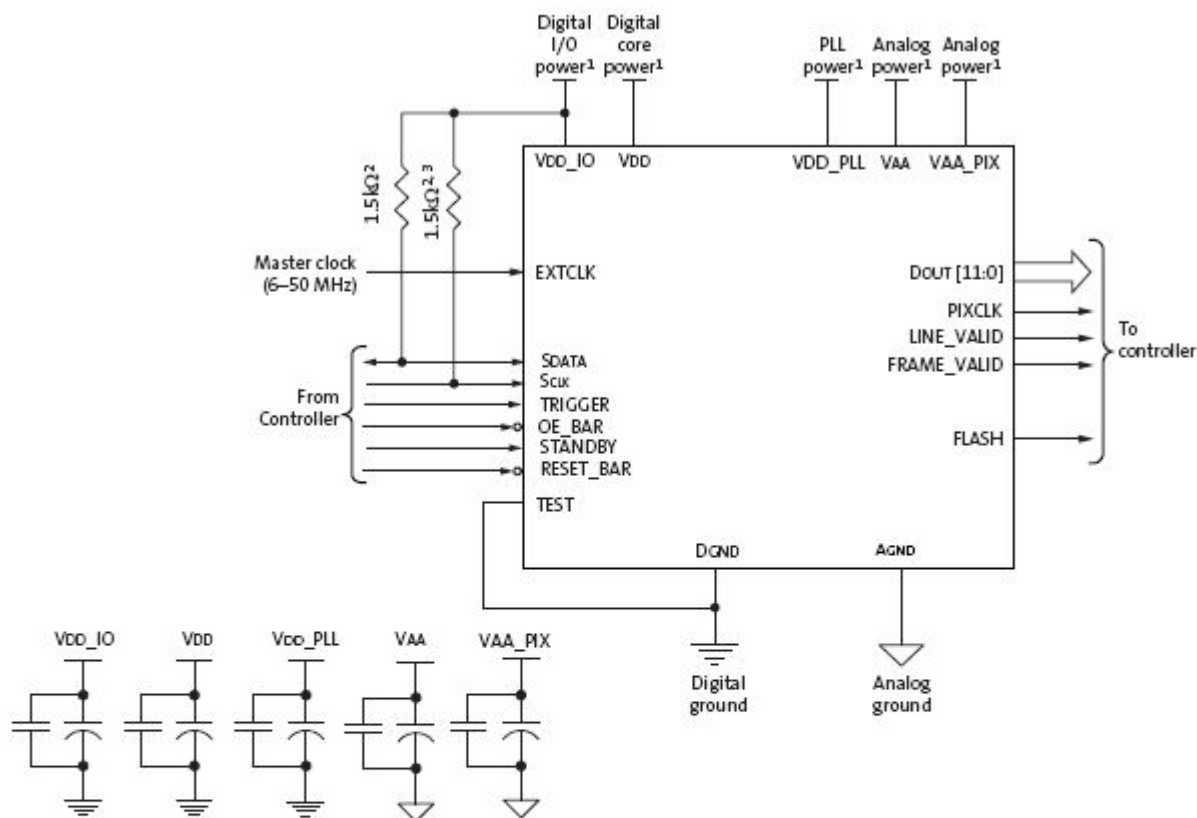
For more features, see the data sheet



| 製品                      | Pricing (\$/Unit) | Compliance                                   | Status | Type | Megapixels | Frame Rate (fps) | Optical Format | Shutter Type   | Pixel Size (µm) | Output Interface   | Color | Package Type |
|-------------------------|-------------------|----------------------------------------------|--------|------|------------|------------------|----------------|----------------|-----------------|--------------------|-------|--------------|
| AR0134C5SC00SPCA0-DPBR  |                   | Pb-free<br>Halide free<br>non AEC-Q and PPAP | Active | CMOS | 1.2        | 54               | 1/3 inch       | Global Shutter | 3.75 x 3.75     | HiSPi™<br>Parallel | RGB   | ILCC-48      |
| AR0134C5SC00SPCA0-DRBR  |                   | Pb-free<br>Halide free<br>non AEC-Q and PPAP | Active | CMOS | 1.2        | 54               | 1/3 inch       | Global Shutter | 3.75 x 3.75     | HiSPi™<br>Parallel | RGB   | ILCC-48      |
| AR0134C5SC00SPCA0-TPBR  |                   | Pb-free<br>Halide free<br>non AEC-Q and PPAP | Active | CMOS | 1.2        | 54               | 1/3 inch       | Global Shutter | 3.75 x 3.75     | HiSPi™<br>Parallel | RGB   | ILCC-48      |
| AR0134C5SC00SPCA0-TRBR  |                   | Pb-free<br>Halide free<br>non AEC-Q and PPAP | Active | CMOS | 1.2        | 54               | 1/3 inch       | Global Shutter | 3.75 x 3.75     | HiSPi™<br>Parallel | RGB   | ILCC-48      |
| AR0134C5SC00SPD20       |                   | Pb-free<br>Halide free<br>non AEC-Q and PPAP | Active | CMOS | 1.2        | 54               | 1/3 inch       | Global Shutter | 3.75 x 3.75     | HiSPi™<br>Parallel | RGB   |              |
| AR0134C5SC00SUEA0-DPBR  |                   | Pb-free<br>Halide free<br>non AEC-Q and PPAP | Active | CMOS | 1.2        | 54               | 1/3 inch       | Global Shutter | 3.75 x 3.75     | HiSPi™<br>Parallel | RGB   | IBGA-63      |
| AR0134C5SC00SUEA0-DPBR1 |                   | Pb-free<br>Halide free<br>non AEC-Q and PPAP | Active | CMOS | 1.2        | 54               | 1/3 inch       | Global Shutter | 3.75 x 3.75     | HiSPi™<br>Parallel | RGB   | IBGA-63      |
| AR0134C5SC00SUEA0-DRBR  |                   | Pb-free<br>Halide free<br>non AEC-Q and PPAP | Active | CMOS | 1.2        | 54               | 1/3 inch       | Global Shutter | 3.75 x 3.75     | HiSPi™<br>Parallel | RGB   | IBGA-63      |
| AR0134C5SC00SUEA0-TPBR  |                   | Pb-free<br>Halide free<br>non AEC-Q and PPAP | Active | CMOS | 1.2        | 54               | 1/3 inch       | Global Shutter | 3.75 x 3.75     | HiSPi™<br>Parallel | RGB   | IBGA-63      |
| AR0134C5SC00SUEA0-TRBR  |                   | Pb-free<br>Halide free<br>non AEC-Q and PPAP | Active | CMOS | 1.2        | 54               | 1/3 inch       | Global Shutter | 3.75 x 3.75     | HiSPi™<br>Parallel | RGB   | IBGA-63      |
| AR0134C5SM00SPCA0-DPBR  |                   | Pb-free<br>Halide free<br>non AEC-Q and PPAP | Active | CMOS | 1.2        | 54               | 1/3 inch       | Global Shutter | 3.75 x 3.75     | HiSPi™<br>Parallel | Mono  | ILCC-48      |
| AR0134C5SM00SPCA0-DPBR1 |                   | Pb-free<br>Halide free<br>non AEC-Q and PPAP | Active | CMOS | 1.2        | 54               | 1/3 inch       | Global Shutter | 3.75 x 3.75     | HiSPi™<br>Parallel | Mono  | ILCC-48      |
| AR0134C5SM00SPCA0-DRBR  |                   | Pb-free<br>Halide free<br>non AEC-Q and PPAP | Active | CMOS | 1.2        | 54               | 1/3 inch       | Global Shutter | 3.75 x 3.75     | HiSPi™<br>Parallel | Mono  | ILCC-48      |
| AR0134C5SM00SPCA0-TPBR  |                   | Pb-free<br>Halide free<br>non AEC-Q and PPAP | Active | CMOS | 1.2        | 54               | 1/3 inch       | Global Shutter | 3.75 x 3.75     | HiSPi™<br>Parallel | Mono  | ILCC-48      |
| AR0134C5SM00SPCA0-TRBR  |                   | Pb-free<br>Halide free<br>non AEC-Q and PPAP | Active | CMOS | 1.2        | 54               | 1/3 inch       | Global Shutter | 3.75 x 3.75     | HiSPi™<br>Parallel | Mono  | ILCC-48      |
| AR0134C5SM00SPD20       |                   | Pb-free<br>Halide free<br>non AEC-Q and PPAP | Active | CMOS | 1.2        | 54               | 1/3 inch       | Global Shutter | 3.75 x 3.75     | HiSPi™             | Mono  |              |

|                         |  |                                                 |        |      |     |    |          |                |                |                    |      |         |
|-------------------------|--|-------------------------------------------------|--------|------|-----|----|----------|----------------|----------------|--------------------|------|---------|
| AR0134CSSM00SUEA0-DPBR  |  | Pb-free<br>Halide free<br>non AEC-Q<br>and PPAP | Active | CMOS | 1.2 | 54 | 1/3 inch | Global Shutter | 3.75 x<br>3.75 | HiSPi™<br>Parallel | Mono | IBGA-63 |
| AR0134CSSM00SUEA0-DPBR1 |  | Pb-free<br>Halide free<br>non AEC-Q<br>and PPAP | Active | CMOS | 1.2 | 54 | 1/3 inch | Global Shutter | 3.75 x<br>3.75 | HiSPi™<br>Parallel | Mono | IBGA-63 |
| AR0134CSSM00SUEA0-DRBR  |  | Pb-free<br>Halide free<br>non AEC-Q<br>and PPAP | Active | CMOS | 1.2 | 54 | 1/3 inch | Global Shutter | 3.75 x<br>3.75 | HiSPi™<br>Parallel | Mono | IBGA-63 |
| AR0134CSSM00SUEA0-TPBR  |  | Pb-free<br>Halide free<br>non AEC-Q<br>and PPAP | Active | CMOS | 1.2 | 54 | 1/3 inch | Global Shutter | 3.75 x<br>3.75 | HiSPi™<br>Parallel | Mono | IBGA-63 |
| AR0134CSSM00SUEA0-TRBR  |  | Pb-free<br>Halide free<br>non AEC-Q<br>and PPAP | Active | CMOS | 1.2 | 54 | 1/3 inch | Global Shutter | 3.75 x<br>3.75 | HiSPi™<br>Parallel | Mono | IBGA-63 |
| AR0134CSSM25SPCA0-TPBR  |  | Pb-free<br>Halide free<br>non AEC-Q<br>and PPAP | Active | CMOS | 1.2 | 54 | 1/3 inch | Global Shutter | 3.75 x<br>3.75 | HiSPi™<br>Parallel | Mono | ILCC-48 |
| AR0134CSSM25SPD20       |  | Pb-free<br>Halide free<br>non AEC-Q<br>and PPAP | Active | CMOS | 1.2 | 54 | 1/3 inch | Global Shutter | 3.75 x<br>3.75 | HiSPi™<br>Parallel | Mono |         |
| AR0134CSSM25SUEA0-DPBR  |  | Pb-free<br>Halide free<br>non AEC-Q<br>and PPAP | Active | CMOS | 1.2 | 54 | 1/3 inch | Global Shutter | 3.75 x<br>3.75 | HiSPi™<br>Parallel | Mono | IBGA-63 |
| AR0134CSSM25SUEA0-DRBR  |  | Pb-free<br>Halide free<br>non AEC-Q<br>and PPAP | Active | CMOS | 1.2 | 54 | 1/3 inch | Global Shutter | 3.75 x<br>3.75 | HiSPi™<br>Parallel | Mono | IBGA-63 |
| AR0134CSSM25SUEA0-DRBR1 |  | Pb-free<br>Halide free<br>non AEC-Q<br>and PPAP | Active | CMOS | 1.2 | 54 | 1/3 inch | Global Shutter | 3.75 x<br>3.75 | HiSPi™<br>Parallel | Mono | IBGA-63 |
| AR0134CSSM25SUEA0-TPBR  |  | Pb-free<br>Halide free<br>non AEC-Q<br>and PPAP | Active | CMOS | 1.2 | 54 | 1/3 inch | Global Shutter | 3.75 x<br>3.75 | HiSPi™<br>Parallel | Mono | IBGA-63 |
| AR0134CSSM25SUEA0-TRBR  |  | Pb-free<br>Halide free<br>non AEC-Q<br>and PPAP | Active | CMOS | 1.2 | 54 | 1/3 inch | Global Shutter | 3.75 x<br>3.75 | HiSPi™<br>Parallel | Mono | IBGA-63 |

## アプリケーション・ダイアグラム



- Notes:
1. All power supplies must be adequately decoupled.
  2. ON Semiconductor recommends a resistor value of 1.5kΩ, but it may be greater for slower two-wire speed.
  3. This pull-up resistor is not required if the controller drives a valid logic level on SCLK at all times.
  4. The serial interface output pads can be left unconnected if the parallel output interface is used.
  5. ON Semiconductor recommends that 0.1μF and 10μF decoupling capacitors for each power supply are mounted as close as possible to the pad. Actual values and results may vary depending on layout and design considerations. Refer to the ARO134 demo headboard schematics for circuit recommendations.
  6. ON Semiconductor recommends that analog power planes be placed in a manner such that coupling with the digital power planes is minimized.

詳細は、弊社 [www.onsemi.jp](http://www.onsemi.jp) の営業または販売代理店にお問い合わせください。

1/27/2021 作成