

## AR0134CS



































### CMOS 1.2 MP1/3

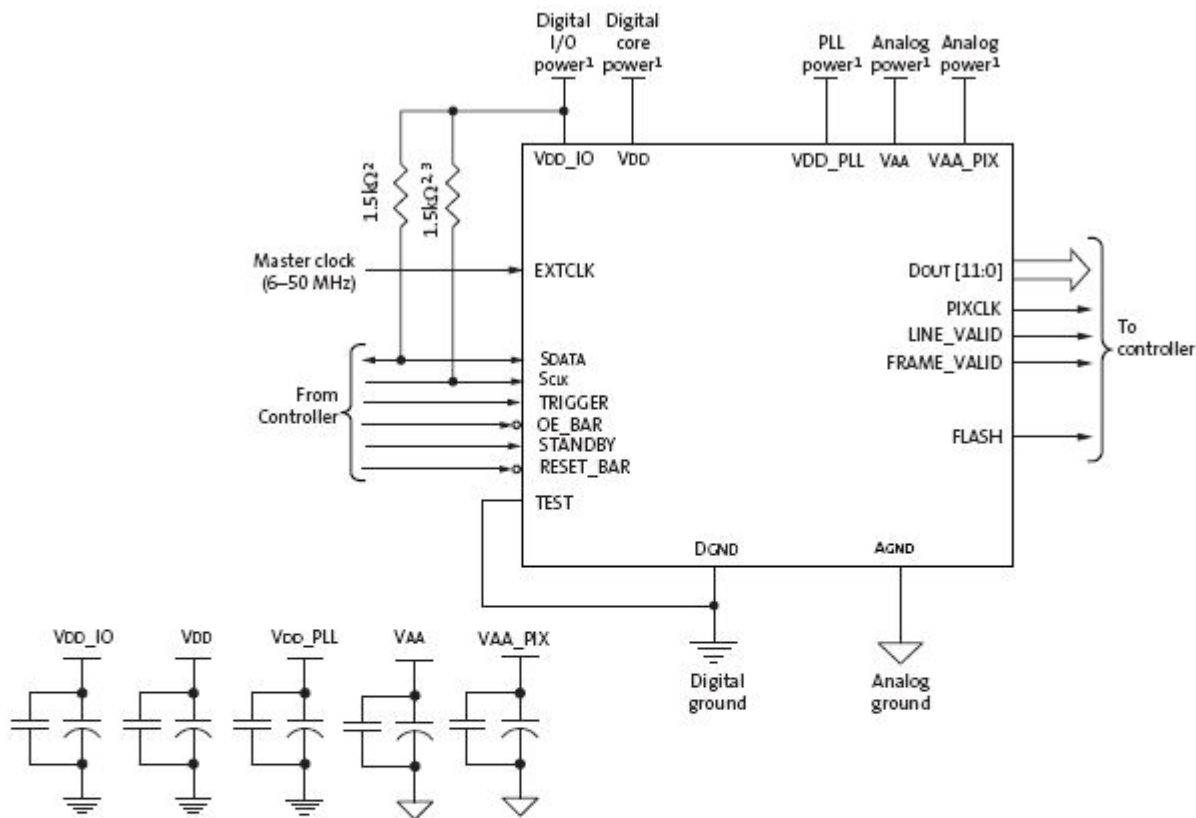
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1/3 (HD) 3.75

- ON Semiconductor's 3rd Generation Global Shutter Technology
- Superior low-light performance
- HD video (720p60)
- Video/Single Frame mode
- Flexible row-skip modes
- On-chip AE and statistics engine
- Parallel and serial output
- Support for external LED or flash
- Auto black level calibration
- Context switching

For more features, see the data sheet

	Pricing (\$/Unit)	Compliance	Status	Type	Megapixels	Frame Rate (fps)	Optical Format	Shutter Type	Pixel Size (µm)	Output Interface	Color	Package Type
AR0134CSSC00 SPCA0-DPBR		 	Active	CMOS	1.2	54	1/3 inch	Global Shutter	3.75 x 3.75		RGB	ILCC-48
AR0134CSSC00 SPCA0-DRBR		 	Active	CMOS	1.2	54	1/3 inch	Global Shutter	3.75 x 3.75		RGB	ILCC-48
AR0134CSSC00 SPCA0-TPBR		 	Active	CMOS	1.2	54	1/3 inch	Global Shutter	3.75 x 3.75		RGB	ILCC-48
AR0134CSSC00 SUEA0-DPBR		 	Active	CMOS	1.2	54	1/3 inch	Global Shutter	3.75 x 3.75		RGB	IBGA-63
AR0134CSSC00 SUEA0-DPBR1		 	Active	CMOS	1.2	54	1/3 inch	Global Shutter	3.75 x 3.75		RGB	IBGA-63
AR0134CSSC00 SUEA0-DRBR		 	Active	CMOS	1.2	54	1/3 inch	Global Shutter	3.75 x 3.75		RGB	IBGA-63
AR0134CSSC00 SUEA0-TPBR		 	Active	CMOS	1.2	54	1/3 inch	Global Shutter	3.75 x 3.75		RGB	IBGA-63
AR0134CSSM00 SPCA0-DPBR		 	Active	CMOS	1.2	54	1/3 inch	Global Shutter	3.75 x 3.75		Mono	ILCC-48
AR0134CSSM00 SPCA0-DPBR1		 	Active	CMOS	1.2	54	1/3 inch	Global Shutter	3.75 x 3.75		Mono	ILCC-48
AR0134CSSM00 SPCA0-DRBR		 	Active	CMOS	1.2	54	1/3 inch	Global Shutter	3.75 x 3.75		Mono	ILCC-48
AR0134CSSM00 SUEA0-DPBR		 	Active	CMOS	1.2	54	1/3 inch	Global Shutter	3.75 x 3.75		Mono	IBGA-63
AR0134CSSM00 SUEA0-DPBR1		 	Active	CMOS	1.2	54	1/3 inch	Global Shutter	3.75 x 3.75		Mono	IBGA-63
AR0134CSSM00 SUEA0-DRBR		 	Active	CMOS	1.2	54	1/3 inch	Global Shutter	3.75 x 3.75		Mono	IBGA-63
AR0134CSSM00 SUEA0-TPBR		 	Active	CMOS	1.2	54	1/3 inch	Global Shutter	3.75 x 3.75		Mono	IBGA-63
AR0134CSSM25 SUEA0-DPBR		 	Active	CMOS	1.2	54	1/3 inch	Global Shutter	3.75 x 3.75		Mono	IBGA-63
AR0134CSSM25 SUEA0-DRBR		 	Active	CMOS	1.2	54	1/3 inch	Global Shutter	3.75 x 3.75		Mono	IBGA-63
AR0134CSSM25 SUEA0-DRBR1		 	Active	CMOS	1.2	54	1/3 inch	Global Shutter	3.75 x 3.75		Mono	IBGA-63



- Notes:
1. All power supplies must be adequately decoupled.
  2. ON Semiconductor recommends a resistor value of 1.5k $\Omega$ , but it may be greater for slower two-wire speed.
  3. This pull-up resistor is not required if the controller drives a valid logic level on SCLK at all times.
  4. The serial interface output pads can be left unconnected if the parallel output interface is used.
  5. ON Semiconductor recommends that 0.1 $\mu$ F and 10 $\mu$ F decoupling capacitors for each power supply are mounted as close as possible to the pad. Actual values and results may vary depending on layout and design considerations. Refer to the AR0134 demo headboard schematics for circuit recommendations.
  6. ON Semiconductor recommends that analog power planes be placed in a manner such that coupling with the digital power planes is minimized.